
MODULE *t2pc*

EXTENDS *Integers, Sequences, FiniteSets, TLC*

CONSTANTS *RM*, The number of resource managers.
 BTM, Whether have *backupTM*.
 RMMAYFAIL, Whether *RM* could fail.
 TMMAYFAIL Whether *TM* could fail.

```

--algorithm TransactionCommit{
  variable rmState = [rm ∈ RM ↦ "working"],
           tmState = "init" ;           tmState's init state.
           btmState = "init" ;         backupTM's init state.

  define {
    canCommit ≜ ∀ rm ∈ RM : rmState[rm] ∈ { "prepared", "committed" }
    canAbort ≜ ∀ rm ∈ RM : rmState[rm] ≠ "committed"
  }
  If some rm are committed or all rm are
  which means tmState is "commit", sow

  If no rm are committed, we don't know the state of tmState
  if tmState is not "commit", we cannot commit.

  }

  macro Prepare( p ) {
    await rmState[p] = "working" ;    if rmState[p] is working, then it will be prepared
    rmState[p] := "prepared" ;
  }

  macro Decide( p ) {
    either { await rmState[p] = "prepared" ∧ canCommit ∧ (tmState = "commit" ∨ btmState = "commit") ;

           rmState[p] := "committed" ;
         }
    or    { await rmState[p] ∈ { "working", "prepared" } ∧ canAbort ;
           rmState[p] := "abort"
         }
  }

  macro Fail( p ) {
    if ( RMMAYFAIL ) rmState[p] := "crash"
  }

  fair process ( RManager ∈ RM ) {
    RS: while ( rmState[self] ∈ { "working", "prepared" } ) {
      either Prepare(self) or Decide(self) or Fail(self)
    }
  }

  fair process ( TManager = 0 ) {
    TS: either { await canCommit ;

```

If *RMMAYFAIL*, *rmState*[*p*] could b

If *rmState* is working or prepared
set up *backupTM*. Otherwise ter

If all *rm* are prepared, it's ti

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TC: tmState := "commit" ;
    if ( BTM ) btmState := "commit" ;
F1: if ( TMMAYFAIL ) tmState := "hidden" ; }

or { await canAbort ;
    TA: tmState := "abort" ;
        if ( BTM ) btmState := "abort" ;
    F2: if ( TMMAYFAIL ) tmState := "hidden" ; }
}

```

If we set *backupTM*, change

Abort can appear any time u

If we set *backupTM*, change

BEGIN TRANSLATION

VARIABLES *rmState*, *tmState*, *btmState*, *pc*

define statement

$canCommit \triangleq \forall rm \in RM : rmState[rm] \in \{ \text{"prepared"}, \text{"committed"} \}$

$canAbort \triangleq \forall rm \in RM : rmState[rm] \neq \text{"committed"}$

$vars \triangleq \langle rmState, tmState, btmState, pc \rangle$

$ProcSet \triangleq (RM) \cup \{0\}$

$Init \triangleq$ Global variables

$\wedge rmState = [rm \in RM \mapsto \text{"working"}]$

$\wedge tmState = \text{"init"}$

$\wedge btmState = \text{"init"}$

$\wedge pc = [self \in ProcSet \mapsto \text{CASE } self \in RM \rightarrow \text{"RS"} \\ \square \quad self = 0 \rightarrow \text{"TS"}]$

$RS(self) \triangleq \wedge pc[self] = \text{"RS"}$

$\wedge \text{IF } rmState[self] \in \{ \text{"working"}, \text{"prepared"} \}$

THEN $\wedge \vee \wedge rmState[self] = \text{"working"}$

$\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"prepared"}]$

$\vee \wedge \vee \wedge rmState[self] = \text{"prepared"} \wedge canCommit \wedge (tmState = \text{"commit"} \vee btmState = \text{"commit"})$

$\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"committed"}]$

$\vee \wedge rmState[self] \in \{ \text{"working"}, \text{"prepared"} \} \wedge canAbort$

$\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"abort"}]$

$\vee \wedge \text{IF } RMMAYFAIL$

THEN $\wedge rmState' = [rmState \text{ EXCEPT } ![self] = \text{"crash"}]$

ELSE $\wedge \text{TRUE}$

$\wedge \text{UNCHANGED } rmState$

$\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"RS"}]$

ELSE $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"Done"}]$

$\wedge \text{UNCHANGED } rmState$

$\wedge \text{UNCHANGED } \langle tmState, btmState \rangle$

$RManager(self) \triangleq RS(self)$

$$\begin{aligned}
TS &\triangleq \wedge pc[0] = \text{"TS"} \\
&\quad \wedge \vee \wedge canCommit \\
&\quad \quad \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TC"}] \\
&\quad \quad \vee \wedge canAbort \\
&\quad \quad \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"TA"}] \\
&\quad \wedge \text{UNCHANGED } \langle rmState, tmState, btmState \rangle \\
TC &\triangleq \wedge pc[0] = \text{"TC"} \\
&\quad \wedge tmState' = \text{"commit"} \\
&\quad \wedge \text{IF } BTM \\
&\quad \quad \text{THEN } \wedge btmState' = \text{"commit"} \\
&\quad \quad \text{ELSE } \wedge \text{TRUE} \\
&\quad \quad \quad \wedge \text{UNCHANGED } btmState \\
&\quad \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F1"}] \\
&\quad \wedge \text{UNCHANGED } rmState \\
F1 &\triangleq \wedge pc[0] = \text{"F1"} \\
&\quad \wedge \text{IF } TMMAYFAIL \\
&\quad \quad \text{THEN } \wedge tmState' = \text{"hidden"} \\
&\quad \quad \text{ELSE } \wedge \text{TRUE} \\
&\quad \quad \quad \wedge \text{UNCHANGED } tmState \\
&\quad \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}] \\
&\quad \wedge \text{UNCHANGED } \langle rmState, btmState \rangle \\
TA &\triangleq \wedge pc[0] = \text{"TA"} \\
&\quad \wedge tmState' = \text{"abort"} \\
&\quad \wedge \text{IF } BTM \\
&\quad \quad \text{THEN } \wedge btmState' = \text{"abort"} \\
&\quad \quad \text{ELSE } \wedge \text{TRUE} \\
&\quad \quad \quad \wedge \text{UNCHANGED } btmState \\
&\quad \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"F2"}] \\
&\quad \wedge \text{UNCHANGED } rmState \\
F2 &\triangleq \wedge pc[0] = \text{"F2"} \\
&\quad \wedge \text{IF } TMMAYFAIL \\
&\quad \quad \text{THEN } \wedge tmState' = \text{"hidden"} \\
&\quad \quad \text{ELSE } \wedge \text{TRUE} \\
&\quad \quad \quad \wedge \text{UNCHANGED } tmState \\
&\quad \wedge pc' = [pc \text{ EXCEPT } ![0] = \text{"Done"}] \\
&\quad \wedge \text{UNCHANGED } \langle rmState, btmState \rangle \\
TManager &\triangleq TS \vee TC \vee F1 \vee TA \vee F2 \\
Next &\triangleq TManager \\
&\quad \vee (\exists self \in RM : RManager(self)) \\
&\quad \vee \text{Disjunct to prevent deadlock on termination} \\
&\quad ((\forall self \in ProcSet : pc[self] = \text{"Done"}) \wedge \text{UNCHANGED } vars)
\end{aligned}$$

$$\begin{aligned}
Spec &\triangleq \wedge Init \wedge \square[Next]_{vars} \\
&\wedge \forall self \in RM : WF_{vars}(RManager(self)) \\
&\wedge WF_{vars}(TManager)
\end{aligned}$$

$$Termination \triangleq \diamond(\forall self \in ProcSet : pc[self] = \text{"Done"})$$

END TRANSLATION

$$\begin{aligned}
consistency &\triangleq tmState = \text{"commit"} \Rightarrow \forall i \in RM : rmState[i] \neq \text{"abort"} \\
&\wedge tmState = \text{"abort"} \Rightarrow \forall j \in RM : rmState[j] \neq \text{"committed"} \\
&\wedge tmState = \text{"hidden"} \Rightarrow \forall k \in RM : rmState[k] \neq \text{"committed"} \\
terminate &\triangleq \diamond(\forall i \in RM : rmState[i] \in \{\text{"committed"}, \text{"abort"}, \text{"crash"}\})
\end{aligned}$$

\ *1.2 *TMMAYFAIL* is true and *RMMAYFAIL* is false means *tmState* could be "hidden" and *rmState* cannot be

\ *hidden. In this situation, termination will be violated. For example, when *TM* is "commit" and some

\ **RM* are committed, then *TM* crashed while some other *RM* is prepared, but they can never be "commit" or *abort*

\ *because *TM* is "hidden" now. That's why we get result when *RM* equals 3 that <"committed", "prepared", "committed">.

\ *It will never be terminated because "prepared" has no way to "commit".

\ *1.3 *Termination* and consistency remain true. The states cancommit and canabort is owned by both *BTM* and *TM*.

\ * So when *TM* crashes, the *RMs* can still consult the *BTM* and make their decision.

\ *If an *RM* crashed, then all other *RMs* can only abort. So all other uncrashed *RMs* remain consistent.

\ * Modification History

\ * Last modified Tue Dec 05 19:55:47 EST 2017 by lenovo

\ * Created Wed Nov 29 17:13:20 EST 2017 by lenovo

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