

EXTENDS *Integers, Sequences, FiniteSets, TLC*
 CONSTANTS $N, FAILNUM$
 ASSUME $N \leq 5 \wedge 0 \leq FAILNUM \wedge FAILNUM \leq 4$
 $Nodes \triangleq 1 \dots N$

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--algorithm syncCon1
{ variables FailNum = FAILNUM ;
  up = [n ∈ Nodes ↦ TRUE] ;
  pt = [n ∈ Nodes ↦ 0] ;
  t = [n ∈ Nodes ↦ FALSE] ;
  d = [n ∈ Nodes ↦ -1] ;
  mb = [n ∈ Nodes ↦ {}] ;

  define {
    SetMin(S)  $\triangleq$  CHOOSE  $i \in S : \forall j \in S : i \leq j$ 
  }

  macro MaybeFail( ) {
    if ( FailNum > 0  $\wedge$  up[self] )
    { either
      { up[self] := FALSE ; FailNum := FailNum - 1 ; }
      or skip ; } ;
  }

  fair process ( n ∈ Nodes )
  variable v = 0, Q = {} ;
  {
    P: if ( up[self] ) {
      v := self ;
      Q := Nodes ;
    }
    PS: while ( up[self]  $\wedge$  Q  $\neq$  {} ) {
      with ( p ∈ Q ) {
        MaybeFail() ; In process n, each time we add v to mb[p], this process might be fail, and once the process
fail, we set up[self] fail and after that, all operations will be invalid.
        if ( up[self] ) mb[p] := mb[p]  $\cup$  {v} ; In process n, add v to mb[1] to mb[N]
        Q := Q  $\setminus$  {p} ; For each element p in Q, we have to broadcast value v to mb[p], after adding v to mb[p],
remove p from Q in case of adding v to mb[p] again.
      } ;
    } ;
    if ( up[self] ) pt[self] := pt[self] + 1 ;
    PR: await ( up[self]  $\wedge$  ( $\forall i \in Nodes : pt[i] = pt[self]$ ) ) ; If no operations are failed and all process enter in round 1
    d[self] := SetMin(mb[self]) ; Set the minimum of mb[p] to d[self], if success, all d[p] will be the same.
    t[self] := TRUE ; The process is terminated
  }
}

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    } ;
  }
}

BEGIN TRANSLATION
VARIABLES FailNum, up, pt, t, d, mb, pc

define statement
SetMin(S)  $\triangleq$  CHOOSE  $i \in S : \forall j \in S : i \leq j$ 

VARIABLES v, Q

vars  $\triangleq$   $\langle \textit{FailNum}, \textit{up}, \textit{pt}, \textit{t}, \textit{d}, \textit{mb}, \textit{pc}, \textit{v}, \textit{Q} \rangle$ 

ProcSet  $\triangleq$  (Nodes)

Init  $\triangleq$  Global variables
 $\wedge \textit{FailNum} = \textit{FAILNUM}$ 
 $\wedge \textit{up} = [n \in \textit{Nodes} \mapsto \text{TRUE}]$ 
 $\wedge \textit{pt} = [n \in \textit{Nodes} \mapsto 0]$ 
 $\wedge \textit{t} = [n \in \textit{Nodes} \mapsto \text{FALSE}]$ 
 $\wedge \textit{d} = [n \in \textit{Nodes} \mapsto -1]$ 
 $\wedge \textit{mb} = [n \in \textit{Nodes} \mapsto \{\}]$ 
Process n
 $\wedge \textit{v} = [\textit{self} \in \textit{Nodes} \mapsto 0]$ 
 $\wedge \textit{Q} = [\textit{self} \in \textit{Nodes} \mapsto \{\}]$ 
 $\wedge \textit{pc} = [\textit{self} \in \textit{ProcSet} \mapsto \text{"P"}]$ 

P(self)  $\triangleq$   $\wedge \textit{pc}[\textit{self}] = \text{"P"}$ 
 $\wedge \text{IF } \textit{up}[\textit{self}]$ 
  THEN  $\wedge \textit{v}' = [\textit{v} \text{ EXCEPT } ![\textit{self}] = \textit{self}]$ 
 $\wedge \textit{Q}' = [\textit{Q} \text{ EXCEPT } ![\textit{self}] = \textit{Nodes}]$ 
 $\wedge \textit{pc}' = [\textit{pc} \text{ EXCEPT } ![\textit{self}] = \text{"PS"}]$ 
  ELSE  $\wedge \textit{pc}' = [\textit{pc} \text{ EXCEPT } ![\textit{self}] = \text{"Done"}]$ 
 $\wedge \text{UNCHANGED } \langle \textit{v}, \textit{Q} \rangle$ 
 $\wedge \text{UNCHANGED } \langle \textit{FailNum}, \textit{up}, \textit{pt}, \textit{t}, \textit{d}, \textit{mb} \rangle$ 

PS(self)  $\triangleq$   $\wedge \textit{pc}[\textit{self}] = \text{"PS"}$ 
 $\wedge \text{IF } \textit{up}[\textit{self}] \wedge \textit{Q}[\textit{self}] \neq \{\}$ 
  THEN  $\wedge \exists p \in \textit{Q}[\textit{self}] :$ 
 $\wedge \text{IF } \textit{FailNum} > 0 \wedge \textit{up}[\textit{self}]$ 
  THEN  $\wedge \vee \wedge \textit{up}' = [\textit{up} \text{ EXCEPT } ![\textit{self}] = \text{FALSE}]$ 
 $\wedge \textit{FailNum}' = \textit{FailNum} - 1$ 
 $\vee \wedge \text{TRUE}$ 
 $\wedge \text{UNCHANGED } \langle \textit{FailNum}, \textit{up} \rangle$ 
  ELSE  $\wedge \text{TRUE}$ 
 $\wedge \text{UNCHANGED } \langle \textit{FailNum}, \textit{up} \rangle$ 
 $\wedge \text{IF } \textit{up}'[\textit{self}]$ 

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        THEN  $\wedge mb' = [mb \text{ EXCEPT } ![p] = mb[p] \cup \{v[self]\}]$ 
        ELSE  $\wedge \text{TRUE}$ 
             $\wedge mb' = mb$ 
             $\wedge Q' = [Q \text{ EXCEPT } ![self] = Q[self] \setminus \{p\}]$ 
             $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"PS"}]$ 
             $\wedge pt' = pt$ 
    ELSE  $\wedge \text{IF } up[self]$ 
        THEN  $\wedge pt' = [pt \text{ EXCEPT } ![self] = pt[self] + 1]$ 
        ELSE  $\wedge \text{TRUE}$ 
             $\wedge pt' = pt$ 
             $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"PR"}]$ 
             $\wedge \text{UNCHANGED } \langle FailNum, up, mb, Q \rangle$ 
 $\wedge \text{UNCHANGED } \langle t, d, v \rangle$ 

PR(self)  $\triangleq$   $\wedge pc[self] = \text{"PR"}$ 
 $\wedge (up[self] \wedge (\forall i \in Nodes : pt[i] = pt[self]))$ 
 $\wedge d' = [d \text{ EXCEPT } ![self] = SetMin(mb[self])]$ 
 $\wedge t' = [t \text{ EXCEPT } ![self] = \text{TRUE}]$ 
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"Done"}]$ 
 $\wedge \text{UNCHANGED } \langle FailNum, up, pt, mb, v, Q \rangle$ 

n(self)  $\triangleq$   $P(self) \vee PS(self) \vee PR(self)$ 

Next  $\triangleq$   $(\exists self \in Nodes : n(self))$ 
 $\vee$  Disjunct to prevent deadlock on termination
 $((\forall self \in ProcSet : pc[self] = \text{"Done"}) \wedge \text{UNCHANGED } vars)$ 

Spec  $\triangleq$   $\wedge Init \wedge \square [Next]_{vars}$ 
 $\wedge \forall self \in Nodes : WF_{vars}(n(self))$ 

Termination  $\triangleq$   $\diamond (\forall self \in ProcSet : pc[self] = \text{"Done"})$ 

END TRANSLATION

agreement  $\triangleq$   $(\forall i, j \in Nodes : t[i] \wedge t[j] \Rightarrow d[i] = d[j])$ 

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\ *When no crash happened, the program will excute without any problems and all d[i]
\ *will be the same. However, if some nodes crash, the crashed process will be in round 0
\ *and other uncrashed processes will be in round 1, so other uncrashed processes will
\ *not execute SetMin(S), so all d[i] will be - 1. Only if all processes are not crashed can
\ *we get minimum value.

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\ * Modification History
\ * Last modified Tue Oct 24 21:46:27 EDT 2017 by lenovo
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