

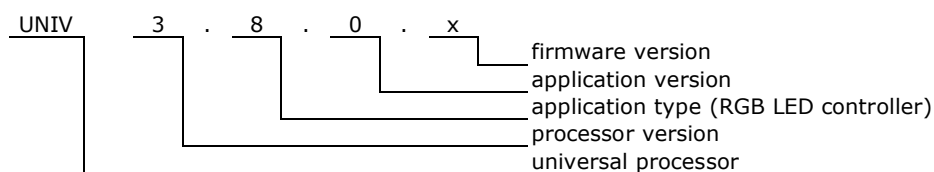


1. Features

- 3 channel LED controller
- Maximum load 120VA for each channel
- PWM (Pulse Width Modulation)
- Controller can generate any colour with required intensity of common anode RGB LED.
- Can work with:
 -  - RGB LEDs or three single colour LEDs (eg. LED strips, tapes, tubes etc.)
 -  - low voltage bulbs (classical and halogen 12-24V)
- Maximum LED supply voltage 12-24V
- Maximum LED current 5A for each channel
- Operation voltage 10-24V.
- Maximum current consumption from the bus 26mA.
- For DIN rail mounting.
- Dimensions 90x58x36 mm (2 mod)
- Operating of module depends on firmware uploaded into it.
- Schematic and PCB design can be downloaded from hapcan.com site



2. Application version



3. Technical data

Bus side

Parameter	Symbol	Value	Unit
Power supply voltage	U_S	10-24V	V
Maximum current consumption with all channels turn on	$I_{S\text{MAX}}$	26	mA
Bus quiescent current	I_{SQ}	7	mA
Bus connector type	2x RJ45 connectors		

RGB LED side

Parameter	Symbol	Value	Unit
Power supply voltage	U_N	12-24V	V
Maximum LED current for one channel	I_N	5	A
Load Power for one channel	S_N	120 @24V	VA
Voltage regulating range	U_ϕ	0 - U_N	V
PWM frequency	f	488	Hz
Connector type	Terminal block 4mm ²		

4. Hardware

4.1. Schematic

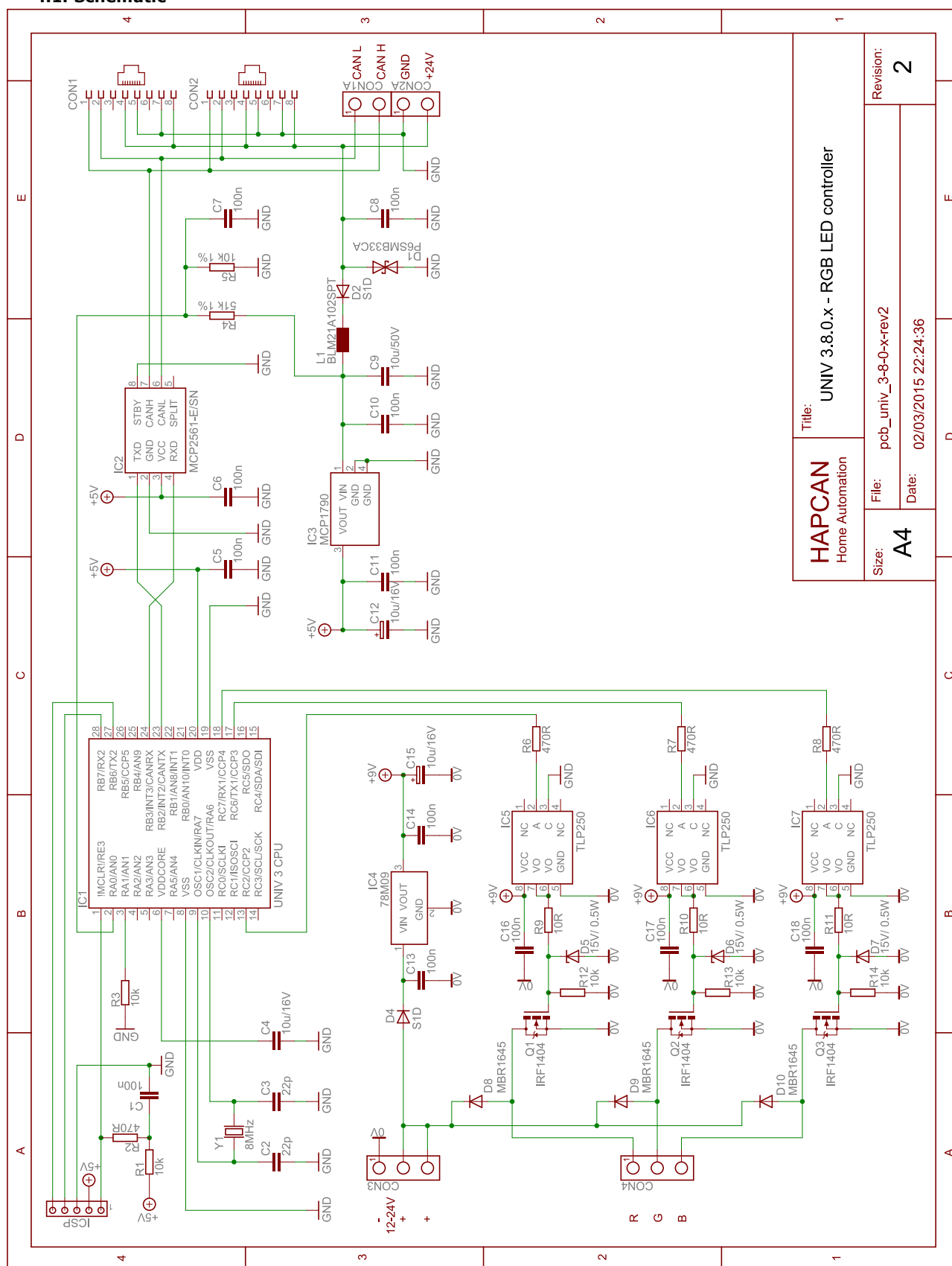


Figure 1. Schematic of UNIV 3.8.0.x module

4.2. Wiring

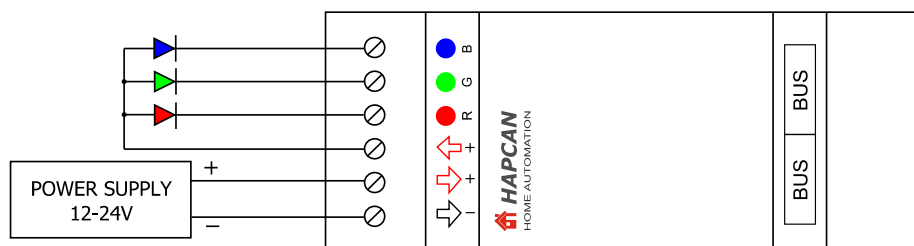
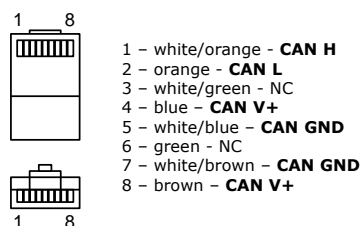


Figure 2. Wiring diagram

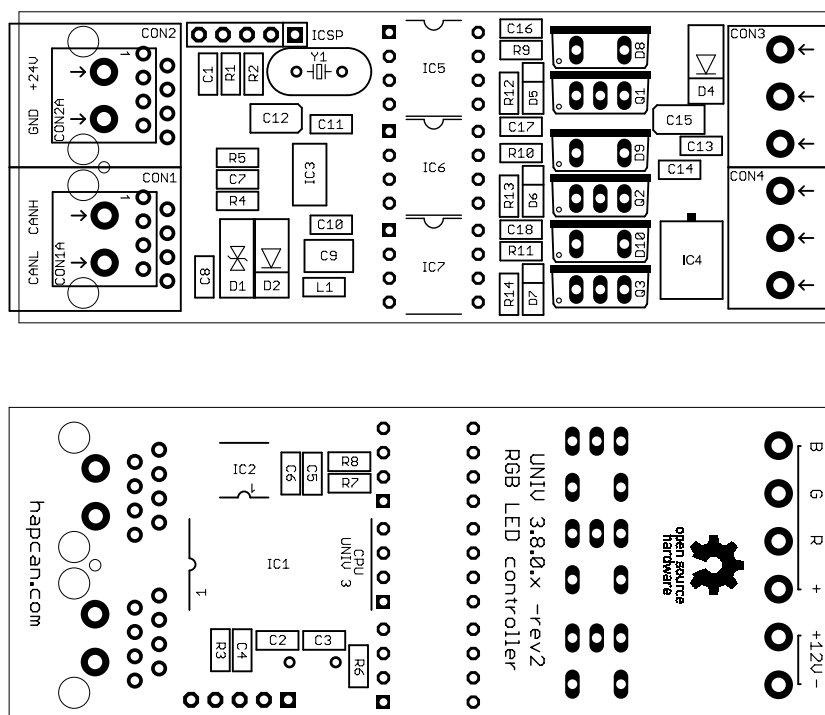


Note that if module is first or last on the bus, the terminator (resistor 120 Ohm) must be plugged into one of BUS ports.

Figure 3. RJ45 bus connector wiring.

4.3. Assembly schematic

- Printed circuit boards *PCB UNIV 3.8.0.x* for UNIV 3.8.0.x module
- PCBs dimensions: 86.5mm x 33mm

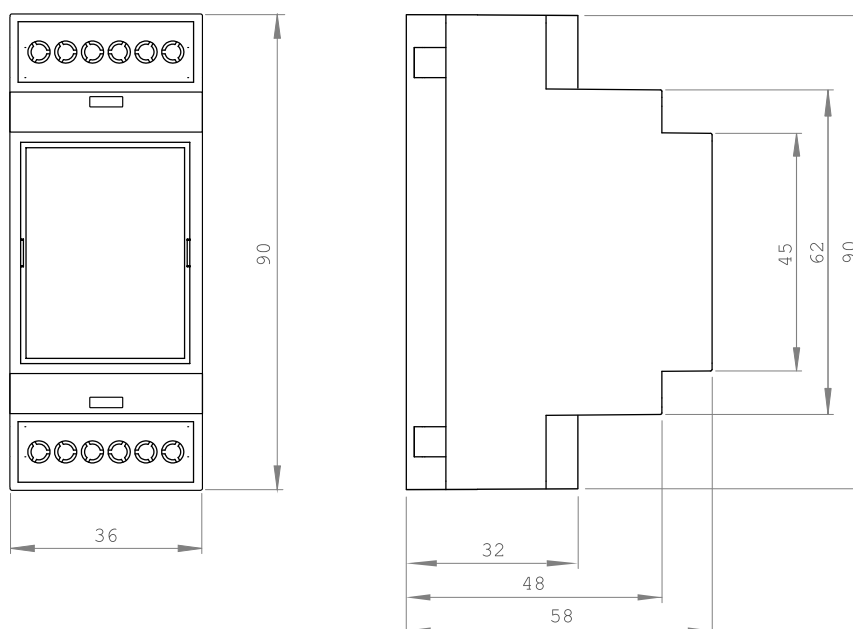


4.4. Components

Designator	Type	Footprint	Description
C1, C5, C6, C7, C8, C10, C11, C13, C14, C16, C17, C18	100nF/50V ±10%	0805	Capacitor
C2, C3	22pF/50V ±10%	0805	Capacitor
C4	10uF/16V ±10%	0805	Capacitor
C9	10uF/50V ±10%	1206, 1210	Capacitor
C12, C15	10uF/16V ±10%	SMA, SMB	Tantalum capacitor
R1, R3, R12, R13, R14	10k	0805	Resistor
R2, R6, R7, R8	470 Ohm	0805	Resistor
R4	51k 1%	0805	Resistor
R5	10k 1%	0805	Resistor
R9, R10, R11	10 Ohm	0805	Resistor
L1	BLM21A102SPT	0805	Murata choke
Y1	8MHz	HC49-S	Quartz crystal
D1	P6SMB33CA	DO-214	Transil diode
D2, D4	100V 1A	DO-214	Rectifying diode
D5, D6, D7	15V/ 0.5W	MINIMELF	Zener diode
D8, D9, D10	MBR1645	TO-220	Rectifying diode
IC1	UNIV 3 CPU	SOIC-28	HAPCAN universal processor
IC2	MCP2561-E/SN	SOIC-8	Microchip CAN transceiver
IC3	MCP1790-5002EDB	SOT-223	Microchip Voltage regulator
IC4	78M09	D-PAK	Voltage regulator
IC5, IC6, IC7	TLP250	DIP-8	MOSFET driver
Q1, Q2, Q3	IRF1404	TO-220	MOSFET transistor
CON1, CON2	95501-2881	L18xW15xH11	Molex RJ45 connector
CON3, CON4	AK700/3-5.0-V-GREEN-BR	L15xW10.5xH19 raster=5mm	PTR Messtechnik Terminal block

4.5. Enclosure

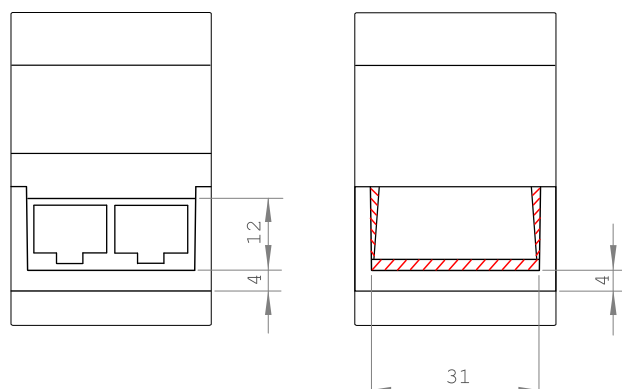
- Gainta D2MG enclosure (2 modules wide)
- Dimensions: 90mm x 58mm x 36mm



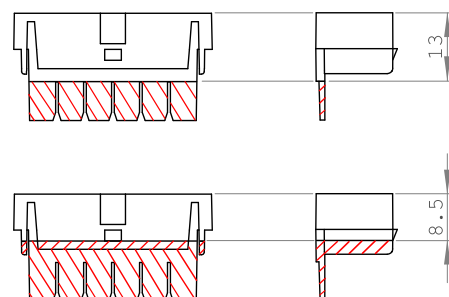
4.7. Mechanical processing

Striped parts must be removed.

BODY

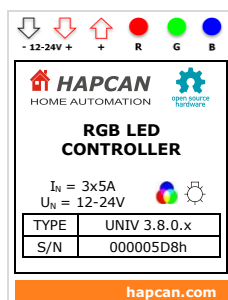


TERMINAL GUARDS



4.8. Labels

Editable labels version is available on hapcan.com site.



5. Commissioning

5.1. CPU voltage measurement

After verifying the correctness and quality of the soldering, the bus voltage should be connected while measuring the processor voltage. To do this, connect a voltmeter to pins 2 and 3 of the ICSP connector. Processor supply voltage should be about 5V.

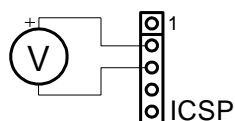


Figure 3. CPU voltage measurement

5.2. Checking the CPU clock

Proper operation of the CPU can be checked by temporarily connecting the LED to pins 3 and 5 of the ICSP connector. When device is powered, the LED should light up four times in the sequence 1 second on - 1 second off - 1 second on. The LED lights up only once for 50ms, if the processor is in programming mode.

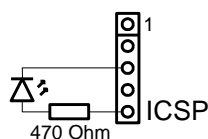


Figure 4. Checking the CPU clock

5.3. Firmware uploading

The device requires a firmware uploading for proper operation. It can be done with HAPCAN Programmer software. Both, firmware and HAPCAN Programmer can be downloaded from hapcan.com website.

6. License



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7. Document version

File	Description	Date
univ_3-8-0-x_a.pdf	Original version	January 2014
univ_3-8-0-x_b.pdf	Enclosure changed	February 2014
univ_3-8-0-x_c.pdf	Hardware revision 2	February 2015