

Krutarth Patel

Research Assistant

Philadelphia, PA - Email me on Indeed: [indeed.com/r/Krutarth-Patel/ff4b011e33273528](https://www.indeed.com/r/Krutarth-Patel/ff4b011e33273528)

WORK EXPERIENCE

Electrical Engineer

Advanced Visual Sciences - Philadelphia, PA - July 2012 to Present

- o Working on troubleshooting of the analog circuit of a tonometer used to measure eye pressure.

- o Converting the analog system to digital which includes embedded programming

Research Assistant

University of Pennsylvania Physics Department and CERN - August 2011 to January 2012

- o Worked towards the front-end ASIC to be used in the binary readout architecture of silicon strip detectors in the ATLAS Semiconductor Tracker Upgrade for the ATLAS detector at CERN, Geneva.

- o Designed various modules of the strip detectors using Verilog Programming.

Technical Intern

Technogravity Solutions - Mumbai, Maharashtra - June 2009 to August 2009

- o Worked for the R&D division on projects that included designing and building application specific robots.

TECHNICAL SKILLS

Languages: Verilog, VHDL, C, C++, Assembly Level languages for x85, x86 and x51 architectures, PYTHON, JAVA, HTML, Voice XML, R

Software Tools: MS OFFICE suite, MATLAB, Simulink, NCVerilog, OrCAD, EAGLE, Keil, LINDO, GAMS

Computer Skills: Microsoft (Windows 2000, XP, Vista, 7), Linux, Macintosh

PROJECTS

University of Pennsylvania, Philadelphia

EDUCATION

Master of Science in Engineering

University of Pennsylvania, School of Engineering and Applied Science - Philadelphia, PA

May 2012

Bachelor of Engineering in Electronics Engineering

Mumbai University - Mumbai, Maharashtra

May 2010