

# HARIHARAN AYAPPANE

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## EDUCATION

<b>National Institute of Technology- Karnataka , Surathkal</b> Bachelor of Technology Electrical and Electronics Engineering 2023 Major in Electrical and Electronics Engineering CGPA: 8.91 Minor in Computer Science and Engineering, CGPA 9.00	July 2019 - Apr
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## EXPERIENCE

<b>Texas Instruments India (TIH), Digital Design Engineer, Bangalore, India</b> Working with the physical design team of the Processors group (Sitara MCU) to synthesize next gen processors for industrial and automotive applications <ul style="list-style-type: none"><li>Developed python scripts to extract and process excel data for IO timing analysis</li><li>Developed Shell/TCL scripts to generate a constraints validation dashboard</li></ul>	July 2023 - C
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<b>Indian Institute of Science (IISc), Research Assistant, Bangalore, India</b> Worked with Professor SK Nandy to to automate the process of running test cases on the Redefine Resource Manager (RRM). <ul style="list-style-type: none"><li>Designed customized hardware IP that loads an ELF file onto the Rocket Chip SoC using memory mapped registers and FIFOs.</li><li>Synthesized the IP on Zynq UltraScale+ MPSoC, FPGA to run test cases onto the RRM core present in the Rocket Chip SoC.</li><li>Set up the pipeline for running test cases on the SoC and ran benchmarks to evaluate the custom hardware's performance.</li></ul>	Jan. 2023 - Mai
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<b>Texas Instruments India (TIH), Digital Design Intern, Bangalore, India</b> Worked with the physical design team at Texas Instruments to improve metrics collection and management for PD flow. <ul style="list-style-type: none"><li>Configured pipeline for automating metrics collection and display for designs like radar_hsm and ecu_periph.</li><li>Enabled designers to efficiently debug their flows by adding an HTML/Javascript-based UI dashboard using Stylus CUI.</li><li>Incorporated addition of custom metrics to get further insight on chip designs.</li><li>Finalized the project by developing a wrapper (in bash) to enable comparison between multiple flows.</li></ul>	May 2022 - July
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<b>Free Unified Rendering in Python (FURY), Open Source Contributor, Indiana University Bloomington, USA</b> <u>FURY</u> is an open-source Python library with 100+ active users, available for scientific visualization. The library is available via PyPi or Anaconda package system. <ul style="list-style-type: none"><li>Developed actors to render dashed and dotted lines in 3D space. (<a href="#">Pull request</a>)</li><li>Designed and implemented a signed distance function (SDF) to render 3 types of Superquadrics. (<a href="#">Pull request</a>)</li><li>Devised an SDF for rendering a capsule actor with 2 parameters. (<a href="#">Pull request</a>)</li></ul>	Jan. 2021 - C
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<b>SandLogic, Edge AI intern, Bangalore, India</b> Was given a task to assess the impact of machine learning and accelerated computing on the hardware industry. <ul style="list-style-type: none"><li>Drew parallels between hardware acceleration of graphics with machine learning</li><li>Projected the potential growth of the ML acceleration technology over the next decade</li><li>Presented an analysis on the same at the end of the internship.</li></ul>	Oct. 2022 - Dec
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## PROJECTS

<b>Solar Energy Forecasting using AI</b> Developed an ensemble machine learning model RDLR (Random Forest, LSTM, Auto-LSTM, Ridge Regression) for short term solar energy forecasting. <ul style="list-style-type: none"><li>Ensembling was carried out using Ridge Regression (RR) to remove co-relation among the output of base models.</li><li>The diversity of base models was assessed and overall model is evaluated across 0, 5, 15, 30 and 90 minute time horizons.</li><li>The highest accuracy is for 0-minute (Coefficient of determination = 99.96%) and 5-min (Coefficient of determination = 30.90%) minute forecast followed by 15, 30 and 90 minute horizons.</li><li>RDLR model outperforms all of it's base models and is ideal to make very short to short term forecasts in the span of (0-10 min).</li></ul>	Aug. 2022 - Apr
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<b>Linear Methods for Image Deconvolution</b> <ul style="list-style-type: none"><li>Applied linear methods to filter out noise from images using deconvolution techniques.</li><li>Carried extensive comparison between Wiener and Median Filters</li><li>Used PSNR and SSIM as metrics to evaluate the quality of deconvolution and optimization of parameters</li></ul>
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<b>Image Processing (BMP formats)</b> <ul style="list-style-type: none"><li>Developed a Verilog (HDL) model to process images in <a href="#">bitmap format</a>.</li><li>Images (768 x 512 pixels) are converted from .bmp to hexadecimal format using MATLAB code and processed using FSMs in Verilog.</li><li>Performs image processing tasks such as inversion, threshold manipulation, and brightness control in under 5-10 milliseconds.</li></ul>
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<b>Optimal Portfolio Allocation</b> <ul style="list-style-type: none"><li>Engineered a Python-based model to generate an optimized portfolio of 10 NSE-listed companies.</li><li>Utilized the Markowitz and Capital Asset Pricing Models to achieve a Sharpe ratio of 1.5 (much greater than 1).</li><li>Using Monte Carlo Simulations, the value-at-risk (VaR) determination of 95% at 1.8% asset risk was carried out. (<a href="#">Project Link</a>)</li></ul>
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## CLUBS AND SOCIETIES

<b>Omdena, Junior ML Engineer (Remote)</b> <ul style="list-style-type: none"><li>Currently working on LLM model deployment for using GCP and docker for a RAG based LLM.</li><li>Worked model building, application building and deployment for plant disease classification under brazil local chapter</li><li>Completed EDA (exploratory data analysis), model building, model tuning and final presentation for student success prediction under turkey Ankara local chapter</li></ul>	July 2023 - C
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<b>IRIS, NITK, Systems Engineer</b> IRIS is a homegrown ERP solution founded and managed by students, with over 7000+ active users. <ul style="list-style-type: none"><li>Orchestrated setting up applications such as Elasticsearch and Moodle for deployment in Kubernetes.</li><li>Oversaw revamp of IRIS staging deployment server for development environment testing.</li><li>Worked on setting up host-based access control (HBAC) using FreeIPA to enable privilege separation among developers.</li></ul>	July 2020 - Apr
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<b>The Institution of Engineers, NITK Chapter, Head of Student Business Interest Group</b> <ul style="list-style-type: none"><li>Coordinated games and events for over 200+ participants as a part of KOTH, the flagship event of even semesters.</li><li>Organized Case-O-Mania, a case study competition for 25+ participants through IE's business student interest group, CAPITAL.</li><li>Used data science tools such as Pandas, NumPy, and Matplotlib to produce a 30% annual return portfolio.</li></ul>	Aug. 2020 - Apr
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