28 marks

Instructions:

- 1. Provide justification/steps for each solution. Answers without reasoning will be awarded 0 marks.
- 2. Answer questions in order. Answer all parts of a question at one place.
- 3. Label x,y axis and other significant parameters in any plot you draw.

Question 1 (a) Consider an element E with the following V-I characteristics:

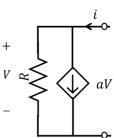
$$i_E = \left\{ \begin{array}{ll} \alpha v_E & \text{if } v_E > 2 \ V \\ 0 & \text{if } v_E \le 2 \ V \end{array} \right.$$

3 marks

with i_E and v_E denoting the current and voltage through/across the device E. Is the element linear? You can treat the current as output and voltage as input to the device.

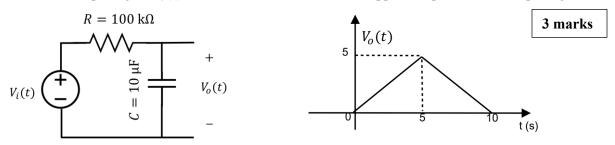
Ш	·
)	for linearly
	If IE, is output of VEI
	& IF2 15 outbut of VE2
	then 181+1E2 should be output of VEI+YEZ
	Hener for divici E
	IEI = a UEI I (UEI) 2)
	1E2 = ave2 # (ve2>2)
	the state of the s
_	1E3 = ave3 I (ve3>2)
	where as 181+182 = avel 1(vel) 2) + avez 1(ve2)
_	
_	NOW If VEI >2, VE2 <2 => UE3 >2
	Now if $V_{E1} > 2$, $V_{E2} < 2 \Rightarrow U_{E3} > 2$ ten $U_{E1} = aU_{E1}$ $U_{E2} = 0$
	VEI + VEZ = avEI
	while big = a ves + avez \$ a vez
_	= 1e1+le2
_	Henu it is not lenear.
	A simble to counter example can be given.
	take VEI = 3V, VE2 = 1V 7 VE3 = 4V
_	1
-	1= 3a 1=2 = 0 1=3 = 4a
_	Here 153 + 151+152-

3 marks

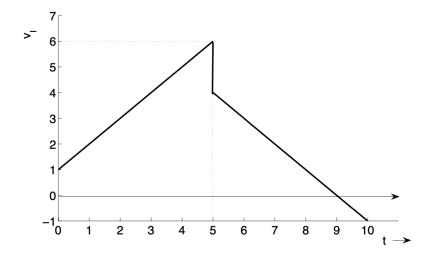


two circuits are equivalent of them w characteristics I's the same lef us combute its IV characteristic I av i = 1, +1,- R av = V(q+1) R consider the following circuit For this circuit, I'm which is a circuit.
Consider the following circuit $R' = Ra + V_R$
$ \begin{array}{c cccc} \hline & & & & & & & & & & & & & & & & & \\ \hline & & & & & & & & & & & & & & & \\ \hline & & & & & & & & & & & & \\ \hline & & & & & & & & & & \\ \hline & & & & & & & & & \\ \hline & & & & & & & & \\ \hline & & & & & & & & \\ \hline & & & & & & & \\ \hline & & & & & & & \\ \hline & & & & & & & \\ \hline & & & & & & & \\ \hline & & & & & & & \\ \hline & & & & \\ \hline & & & & & $
Consider the following circuit R R R R R R R R R R R R R
Consider the following circuit
Consider the following circuit
$\frac{1}{\sqrt{1-\frac{1}{2}}} \frac{1}{\sqrt{1-\frac{1}{2}}} \frac{1}{1-\frac{$
$\frac{1}{2} \frac{1}{2} \frac{1}$
$\frac{1}{2} \frac{1}{2} \frac{1}$
For this circul,
For this circul,
i = v(a+1)
K/
Hunu two circuit are equitodul
Yes, this arcuit is equivalent to a reo is to
Yes, this arcuit is equivaced
Value = a+VR = R
proach 2 Ra+1
this can also be done by confuly therenon
equiralent which will come out to be
a simple resistar, marks can be given it
I had computed this correctly
Yth 20 Rth 2 R
$R_{a}+1$
equivalet 13
E RM to
ov O
Time the state of

Question 2 (a) Consider the RC circuit shown below (left). The output $V_o(t)$ is shown in right. Compute and draw the input signal $V_i(t)$ as a function of t, that must be applied to produce this output signal.



Solution: We have $V_I = RC \frac{dV_0}{dt} + V_0$. RC = 1 s. For 0 < t < 5, we have $V_0 = t \implies V_I = t + 1$. For 5 < t < 10, we have $V_0 = 10 - t \implies V_I = 9 - t$. $V_I(t)$ is plotted as below.



if x,y axis are not properly labeled or other significant parameters are not given, marks will be deducted.

(b) The switch in the circuit below has been in the position A for a long time. At t = 0, the switch moves to B. Detemine $v_o(t)$ for t > 0. Calculate its value at t = 1s.

3 marks

solution:

For t < 0, the switch is at position A. The capacitor acts like an open circuit to dc, but v is the same as the voltage across the 5-k Ω resistor. Hence, the voltage across the capacitor just before t = 0 is obtained by voltage division as

$$v(0^-) = \frac{5}{5+3}(24) = 15 \text{ V}$$

Using the fact that the capacitor voltage cannot change instantaneously,

$$v(0) = v(0^{-}) = v(0^{+}) = 15 \text{ V}$$

For t > 0, the switch is in position B. The Thevenin resistance connected to the capacitor is $R_{\text{Th}} = 4 \text{ k}\Omega$, and the time constant is

$$\tau = R_{\text{Th}} C = 4 \times 10^3 \times 0.5 \times 10^{-3} = 2 \text{ s}$$

Since the capacitor acts like an open circuit to dc at steady state, $V(\infty) = 30 \text{ V. Thus,}$

$$v(t) = v(\infty) + [v(0) - v(\infty)]e^{-t/\tau}$$

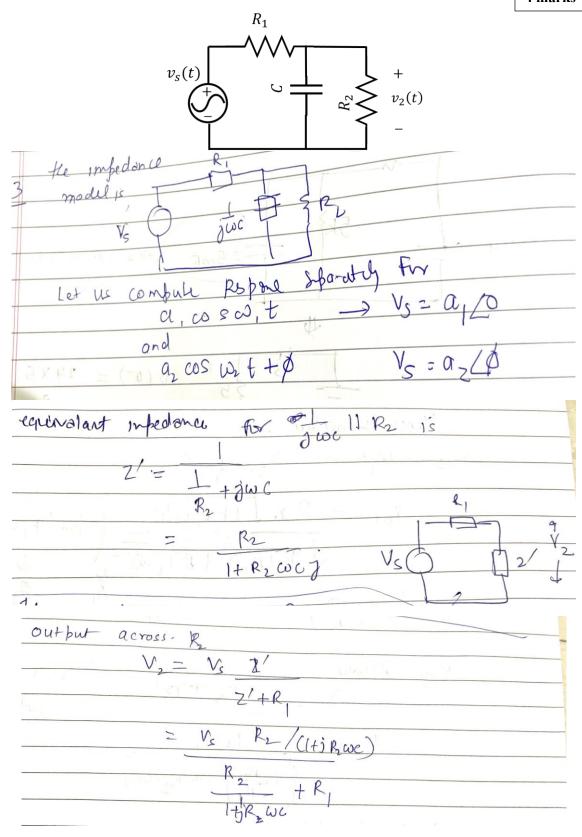
= 30 + (15 - 30)e^{-t/2} = (30 - 15e^{-0.5t}) V

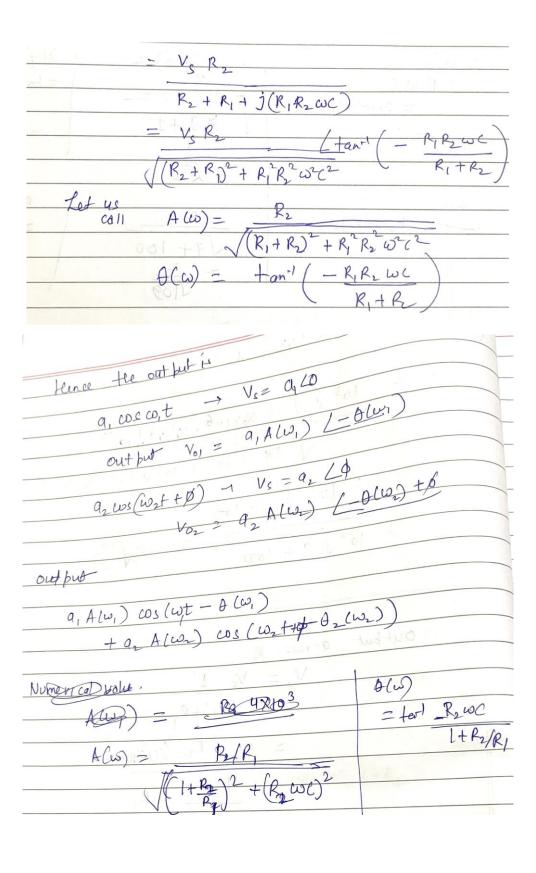
Att= 1,

$$v(1) = 30 - 15e^{-0.5} = 20.9 \text{ V}$$

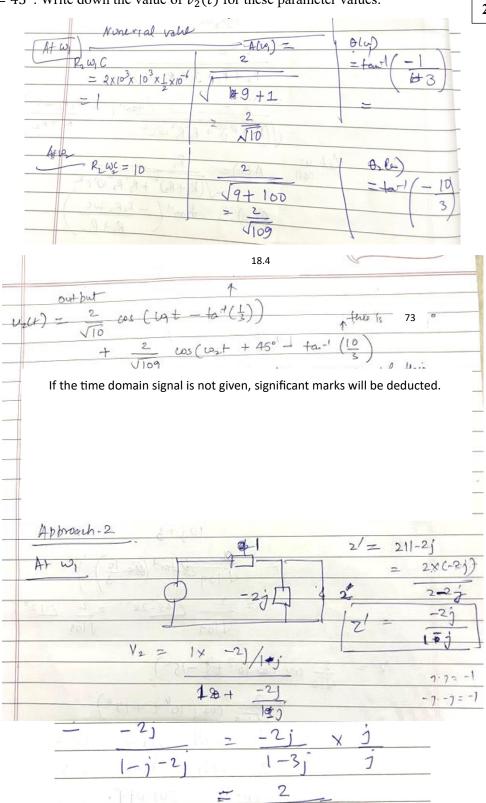
Question 3 (a) Consider the following circuit. The circuit is fed with input $v_s(t) = a_1 \cos(\omega_1 t) + a_2 \cos(\omega_2 t + \phi)$. What will the voltage $v_2(t)$ across the resistor R_2 ?

4 marks





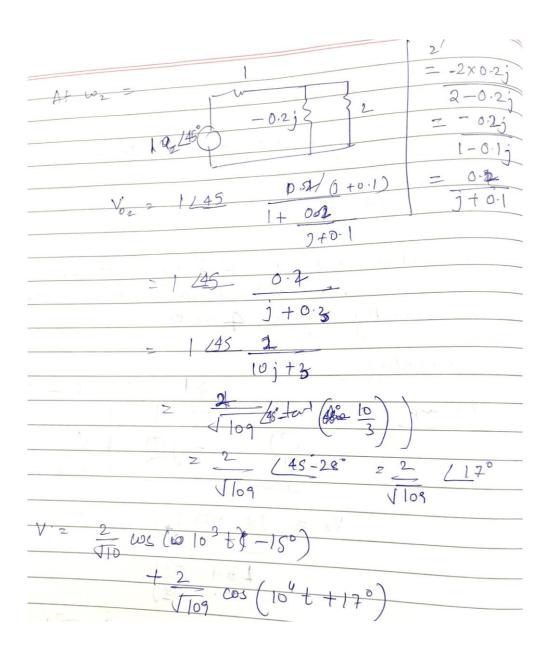
(b) Now consider $R_1 = 1k\Omega$, $R_2 = 2k\Omega$ and $C = 0.5\mu F$. Further, $a_1 = a_2 = 1$, $\omega_1 = 1kHz$, $\omega_2 = 10kHz$, $\phi = 45^\circ$. Write down the value of $v_2(t)$ for these parameter values.



1+3

2 VI+9

7



Question 4 Consider the following circuit consisting of two diodes, made of different semiconductor material (Si and Ge respectively). The Si diode has 0.7V cut-in voltage (V_{γ}) and 0 resistance while the Ge diode has 0.3V cut-in voltage and 0 resistance. Find the voltage v_R across the resistor. Compute the current through each diode.

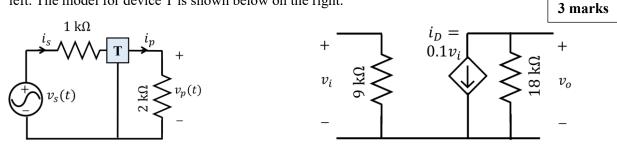
 $\begin{array}{c|c}
Ge & 3 k\Omega \\
\hline
20 V & & & & & & \\
Si & & & & & \\
\end{array}$

It appears that when the applied voltage is switched on, both the diodes will turn "on". But that is not so. When voltage is applied, germanium diode (V0 = 0.3 V) will turn on first and a level of 0.3V is maintained across the parallel circuit.

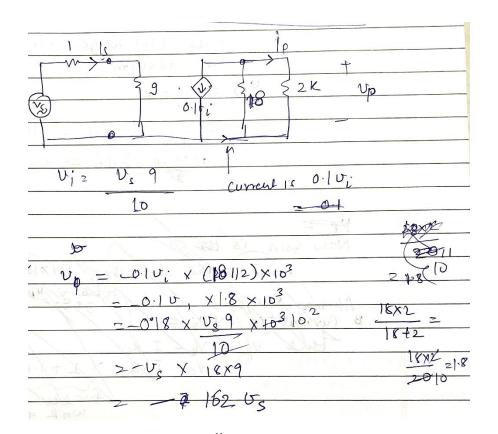
The silicon diode never gets the opportunity to have 0.7 V across it and, therefore, remains in open circuit

$$v_R = 20 - 0.3 = 19.7V$$
 $i_{Ge} = \frac{v_R}{R} = \frac{19.7}{3000} = 6.566 \, mA$
 $i_{Si} = 0 \, mA$

Question 5 (a) Determine the voltage gain v_p/v_s and current gain i_p/i_s of the circuit shown below on the left. The model for device T is shown below on the right.



Solution:



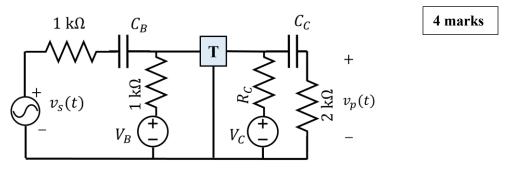
$$\frac{v_p}{v_s} = -162$$

$$\frac{i_p}{i_s} = -810$$

(b) Suppose the model of T is changed. Now the current i_D is given as

$$i_D = \left\{ \begin{array}{cc} 0.1(v_i - 1) & \text{if } v_i > 1 \, V, v_o > 1 \\ 0 & \text{otherwise} \end{array} \right.$$

Can the following circuit provide necessary bias for the device to operate and provide the $\frac{v_p}{v_s} = 50$ amplification gain to a small signal v_s of frequency 1kHz and peak-to-peak voltage 0.01V?. If yes, compute the values of C_B , C_C , V_C , R_C , V_B .



Solution: CB/CC are selected such that are short circuit at $\omega = 1kHz$

$$\frac{1}{\omega C} \gg R \sim 1 k\Omega \Rightarrow C \gg \frac{1}{\omega \times 1 k\Omega} = 1 \mu F \Rightarrow C = 20 \mu F$$
 should be sufficient

