

ESC201A EndSem Part 2

SHIV NARAYAN

TOTAL POINTS

12.5 / 19

QUESTION 1

Q1 9 pts

1.1 1(a) 3 / 3

- + 3 pts Completely Correct
- + 0 pts Completely Incorrect
- + 0 pts Not Attempted
- + 0 pts Copied
- ✓ + 1 pts DC Equivalent circuit correctly found
- ✓ + 1 pts Transistor currents correctly found
- ✓ + 1 pts Collector voltage correctly found

1.2 1(b) 0.5 / 6

- + 6 pts Completely Correct
- + 0 pts Completely Incorrect
- + 0 pts Not Attempted
- + 0 pts Copied
- + 3 pts Transistor currents correctly found
- + 1.5 pts RE correctly calculated
- + 1.5 pts R2 correctly calculated
- + 0.5 Point adjustment

QUESTION 2

Q2 10 pts

2.1 2(a) 5 / 6

- + 6 pts Completely Correct
- + 0 pts Completely Incorrect

+ 0 pts Not Attempted

+ 0 pts Copied

+ 2 pts Desirable circuit schematic correctly drawn

✓ + 1 pts Feedback resistance correctly found

✓ + 1.5 pts Resistors corresponding to source v1 correctly found

✓ + 1.5 pts Resistors corresponding to source v2 correctly found

+ 1 Point adjustment

2.2 2(b) 4 / 4

✓ + 4 pts Completely Correct

+ 0 pts Completely Incorrect

+ 0 pts Not Attempted

+ 0 pts Copied

+ 1 pts Bias state of diodes correctly found

+ 1.5 pts Vo1 correctly found

+ 1.5 pts Vo2 correctly found

Name

SHIV NARAYAN

Roll No.

210978

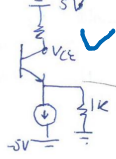
Seat/Room No.

582 / L-20

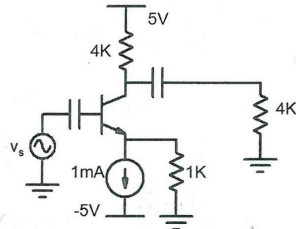
1 (a). Carry out dc analysis to determine collector voltage for the circuit shown below. Assume that current gain $\beta_F = 100$. [3]

DC Analysis $\beta_{\text{follower}} = \text{short circuit}$
 $\beta_{\text{follower}} = \text{open circuit}$

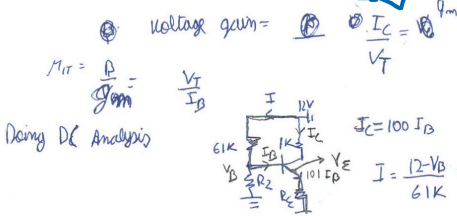
Clearly there is no current in the base terminal of BJT \Rightarrow BJT is in cutoff mode. Hence no current flows.



Hence $V_{\text{collector}} = 5V$



1 (b). Determine suitable values for resistances R_E and R_2 so as to obtain a voltage gain of -50 for the amplifier shown. Assume that dc value of base voltage is $5.9V$, transistor is in forward active mode, thermal voltage $V_T = 0.026V$ and $\beta_F = 100$. [6]



KVL (KVL by pencil path)

$$-12 + I \times 61K + 0.7 + 101 I_B R_E = 0$$

$$I_C = 50 \times V_T = 50 \times 0.026 = 1.3A$$

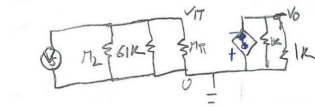
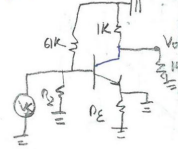
$$0.7 + 101 I_B R_E = V_B$$

$$I_B = \frac{1.3A}{100}$$

$$V_B = 5.9V \text{ (given)}$$

$$5.9 = 0.7 + 101 \times \frac{1.3}{100} R_E \Rightarrow R_E = 3.96\Omega$$

AC Analysis



$$V_o = -g_m V_{\pi} \Rightarrow -g_m V_i$$

$$\frac{-V_o}{V_i} = +g_m$$

$$g_m = +50 \Rightarrow g_m = -50$$

$$\frac{V_o}{V_i} = -50$$

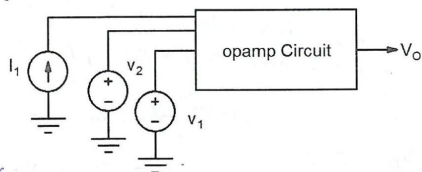
$$\frac{I_D + V_B}{R_2} = I \Rightarrow \frac{1.3}{100} + \frac{5.9}{R_2} = I$$

$$I = \frac{12 - V_B}{61K}$$

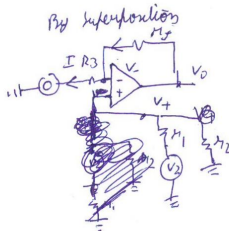
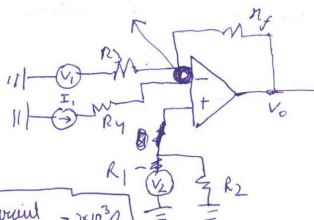
$$\frac{12 - 5.9}{61K} = \frac{1.3}{100} + \frac{5.9}{R_2}$$

$$R_2 = 453.84\Omega$$

2(a). Design an opamp circuit that would produce the output voltage $V_o = -2 \times 10^3 I_1 - 2v_1 + v_2$, where I_1 , v_1 and v_2 are input current and input voltages respectively as shown below. Assume ideal opamp characteristics and use only one opamp. [6]

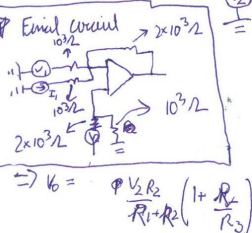


same point

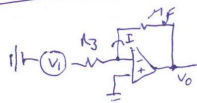


$$V_+ = V_- = \frac{V_2 R_2}{R_1 + R_2}$$

$$I \Rightarrow \frac{V_-}{R_3} = I \Rightarrow I = \frac{V_2 R_2}{R_3 (R_1 + R_2)} \Rightarrow V_o = -I R_f (1 + \frac{R_f}{R_3})$$



Next superposition



$$V_o \approx -V_1 \left(\frac{R_f}{R_3} \right)$$

$$\text{as } V_- = V_+ = 0$$

$$\text{so } I = \frac{V_1}{R_3} \Rightarrow V_o = -\frac{V_1}{R_3} \times R_f$$

Next superposition

$$V_- = V_+ = 0$$

$$\text{so } V_o = -I R_f$$

summing All V_o 's

$$V_{\text{net}} = \frac{V_2 R_2}{R_1 + R_2} \left(1 + \frac{R_f}{R_3} \right) - \frac{V_1 R_f}{R_3} - \frac{I R_f}{R_3}$$

Comparing coefficients we get

$$-I R_f = -2 \times 10^3 I$$

$$\Rightarrow R_f = 2 \times 10^3 \Omega$$

$$\frac{R_2}{R_1 + R_2} \left(1 + \frac{R_f}{R_3} \right) = 1 \Rightarrow \frac{R_2}{R_1 + R_2} = 1$$

$$\frac{R_f}{R_3} = 2 \Rightarrow R_3 = 10^3 \Omega$$

$$2 R_2 = R_1 \Rightarrow R_2 = 10^3 \Omega, R_1 = 2 \times 10^3 \Omega$$

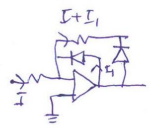
2(b). Assuming ideal opamp and ideal diodes, determine output voltage V_{o1} and V_{o2} . Note that opamp supply voltages are $\pm 12V$. [4]

Assuming Virtual ground property

$$V_- = V_+ = 0$$

So the value of I is

$$\frac{5}{10^3} A \quad \text{assume } D_1 \text{ is forward biased}$$

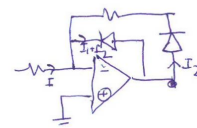


$$\Rightarrow V_{o2} = V_- = 0$$

but this makes D_2 reverse biased hence no current can flow through D_2 hence D_1 forward biased. Not possible

Case II

assume D_2 is forward



$V_{o1} = V_{o2}$ but $I_1 + I_2 > 0$ hence D_1 is reverse biased so not possible

So Both diodes are reverse biased and no current flow $\Rightarrow V_{o1} = V_{o2} = 5V$

$$V_{o2} = -12V \quad (\text{since opamp will be inversely saturated})$$

