# EXPERIMENT-07(A)

#### **+** AIM:

To study and plot the input and output characteristics of common emitter transistor.

## + THEORY:

A Bipolar Junction Transistor (BJT) has three terminals connected to three doped semiconductor regions. The three parts of a BJT are collector, emitter, and base.

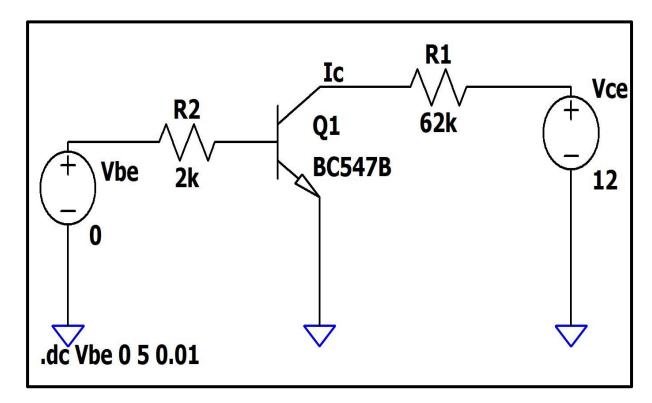
In CE configuration, we use emitter as common terminal for both input and output. This common emitter configuration is an inverting amplifier circuit. Here the input is applied between base-emitter region and the output is taken between collector and emitter terminals. In this configuration, the input parameters are  $V_{\text{BE}}$  and  $I_{\text{B}}$  and the output parameters are  $V_{\text{CE}}$  and  $I_{\text{C}}$ .

In this configuration, the emitter current is equal to the sum of small base current and the large collector current. i.e.,

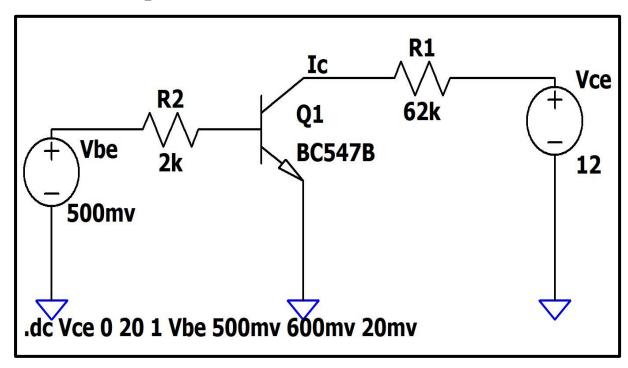
$$I_E = I_C + I_{B.}$$

#### + CIRCUIT DIAGRAM:

# • For Input:



# • For Output:



# **+OBSERVATION TABLE:**

OInput Characteristics:

$V_{CE} = 1V$		$V_{\text{CE}} = 2V$		$V_{CE} = 3V$	
V <sub>BE</sub> (V)	Ів (µА)	V <sub>BE</sub> (V)	Ів (µА)	V <sub>BE</sub> (V)	Ів (µА)
0.555	0	0.568	0	0.627	0
0.673	0.016	0.614	0.006	0.667	0.005
0.698	0.038	0.693	0.034	0.691	0.018
0.715	0.066	0.708	0.056	0.712	0.052
0.730	0.101	0.721	0.086	0.732	0.080
0.738	0.130	0.735	0.139	0.754	0.127
0.745	0.163	0.736	0.155	0.756	0.153

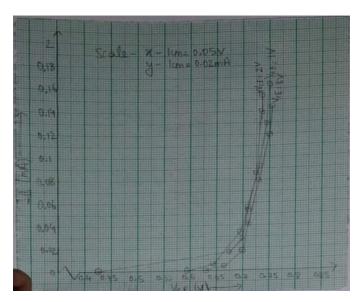
# Output Characteristics: IB

 $=40 \mu A$ 

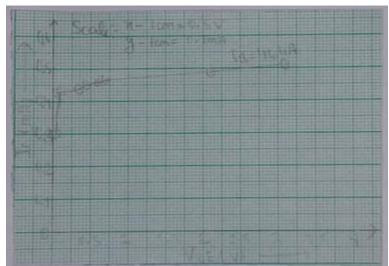
$V_{CE}(V)$	Ic (mA)	
0.014	0.29	
0.145	0.43	
0.312	0.44	
0.423	0.45	
0.599	0.46	
0.881	0.47	
2.146	0.49	
3.150	0.51	

## + GRAPH

• Input Characteristics:



• Output Characteristics:



## + RESULT:

The input and output characteristics of CE transistor are observed.

EXPERIMENT-07(B)

#### + AIM:

To study and plot the input and output characteristics of common base transistor.

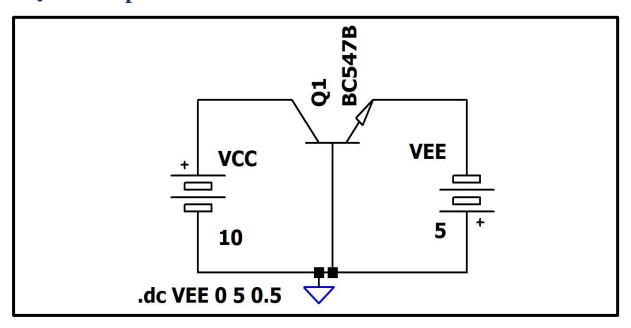
#### + THEORY:

A Bipolar Junction Transistor (BJT) has three terminals connected to three terminals connected to three doped semiconductor regions.

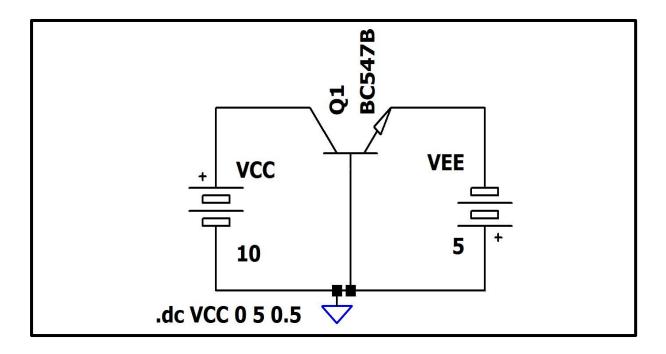
In this configuration, we use base as common terminal for both input and output signals. The common base circuit is mainly used in single stage amplifier circuits, such as microphone pre-amplifiers or radio frequency amplifiers. Here the input is applied between base and emitter terminals and the corresponding output signal is taken between the base and collector terminals with the base terminal grounded. Here the input parameters are  $V_{BE}$  and  $I_E$  and the output parameters are  $V_{CB}$  and  $I_C$ . The input current flowing into the emitter terminal must be higher than the base current and the collector current to operate the transistor, therefore the output collector current is less than the input emitter current.

#### **+** CIRCUIT DIAGRAM:

# • For Input:



# • For Output:



# **+ OBSERVATION TABLE:**

# **OInput Characteristics:**

S.NO.		V <sub>CB</sub> =2V	V <sub>CB</sub> =20V
	$\mathbf{V}_{EB}(\mathbf{V})$	<b>I</b> <sub>B1</sub> ( <b>mA</b> )	<b>I</b> <sub>B2</sub> ( <b>mA</b> )
1.	0.1	0.0	0.0
2.	0.2	0.0	0.0
3.	0.3	0.0	0.0
4.	0.4	0.0	0.0
5.	0.5	0.007	0.008
6.	0.6	0.3	0.5
7.	0.7	3.5	20
8.	0.72	3.9	21.4
9.	0.74	5.0	22.1
10.	0.76	7.0	23.5
11.	0.78	10.0	26.1
12.	0.8	15.0	30.4

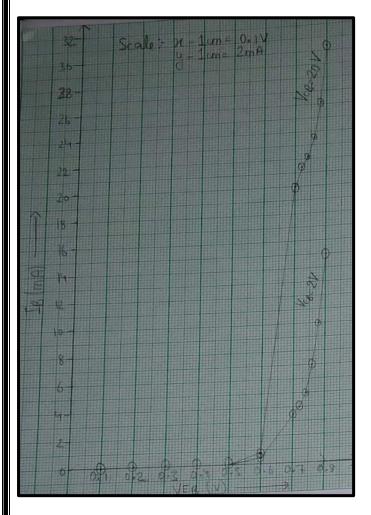
# **O**Output Characteristics:

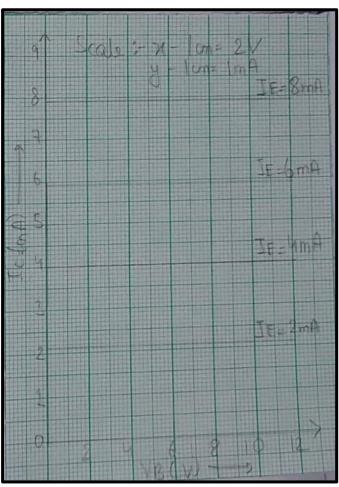
		$I_{\text{E}} = 2mA$	$I_E = 4mA$	$I_E = 6mA$	$I_E = 8mA$
S.NO.	$V_{CB}(V)$	$I_{C1}$ (mA)	$I_{C2}$ (mA)	$I_{C3}$ (mA)	$I_{C4}$ (mA)
1.	2	2.07	4	5.9	7.9
2.	4	2.07	4	5.9	7.9
3.	6	2.07	4	5.9	7.9
4.	8	2.07	4	5.9	7.9
5.	10	2.07	4	5.9	7.9
6.	12	2.07	4	5.9	7.9

## + GRAPH

• Input Characteristics:

• Output Characteristics:





# **→RESULT:**

The input and output characteristics of CB transistor are observed.