

ECE 272 Lab 4
Fall 2018

System Verilog and Complex Projects
Phi Luu

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Grading TA: Edgar Perez
Lab Partner: Benjamin Geyer

1 Introduction

2 Design

3 Results

4 Experiment Notes

Reflection

Study Questions

1. Describe how to control a 7-seg display using a state machine and lighting each digit one at a time.

Appendix

No appendix is available in this lab.