

OREGON STATE UNIVERSITY

ECE 271

Final Design Project

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- 7 1 1 Top Lovel

```
input logic[1:0] dir_x, dir_y,
3
         output logic vs, hs,
         output logic[3:0] r, g, b
5
    );
6
    logic[9:0] xpos, ypos;
8
    logic slowclk;
9
10
    logic[3:0] box_r, box_g, box_b;
11
    logic[9:0] box_x, box_y, box_size;
12
13
    half_clock hc (
14
         .oldclk(clk),
15
         .newclk(slowclk)
16
    );
17
18
    XYCounter c (
19
         .clk(slowclk),
20
         .vs(vs),
21
         .hs(hs),
22
         .x(xpos),
23
         .y(ypos)
24
    );
25
26
    box_controller box (
27
         .vs(vs),
28
         .dir_x(dir_x),
29
         .dir_y(dir_y),
30
         .x(xpos),
31
         .y(ypos),
32
         .box_r(box_r),
         .box_g(box_g),
34
         .box_b(box_b),
35
         .box_x(box_x),
36
         .box_y(box_y),
37
         .box_size(box_size)
38
    );
39
40
    Drawer d (
41
         .clk(slowclk),
42
         .box_r(box_r),
43
         .box_g(box_g),
44
         .box_b(box_b),
45
         .x(xpos),
46
         .y(ypos),
47
         .box_x(box_x),
48
         .box_y(box_y),
```

```
.box_size(box_size),
.r(r),
.g(g),
.b(b)
.b(b)
.equiv between the control of the control of
```

7.1.2 PS/2 Top Level

```
module ps2_top_level(
1
         input logic clk, psClk, data,
        output logic vs, hs,
3
        output logic[3:0] r, g, b
    );
    // Track x and y and slow clock
    logic[9:0] xpos, ypos;
    logic slowclk;
10
    // Get direction input
11
    logic[1:0] dir_x, dir_y;
12
13
    // Track box info
14
    logic[3:0] box_r, box_g, box_b;
    logic[9:0] box_x, box_y, box_size;
16
17
    // Slow clock
18
    half_clock hc (
19
         .oldclk(clk),
20
         .newclk(slowclk)
21
    );
22
23
    // Generate position and vs/hs signals
24
    XYCounter c (
25
         .clk(slowclk),
26
         .vs(vs),
27
         .hs(hs),
28
         .x(xpos),
         .y(ypos)
30
    );
31
32
    // Get keyboard input for direction
33
    ps2 P (
34
```

```
.clk(psClk),
          .data(data),
36
          .dir_x(dir_x),
37
          .dir_y(dir_y)
38
    );
39
40
     // Move box
41
     box_controller box (
42
         .vs(vs),
43
          .dir_x(dir_x),
44
         .dir_y(dir_y),
45
         .box_r(box_r),
46
          .box_g(box_g),
47
         .box_b(box_b),
48
          .box_x(box_x),
          .box_y(box_y),
50
         .box_size(box_size)
51
     );
52
53
     // Draw box
54
     Drawer d (
55
         .clk(slowclk),
56
         .box_r(box_r),
57
          .box_g(box_g),
58
         .box_b(box_b),
59
         .x(xpos),
60
          .y(ypos),
61
         .box_x(box_x),
62
          .box_y(box_y),
63
         .box_size(box_size),
          .r(r),
65
         .g(g),
66
         .b(b)
67
     );
68
69
     endmodule
70
```

7.1.3 PS/2 Controller

```
output logic[1:0] dir_x, dir_y
5
    );
7
    // Track data input
8
    logic[7:0] bits = 8'b0;
10
    // Track count of bits coming in and number of ones
11
    logic[3:0] count = 0;
12
    logic[3:0] even_odd = 0;
13
14
    // Hold output values
15
    // 0 = up/left
16
    // 1 = down/right
17
    // 2 = stop
18
    logic[1:0] x = 2;
19
    logic[1:0] y = 2;
    // Track valid input and whether past input was FO (indicating key release)
22
    logic valid = 0;
23
    logic fo = 0;
24
25
    always_ff @(negedge clk)
26
        begin
27
             if (count == 10) // Ending bit
28
                 begin
29
                      if (valid)
30
                          // Translate directions to movement
31
                          begin
32
                              // not released && is not moving && to move up -> move up
                              if (~fo && y == 2 && bits == UP) y <= 0;</pre>
                              // released && is moving up && to move up -> stop
                              else if (fo && y == 0 && bits == UP) y <= 2;
36
                              // not released && is not moving && to move down -> move
37
                               \rightarrow down
                              else if (~fo && y == 2 && bits == DOWN) y <= 1;
38
                              // released && is moving down && to move down -> stop
39
                              else if (fo && y == 1 && bits == DOWN) y <= 2;
40
41
                              // not released && is not moving && to move left -> move
42
                               \hookrightarrow left
                              if (~fo && x == 2 && bits == LEFT) x <= 0;</pre>
43
                              // released && is moving left && to move left -> stop
44
                              else if (fo && x == 0 && bits == LEFT) x <= 2;
45
                              // not released && is not moving && to move right -> move
46
                               \hookrightarrow right
                              else if ("fo && x == 2 && bits == RIGHT) x <= 1;
                              // released && is moving right && to move right -> stop
```

```
else if (fo && x == 1 && bits == RIGHT) x <= 2;
50
                                // Get code for key release
51
                                if (bits == 8'hF0) fo <= 1;</pre>
52
                                 else fo <= 0;</pre>
53
                            end
                       even_odd <= 0;</pre>
                       count <= 0;</pre>
57
                       bits <= 8'd0;
58
                       valid <= 0;</pre>
59
60
              else if (count == 9) // Check parity bit
61
                  begin
62
                       count <= count + 1;</pre>
64
                       // Check validity based on number of ones and parity bit (odd
65
                        → total)
                       valid <= even_odd[0] == ~data;</pre>
66
67
              else if (count == 0) // Start bit -- do nothing but advance count
                   count <= count + 1;</pre>
              else
70
                  begin
71
                       // Track count and number of odd
72
                       even_odd <= even_odd + data;</pre>
73
                       count <= count + 1;</pre>
74
75
                       // Load data
76
                       bits <= (bits >> 1) + (data * 128);
                   end
         end
79
80
     // Set output
81
     assign dir_x = x;
82
     assign dir_y = y;
83
84
     endmodule
85
```

7.1.4 RC5 IR Top Level

```
module ir_top_level(
input logic clk, data,
output logic vs, hs,
```

```
output logic[3:0] r, g, b
    );
6
    // Track x and y and slow clock
    logic[9:0] xpos, ypos;
    logic slowclk;
9
10
    // Get direction input
11
    logic[1:0] dir_x, dir_y;
12
13
    // Track box info
14
    logic[3:0] box_r, box_g, box_b;
15
    logic[9:0] box_x, box_y, box_size;
16
17
    // Slow clock
18
    half_clock hc (
19
         .oldclk(clk),
20
         .newclk(slowclk)
21
    );
22
23
    // Generate position and vs/hs signals
24
    XYCounter c (
25
         .clk(slowclk),
26
         .vs(vs),
         .hs(hs),
28
         .x(xpos),
29
         .y(ypos)
30
    );
31
32
    // Get IR input for direction
33
    ir I (
34
         .clk(clk),
35
         .data(data),
36
         .dir_x(dir_x),
37
         .dir_y(dir_y)
38
    );
39
40
    // Move box
41
    box_controller box (
42
         .vs(vs),
43
         .dir_x(dir_x),
44
         .dir_y(dir_y),
45
         .box_r(box_r),
46
         .box_g(box_g),
47
         .box_b(box_b),
48
         .box_x(box_x),
         .box_y(box_y),
```

```
.box_size(box_size)
    );
52
53
    // Draw box
54
    Drawer d (
55
         .clk(slowclk),
56
         .box_r(box_r),
57
         .box_g(box_g),
         .box_b(box_b),
59
         .x(xpos),
60
         .y(ypos),
61
         .box_x(box_x),
62
          .box_y(box_y),
63
         .box_size(box_size),
64
         .r(r),
         .g(g),
66
         .b(b)
67
     );
68
69
     endmodule
70
```

7.1.5 RC5 IR Controller

```
// IR module for RC5 code
    module ir #(parameter
2
        UP = 2, DOWN = 8, LEFT = 4, RIGHT = 6
3
    )(
4
        input logic data, clk,
5
        output logic[1:0] dir_x, dir_y
6
    );
7
    // Count for slowing clock and new clock
    logic[10:0] clkCount = 0;
10
    logic newclk = 0;
11
12
    // Complete data word and its update status
13
    logic[5:0] dataWord = 0;
14
    logic newDataWord = 0;
15
16
    // Internal x/y movement
    logic[1:0] x = 2;
18
    logic[1:0] y = 2;
19
20
    // Used for handling pulses
21
```

```
logic pusleVal = 0;
     logic newPulseVal = 0;
23
     logic waitForRepeat = 0;
24
25
     // Slow clock from 50MHz to 36KHz as is used for RC5
26
     always_ff @(posedge clk)
27
         if (clkCount < 1388)</pre>
28
                   clkCount <= clkCount + 1;</pre>
29
         else
30
              begin
31
                   clkCount <= 0;</pre>
32
                  newclk = ~newclk;
33
              end
34
35
     // Converts from encoded pulse to binary
36
     irConvertPulse cp(
37
         .data(data),
38
         .clk(newclk),
39
          .val(pusleVal),
40
          .newval(newPulseVal),
41
         .waitForRepeat(waitForRepeat)
42
     );
43
     // Converts from encoded binary to data word
45
     irReadWord rw(
46
         .val(pulseVal),
47
          .clk(newPulseVal),
48
          .waitForRepeat(waitForRepeat),
49
          .data(dataWord),
50
         .finished(newDataWord)
51
     );
52
     // Translate from data word to movement value
54
     always_ff @(posedge newDataWord)
55
         begin
56
              if (dataWord == UP) y <= 0;</pre>
57
              else if (dataWord == DOWN) y <= 1;</pre>
              else y <= 2;</pre>
60
              if (dataWord == LEFT) x <= 0;</pre>
61
              else if (dataWord == RIGHT) x <= 1;</pre>
62
              else x <= 2;</pre>
63
         end
64
65
     assign dir_x = x;
66
     assign dir_y = y;
67
68
```

```
endmodule
69
70
71
     module irReadWord(
72
          input logic val, clk, waitForRepeat,
73
          output logic[5:0] data,
74
          output logic finished
75
     );
76
77
     // Stores output and counts bits
78
     logic[5:0] out = 0;
79
     logic[4:0] count = 0;
80
81
     // Tracks previous toggle -- not used in this implementation
82
     // but necessary for certain controls so it will be tracked
83
     // anyway
     logic prevToggle = 1;
85
86
     always_ff @(posedge clk, negedge waitForRepeat)
87
          begin
88
               if (~waitForRepeat) // Reset if time limit for repeating signal is up
89
                   begin
90
                        out <= 0;
                        count <= 0;</pre>
92
                        finished <= 1;</pre>
93
94
               else if (count == 13) // Reset count, store final value, and signal
95
               \hookrightarrow finished transmission
                   begin
96
                        out <= (out << 1) + val;
97
                        finished <= 1;</pre>
                        count <= 0;</pre>
99
                   end
100
               else if (count == 2 && val != prevToggle) // Set toggle and reset value
101
               \rightarrow if different button press
                   begin
102
                        prevToggle <= val;</pre>
103
                        out <= 0;
104
                        count <= count + 1;</pre>
105
                        finished <= 0;</pre>
106
                   end
107
               else if (count > 7) // If in data word, add to output
108
                   begin
109
                        out <= (out << 1) + val;
110
                        count <= count + 1;</pre>
111
                        finished <= 0;</pre>
112
                   end
```

```
else
114
                   begin
115
116
                        count <= count + 1;</pre>
                        finished <= 0;</pre>
117
                   end
118
          end
119
120
     assign data = out;
121
     endmodule
123
124
125
     module irConvertPulse(
126
          input logic data, clk,
127
          output logic val, newval, waitForRepeat
128
     );
129
130
     // Tracks two sections with potential to have pulses
131
     logic[5:0] dataCounterOne = 0;
132
     logic[5:0] dataCounterTwo = 0;
133
134
     // Counts time
135
     logic[5:0] timeCounter = 0;
136
137
     // Counts time from
     logic[6:0] waitCounter = 78;
139
     logic idle = 1;
140
     logic out = 0;
141
142
     logic tcReset = 0;
143
     logic dcReset = 0;
144
     logic dcResetSuccess = 0;
146
     always_ff @(posedge data)
147
          begin
148
               if (idle) // Reset time counter if start of a transmission
149
                   begin
150
                        dataCounterOne <= 0;</pre>
151
                        dataCounterTwo <= 1;</pre>
152
                        tcReset <= 1;
153
                   end
154
               else if (dcReset)
155
                   begin
156
                        if (timeCounter > 31 && dataCounterTwo < 32)</pre>
157
                             begin
158
                                 dataCounterOne <= 0;</pre>
                                 dataCounterTwo <= 1;</pre>
160
```

```
end
                        else if (dataCounterOne < 32)</pre>
162
                            begin
163
                                 dataCounterOne <= 1;</pre>
164
                                 dataCounterTwo <= 0;</pre>
165
                            end
166
167
                        dcResetSuccess <= 1;</pre>
168
                   end
169
               else // Increment data
170
                   begin
171
                        if (timeCounter > 31 && dataCounterTwo < 32) dataCounterTwo <=</pre>
172

→ dataCounterTwo + 1;

                        else if (dataCounterOne < 32) dataCounterOne <= dataCounterOne +</pre>
173
                        tcReset <= 0;
175
                        dcResetSuccess <= 0;</pre>
176
                   end
177
          end
178
179
     always_ff @(posedge clk)
180
          begin
181
               if (tcReset)
182
                   begin
183
                        timeCounter <= 33;
184
                        waitCounter <= 0;</pre>
185
                        idle <= 0;
186
                   end
187
               else if (timeCounter == 63) // Set output and reset
                   begin
                        // Increment counter if still in window for additional
190
                        if (idle && dataCounterOne == 0 && dataCounterTwo == 0 &&
191
                           waitCounter < 78)</pre>
                            waitCounter <= waitCounter + 1;</pre>
192
193
                        out <= dataCounterOne == 0 && dataCounterTwo == 32 && ~dcReset;
194
                        newval <= (dataCounterOne == 32 || dataCounterTwo == 32) &&
                        → ~dcReset;
                        idle <= (dataCounterOne == 0 && dataCounterTwo == 0) || ~dcReset;</pre>
196
                        dcReset <= 1;
197
                        timeCounter <= 0;</pre>
198
                   end
199
               else // Increment data
200
                   begin
201
                        timeCounter <= timeCounter + 1;</pre>
                        newval <= 0;</pre>
```

```
if (dcResetSuccess)
205
                             dcReset <= 0;</pre>
206
                    end
207
          end
208
209
     assign val = out;
210
211
     // Do not wait if outside of continued transmission window
     assign waitForRepeat = waitCounter < 78;</pre>
213
214
     endmodule
215
```

7.1.6 NES Top Level

```
module nes_top_level(
         input logic clk, data,
         output logic vs, hs, latch_out, clk_out,
        output logic[3:0] r, g, b
4
    );
5
    // Track x and y and slow clock
    logic[9:0] xpos, ypos;
    logic slowclk;
    // Get direction input
    logic[1:0] dir_x, dir_y;
12
13
    // Track box info
14
    logic[3:0] box_r, box_g, box_b;
15
    logic[9:0] box_x, box_y, box_size;
16
17
    // Slow clock
18
    half_clock hc (
19
         .oldclk(clk),
20
         .newclk(slowclk)
21
    );
22
23
    // Generate position and vs/hs signals
24
    XYCounter c (
25
         .clk(slowclk),
26
         .vs(vs),
27
         .hs(hs),
28
         .x(xpos),
29
```

```
.y(ypos)
30
    );
31
32
    // Get nes input for direction
33
    nes N (
34
         .clk(clk),
35
         .data(data),
36
         .dir_x(dir_x),
37
         .dir_y(dir_y),
38
         .latch_out(latch_out),
39
         .clk_out(clk_out)
40
    );
41
42
    // Move box
43
    box_controller box (
44
         .vs(vs),
45
         .dir_x(dir_x),
46
         .dir_y(dir_y),
47
         .box_r(box_r),
48
         .box_g(box_g),
49
         .box_b(box_b),
50
         .box_x(box_x),
51
         .box_y(box_y),
52
         .box_size(box_size)
53
    );
54
55
    // Draw box
56
    Drawer d (
57
         .clk(slowclk),
58
         .box_r(box_r),
         .box_g(box_g),
60
         .box_b(box_b),
61
         .x(xpos),
62
         .y(ypos),
63
         .box_x(box_x),
64
         .box_y(box_y),
65
         .box_size(box_size),
66
         .r(r),
67
         .g(g),
68
         .b(b)
69
    );
70
71
    endmodule
72
```

7.1.7 NES Controller

```
module nes #(parameter
1
        UP = 5, DOWN = 6, LEFT = 7, RIGHT = 8
2
    )(
3
         input logic data, clk,
4
         output logic latch_out, clk_out,
5
         output logic[1:0] dir_x, dir_y
6
    );
    // Track status of x and y direction and if a directional button was pressed
9
    logic[1:0] x = 2;
    logic[1:0] y = 2;
11
    logic x_btn_press = 0;
12
    logic y_btn_press = 0;
13
14
    // Track when to poll controller for button presses
15
    logic[19:0] pollClkCount = 0;
16
    logic pollClk = 0;
17
18
    // Track when to pulse clock
19
    logic[9:0] pulseClkCount = 0;
20
    logic pulseClk = 0;
21
22
    // Track info about latch and clock pulse
23
    logic latch_inner = 0;
24
    logic new_pulse = 0;
25
    logic start_pulse_out = 0;
26
    logic[3:0] pulseCounter = 0;
27
28
    // Convert clk from 50MHz to 60Hz for polling controller
29
    // and to 83kHz to produce clock pulses
30
    always_ff @(posedge clk)
31
         begin
32
             if (pollClkCount == 416666)
33
                 begin
34
                      // Start polling controller by resetting pulse data, starting
                      → latch,
                      // and signaling start of new pulse
                      if (~pollClk)
37
                          begin
38
                              latch_inner <= 1;</pre>
39
                              new_pulse <= 1;</pre>
40
                              pulseClkCount <= 0;</pre>
41
                              pulseClk <= 0;</pre>
42
                              pulseCounter <= 0;</pre>
```

```
end
45
                       pollClkCount <= 0;</pre>
46
                       pollClk <= ~pollClk;</pre>
47
                   end
48
              else
49
                  pollClkCount <= pollClkCount + 1;</pre>
51
              if (pulseClkCount == 301)
52
                  begin
53
                       if (new_pulse && ~pulseClk) // Only update if controller poll
54
                           has started
                            begin
55
                                // Track beginning of clock pulse to controller (after
                                 → latch)
                                if (pulseCounter > 0 && latch_inner == 0)
57
                                     start_pulse_out <= 1;
58
59
                                // Count pulse
60
                                pulseCounter <= pulseCounter + 1;</pre>
61
                            end
62
63
                       if (new_pulse && pulseClk) // Only update if controller poll has
64
                        \hookrightarrow started
                            begin
65
                                // Reset latch once its 12us have passed
66
                                if (pulseCounter > 0 && latch_inner)
67
                                     latch_inner <= 0;</pre>
68
69
                                // Reset pulse data if all buttons have been accounted
                                if (pulseCounter == 9)
71
                                     begin
                                          new_pulse <= 0;</pre>
73
                                          start_pulse_out <= 0;
74
                                          y_btn_press <= 0;</pre>
75
                                          x_btn_press <= 0;</pre>
76
                                     end
77
                                else
                                     begin
79
                                          // Check for UP or DOWN buttons being pressed and
80
                                          \hookrightarrow track that
                                          // a button in y direction has been pressed
81
                                          if (pulseCounter == UP && data == 0)
82
                                              begin
83
                                                   y \ll 0;
84
                                                   y_btn_press <= 1;</pre>
                                              end
86
```

```
else if (pulseCounter == DOWN && data == 0)
87
                                              begin
88
                                                   y <= 1;
89
                                                   y_btn_press <= 1;</pre>
90
                                              end
91
                                          else if (pulseCounter >= UP && pulseCounter >=
92
                                          → DOWN && y_btn_press == 0) // Reset if no y
                                            button press
                                              y <= 2;
93
94
                                          // Check for RIGHT or LEFT buttons being pressed
95
                                          \hookrightarrow and track that
                                          // a button in x direction has been pressed
96
                                          if (pulseCounter == LEFT && data == 0)
97
                                              begin
                                                   x \ll 0;
99
                                                   x_btn_press <= 1;</pre>
100
101
                                          else if (pulseCounter == RIGHT && data == 0)
102
                                              begin
103
                                                   x <= 1;
104
                                                   x_btn_press <= 1;</pre>
105
                                              end
106
                                          else if (pulseCounter >= LEFT && pulseCounter >=
107

→ RIGHT && y_btn_press == 0) // Reset if no x
                                            button press
                                              x <= 2;
108
                                     end
109
                            end
110
111
                       pulseClkCount <= 0;</pre>
                       pulseClk <= ~pulseClk;</pre>
                   end
114
              else
115
                   pulseClkCount <= pulseClkCount + 1;</pre>
116
          end
117
118
     // Assign outputs, and only set clock if part of pulse output
119
     assign latch_out = latch_inner;
120
     assign clk_out = start_pulse_out && pulseClk;
121
     assign dir_x = x;
122
123
     assign dir_y = y;
124
     endmodule
125
```

7.1.8 Box Controller

```
module box_controller #(parameter
1
         SIZE = 40
2
    )(
3
         input logic vs,
4
         input logic[1:0] dir_x, dir_y,
5
         output logic[3:0] box_r, box_g, box_b,
         output logic[9:0] box_x, box_y, box_size
    );
9
    // Track x and y
10
    logic[9:0] xpos = 300;
11
    logic[9:0] ypos = 220;
12
13
    // Track box color
    logic[3:0] r = 4'b1111;
15
    logic[3:0] g = 4'b0;
16
    logic[3:0] b = 4'b0;
17
18
    always_ff @(posedge vs)
19
         begin
20
             if(~(dir_x == 0 && xpos == 0) && ~(dir_x == 1 && xpos == 640 - SIZE)) //
21
              \,\,\hookrightarrow\,\, Check that box will remain in screen bounds
                  begin
22
                      if((xpos - 1 == 0 && dir_x == 0) || (xpos + 1 == 640 - SIZE &&
23
                          dir_x == 1)) // Check for collision
                          begin
24
                               // Swap colors if collision occurs
25
                               r \le g;
26
                               g <= b;
                               b <= r;
28
                          end
29
30
                      // Increment position
31
                      case(dir_x)
32
                          0: xpos <= xpos - 1;
                          1: xpos <= xpos + 1;
                      endcase
                  end
36
37
             if(~(dir_y == 0 && ypos == 0) && ~(dir_y == 1 && ypos == 480 - SIZE)) //
38
              \hookrightarrow Check that box will remain in screen bounds
                  begin
39
                      if((ypos - 1 == 0 && dir_y == 0) || (ypos + 1 == 480 - SIZE &&
40
                      \rightarrow dir_y == 1)) // Check for collision
```

```
begin
                               // Swap colors if collision occurs
42
43
                               r <= g;
                               g <= b;
44
                               b <= r;
45
                           end
46
47
                      // Increment position
48
                      case(dir_y)
49
                           0: ypos <= ypos - 1;
50
                           1: ypos <= ypos + 1;
51
                      endcase
52
                  end
53
         end
54
    assign box_x = xpos;
56
    assign box_y = ypos;
57
    assign box_size = SIZE;
58
    assign box_r = r;
59
    assign box_g = g;
60
    assign box_b = b;
61
62
    endmodule
63
```

7.1.9 Half Clock

```
module half_clock(
1
         input logic oldclk,
2
         output logic newclk
3
    );
4
    logic count = 0;
     always_ff @(posedge oldclk)
8
         if (count == 1)
9
              begin
10
                  newclk <= 1;</pre>
11
                   count <= 0;
12
              end
         else
14
              begin
15
                  newclk <= 0;</pre>
16
                   count <= count + 1;</pre>
17
              end
18
```

20 endmodule

7.1.10 XY Counter

```
module XYCounter(
1
         input logic clk,
2
         output logic vs, hs,
         output logic[9:0] x, y
    );
    // Track current x and y
    logic[9:0] xpos = 0;
    logic[9:0] ypos = 0;
9
10
    always_ff @(posedge clk)
11
         begin
12
             // Generate HSync and VSync signals when in sync section
13
             hs \leq ~((xpos >= 640 + 16) && (xpos < 640 + 16 + 96));
14
             vs <= ((ypos >= 480 + 10) && (ypos < 480 + 10 + 2));
15
16
             if(xpos == 800 && ypos == 525) // Reset when at end of screen
17
                 begin
                      ypos <= 0;</pre>
                      xpos <= 0;</pre>
20
                  end
21
             else if(xpos == 800) // Increment y and reset x at end of line
22
                 begin
23
                      ypos <= ypos + 1;</pre>
24
                      xpos <= 0;</pre>
25
                  end
26
             else // Otherwise increment x
                 xpos \le xpos + 1;
28
29
         end
30
    assign x = xpos;
31
    assign y = ypos;
32
33
    endmodule
34
```

7.1.11 Drawer

```
module Drawer(
        input logic clk,
2
        input logic[3:0] box_r, box_g, box_b,
3
        input logic[9:0] x, y, box_x, box_y, box_size,
        output logic[3:0] r, g, b
5
    );
6
    always_ff @(posedge clk)
        // Draw black if not in box pixels
9
        if (x > 640 || y > 480 || x < box_x || y < box_y || x > box_x + box_size || y
10

→ > box_y + box_size)

             begin
11
                 r \le 4'b0;
12
                 g \le 4'b0;
                 b \le 4'b0;
14
             end
15
        else // Draw box
16
             begin
17
                 r <= box_r;
18
                 g \le box_g;
19
                 b <= box_b;</pre>
             end
22
23
    endmodule
```