

KALAIMANI L

Electronics & Communication Engg. Graduate — Full Stack Developer

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Profile Summary

Motivated Electronics and Communication Engineering graduate with strong foundational knowledge in core electronics and full-stack development. Trained in both front-end and back-end technologies with hands-on experience in developing scalable web applications. Skilled in Java, Spring Boot, and React with strong problem-solving and debugging abilities. A quick learner with good analytical skills and a collaborative team player.

Education

B.E. in Electronics & Communication Engineering	2021 – 2025
A.K.T Memorial College of Engineering & Technology	CGPA: 7.9/10

Technical Skills

Hardware & Verification: Digital Electronics, Verilog (Xilinx Vivado, EDA Playgraound)

Frontend Development: HTML5, CSS3, JavaScript, React.js, Next.js, Redux, React Router,

Backend Development: Java, Spring Boot, Node.js, RESTful APIs

Databases: MySQL

Developer Tools & Platforms: Git, GitHub, VS Code, Postman (API Testing)

Experience & Training

Full Stack Development– FITA Academy, Chennai | August 2025

- Trained in frontend (HTML, CSS, JavaScript, React.js), backend (Java, Spring Boot), and databases (MySQL) with hands-on projects.

Intern - Jyotsna and Kevin Technologies, Tiruvannamalai | October 2024

- Created responsive web apps using HTML5 and CSS3, ensuring cross device compatibility and collaborating on UI performance improvements.

Intern - CodeBind Technologies, Chennai | July 2023

- Worked on IoT systems handling real-time sensor data, developing problem-solving and system integration skills bridging hardware and software components.

Achievements

- Secured **First Prize** in the *Debugit Hackathon* conducted by Edveon Technology under the **Naan Mudhalvan** initiative.
- Awarded “**Best Project Model**” in a college-level innovation contest.

Projects

MyStore – Online E-Commerce Website

- Built a full-stack e-commerce platform with product listing, cart management, order processing, and user friendly UI.

Task Manager Web Application

- Designed and implemented a CRUD-based task management app enabling users to create, update, delete, and track daily tasks efficiently.

RCA & BCD Adder (Verilog, Xilinx Vivado)

- Designed, implemented, and verified 4-bit Ripple Carry Adder and BCD Adder in Verilog using Xilinx Vivado with testbench validation.

IoT-Based Real-Time Health Monitoring System

- Designed an IoT-based system to monitor elderly patients' health parameters in real time and display data via a mobile/web application.