```
-- Company:
-- Engineer:
-- Create Date: 02/01/2024 02:16:51 PM
-- Design Name:
-- Module Name: compunit tb - Behavioral
-- Project Name:
-- Target Devices:
-- Tool Versions:
-- Description:
-- Dependencies:
-- Revision:
-- Revision 0.01 - File Created
-- Additional Comments:
library IEEE;
use IEEE.STD LOGIC 1164.ALL;
use std.env.finish;
-- Uncomment the following library declaration if using
-- arithmetic functions with Signed or Unsigned values
--use IEEE.NUMERIC STD.ALL;
-- Uncomment the following library declaration if instantiating
-- any Xilinx leaf cells in this code.
--library UNISIM;
--use UNISIM.VComponents.all;
entity compunit tb is
```

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end compunit tb;
architecture Behavioral of compunit tb is
signal a ,b: std logic vector ( dw-1 downto 0);
signal s: std logic vector(dw downto 0);
signal o: std logic vector (dw-1 downto 0);
begin
test: entity work.compunit(Behavioral)
port map(a => a,b => b,s => s, o=>o);
ts: process
begin
a<= "1110";b<= "1101";
s \le "00000"; wait for 300ps;
s \le "00001"; wait for 300ps;
s \le "00010"; wait for 300ps;
s \le "00011"; wait for 300ps;
s \le "00100"; wait for 300ps;
s \le "00101"; wait for 300ps;
s \le "00110"; wait for 300ps;
s \le "00111"; wait for 300ps;
s \le "01000"; wait for 300ps;
s \le "01001"; wait for 300ps;
s \le "01010"; wait for 300ps;
s \le "01011"; wait for 300ps;
s \le "01100"; wait for 300ps;
s \le "01101"; wait for 300ps;
s \le "01110"; wait for 300ps;
s \le "01111"; wait for 300ps;
s \le "10000"; wait for 300ps;
s \le "10001"; wait for 300ps;
s \le "10010"; wait for 300ps;
s \le "10011"; wait for 300ps;
s \le "10100"; wait for 300ps;
```

generic( dw: integer:=4);

```
a \le "1110"; b \le "1011";
s \le "00000"; wait for 300ps;
s \le "00001"; wait for 300ps;
s \le "00010"; wait for 300ps;
s \le "00011"; wait for 300ps;
s \le "00100"; wait for 300ps;
s \le "00101"; wait for 300ps;
s \le "00110"; wait for 300ps;
s \le "00111"; wait for 300ps;
s \le "01000"; wait for 300ps;
s \le "01001"; wait for 300ps;
s \le "01010"; wait for 300ps;
s \le "01011"; wait for 300ps;
s \le "01100"; wait for 300ps;
s \le "01101"; wait for 300ps;
s \le "01110"; wait for 300ps;
s \le "01111"; wait for 300ps;
s \le "10000"; wait for 300ps;
s<= "10001"; wait for 300ps;</pre>
s \le "10010"; wait for 300ps;
s \le "10011"; wait for 300ps;
s \le "10100"; wait for 300ps;
s \le "10101"; wait for 300ps;
finish;
end process;
```

 $s \le "10101";$  wait for 300ps;

end Behavioral;