



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Fourth Semester July 2016

Electronics & Communication Engineering



J
16 578270

Name of the Student : **HARISH.N**

Father's / Mother's Name : **NARASIMHA MURTHY N**

Seat No. : **1SJ14EC037**

Name of the College : **S.J.C. INSTITUTE OF TECHNOLOGY, CHICKBALLAPUR**

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT41	Engineering Mathematics - IV	100	35	84	25	-	22	125	50	106	Pass
2	10ES42	Microcontrollers	100	35	61	25	-	23	125	50	84	Pass
3	10ES43	Control Systems	100	35	72	25	-	25	125	50	97	Pass
4	10EC44	Signals & Systems	100	35	54	25	-	23	125	50	77	Pass
5	10EC45	Fundamentals of HDL	100	35	77	25	-	25	125	50	102	Pass
6	10EC46	Linear ICs & Applications	100	35	45	25	-	25	125	50	70	Pass
7	10ESL47	Microcontrollers Lab	50	20	49	25	12	23	75	32	72	Pass
8	10ECL48	HDL Lab	50	20	50	25	12	25	75	32	75	Pass
GRAND TOTAL									900	364	683	

Total Marks Obtained (in words) : **SIX HUNDRED AND EIGHTY THREE ONLY**

Result of the Semester : **FIRST CLASS WITH DISTINCTION**

DATE : **NOV 07, 2016**

A - Absent NE - Not Eligible



Registrar (Evaluation)