



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Fifth Semester January 2017

Electronics & Communication Engineering



J
17 268983

Name of the Student : HARISH.N

Father's / Mother's Name : NARASIMHA MURTHY N

Seat No. : 1SJ14EC037

Name of the College : S.J.C. INSTITUTE OF TECHNOLOGY, CHICKBALLAPUR

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10AL51	Management & Entrepreneurship	100	35	75	25	-	25	125	50	100	Pass
2	10EC52	Digital Signal Processing	100	35	69	25	-	23	125	50	92	Pass
3	10EC53	Analog Communication	100	35	76	25	-	21	125	50	97	Pass
4	10EC54	Microwaves & Radar	100	35	54	25	-	25	125	50	79	Pass
5	10EC55	Information Theory & Coding	100	35	81	25	-	24	125	50	105	Pass
6	10EC56	Fundamentals of CMOS VLSI	100	35	74	25	-	21	125	50	95	Pass
7	10ECL57	DSP Lab.	50	20	49	25	12	25	75	32	74	Pass
8	10ECL58	Analog Communication Lab. + LIC Lab.	50	20	49	25	12	23	75	32	72	Pass
GRAND TOTAL									900	364	714	

Total Marks Obtained (in words) : SEVEN HUNDRED AND FOURTEEN ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

DATE : JUL 18, 2017

A - Absent NE - Not Eligible

Amrithi

Registrar (Evaluation)