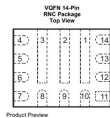
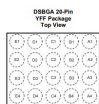
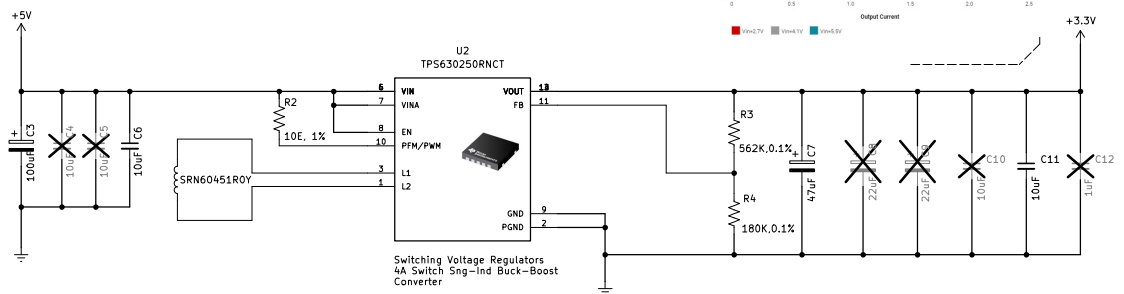
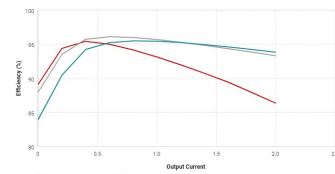
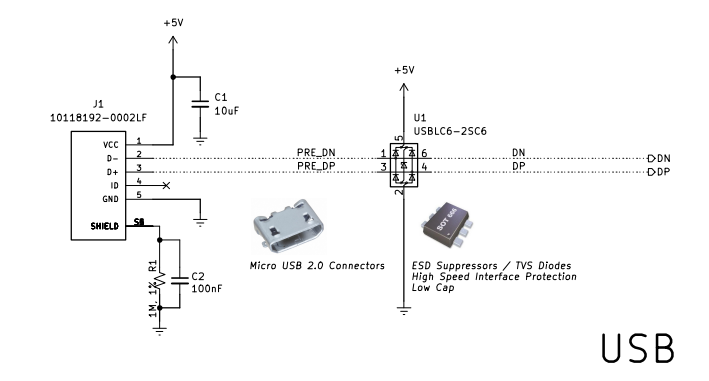
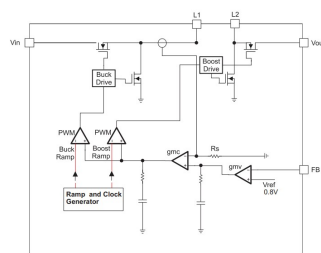


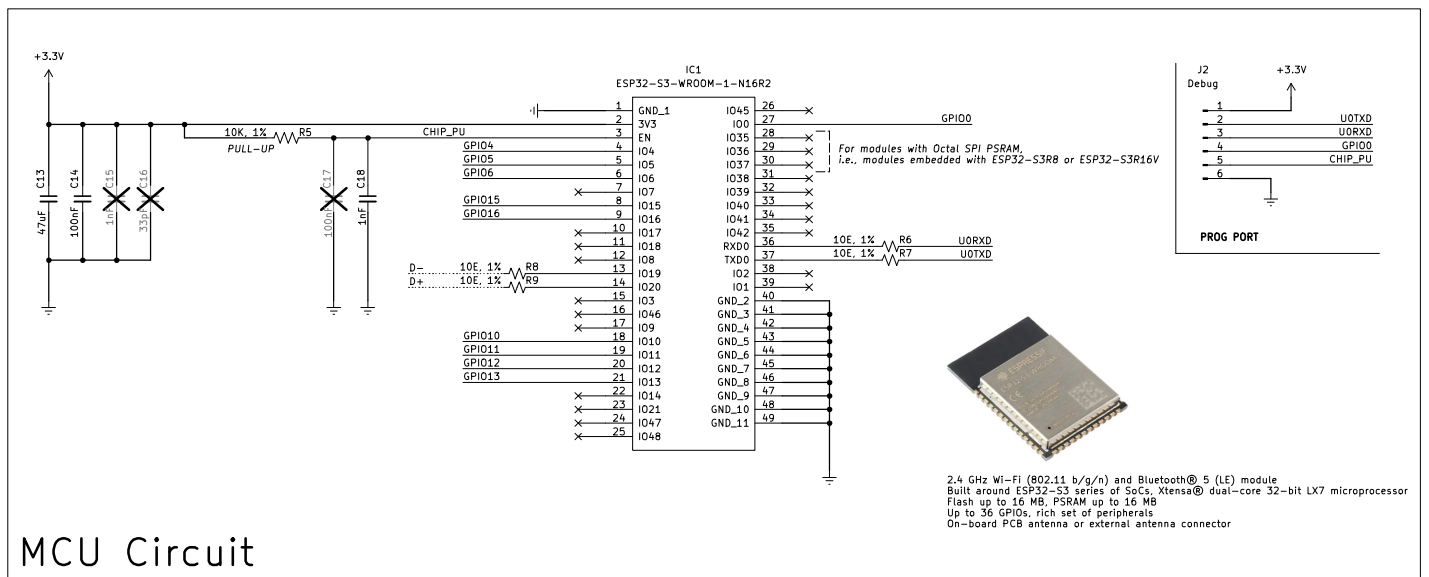
- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole



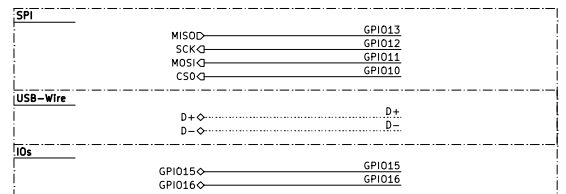
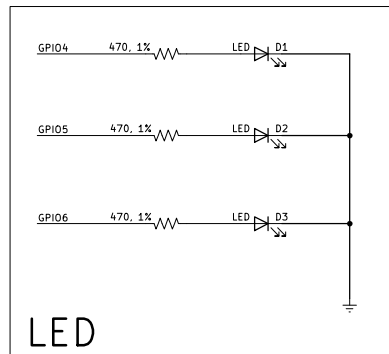
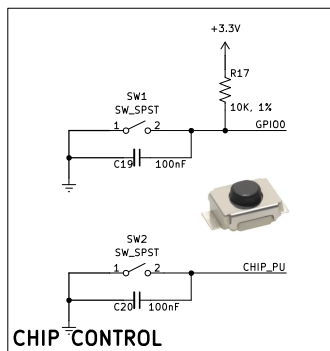
Product Preview

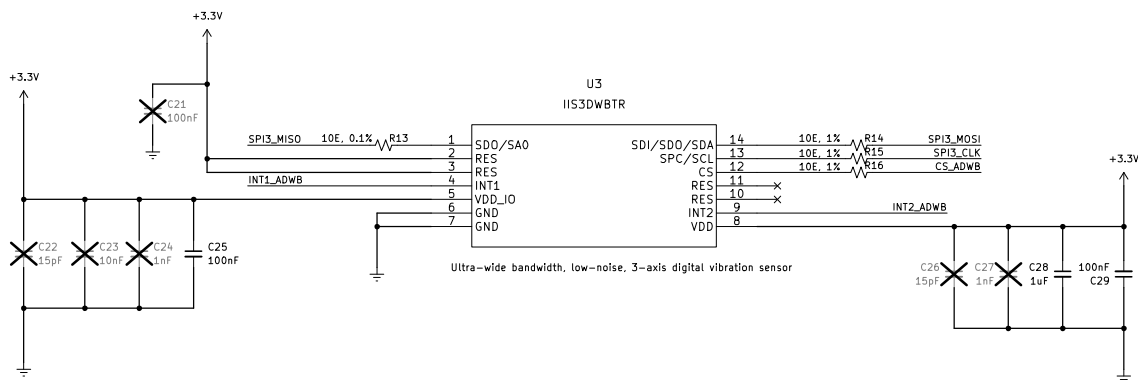
NAME	PIN	SSBGA	RNC	IO	DESCRIPTION
VOUT	A1 A2 A3	12, 13, 14	PWR		Buck-Boost converter output.
FB	A4	15	IN		Voltage feedback of adjustable version, must be connected to VOUT on fixed output voltage versions.
L2	B1 B2 B3	1	PWR		Connection for inductor.
PTMPWM	B4	10	IN		set low for PWM mode, set high for forced PWM mode. It must not be left floating.
PGND	C1 C2 C3	2	PWR		Power Ground.
AGND	C4	9	PWR		Analog Ground.
L1	D1 D2 D3	3	PWR		Connection for inductor.
EN	D4	5	IN		Enable input. Set high to enable and low to disable. It must not be left floating.
VIN	E1 E2 E3	4, 5, 6	PWR		Supply voltage for power stage.
VINA	E4	7	PWR		Supply voltage for control stage.





MCU Circuit

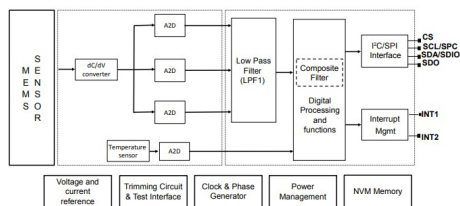




LGA-14L
(2.5 x 3.0 x 0.83 mm) typ.

Table 2. Default pin definition			
Pin#	Name	Function	Recommended connection
1	SDIO/SPI	SPI 3-wire interface serial data output (SDIO) / Serial peripheral interface data output (SPI)	Input without pull-up
2	RES	Reset	Input without pull-up
3	RES	Reset	Input without pull-up
4	INT1	Programmable interrupt #1	Input without pull-up
5	VDD_IO	Power supply for I/O pins	Connect to VDD, GND or GND
6	GND	Ground	Connect to VDD, GND or GND
7	GND	Ground	Connect to VDD, GND or GND
8	VDD	Power supply	Connect to VDD, GND or GND
9	INT2	Programmable interrupt #2	Input without pull-up
10	RES	Reset	Input without pull-up
11	RES	Reset	Input without pull-up
12	CS	PCSPI mode selection	Input without pull-up
13	SPC/SCL	SPI 3-wire interface serial data output (SDIO) / Serial peripheral interface data output (SPI)	Input without pull-up
14	SDIO/SPI	SPI 3-wire interface serial data output (SDIO) / Serial peripheral interface data output (SPI)	Input without pull-up

Figure 9. Accelerometer architecture



Peripherals	
SPI3_CLK	→ SPI3_CLK
SPI3_MOSI	→ SPI3_MOSI
SPI3_MISO	→ SPI3_MISO
CS_ADWB	→ CS_ADWB
INT1_ADWB	→ INT1_ADWB
INT2_ADWB	→ INT2_ADWB