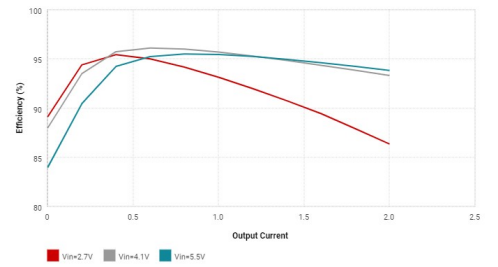
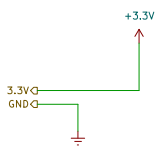


Switching Voltage Regulators 4A Switch
Sng-Ind Buck-Boost Converter



- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole

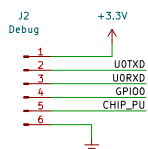


MISO → GPIO13
SCK → GPIO12
MOSI → GPIO11
CS → GPIO10

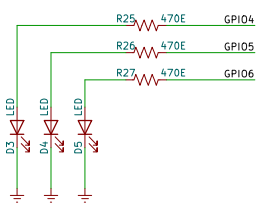
SPI

IO14 → GPIO14
IO15 → GPIO15
IO16 → GPIO16
TXD → GPIO17
RXD → GPIO18
GPIO1 → GPIO1
GPIO2 → GPIO2
GPIO7 → GPIO7

IOs



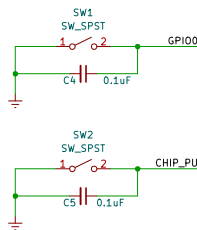
PROG PORT



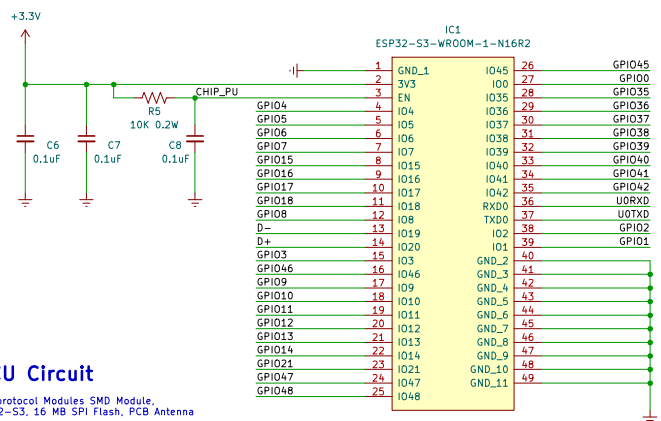
LED

D+ → D+
D- → D-

USB-Wire

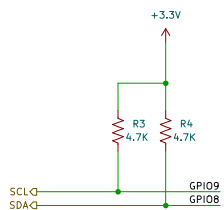


RST,BOOT Button



MCU Circuit

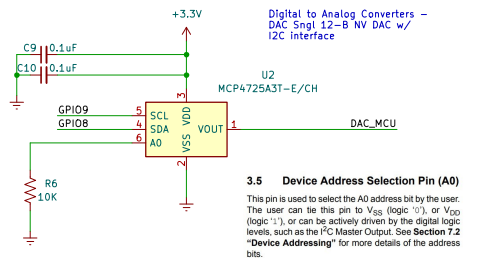
Multiprotocol Modules SMD Module,
ESP32-S3, 16 MB SPI Flash, PCB Antenna



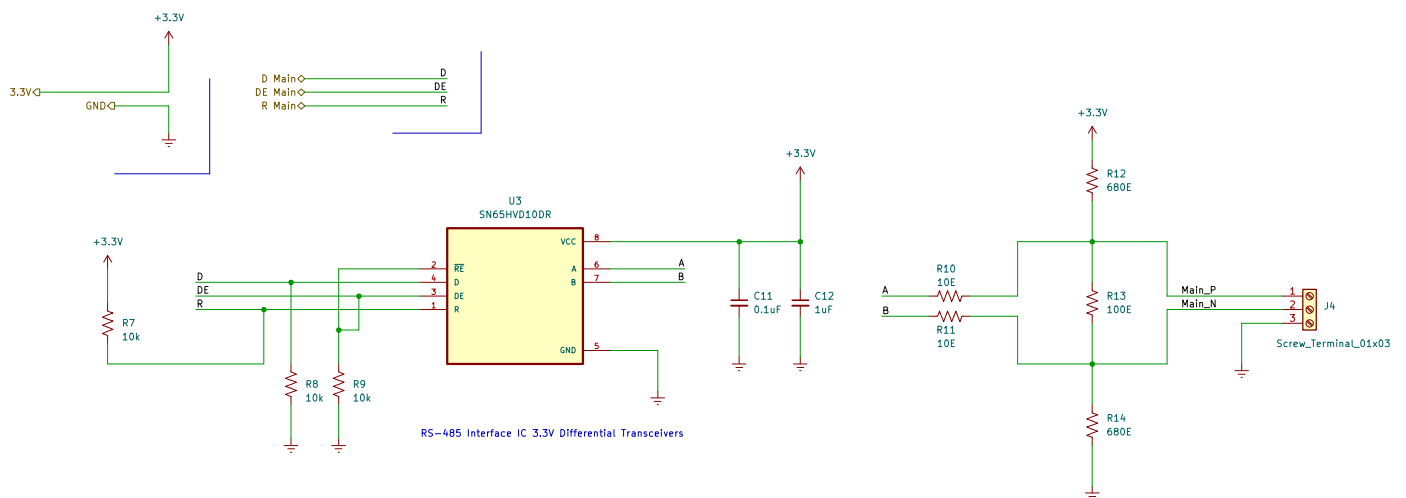
I2C

DAC_MCU → DAC_MCU

DAC



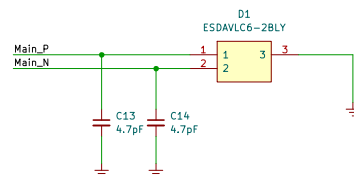
3.5 Device Address Selection Pin (A0)
This pin is used to select the A0 address bit by the user. The user can tie this pin to V_{SS} (logic '0'), or V_{DD} (logic '1'), or can be actively driven by the digital logic levels, such as the I²C Master Output. See Section 7.2 "Device Addressing" for more details of the address bits.



Driver Functions

INPUT	ENABLE	OUTPUTS		FUNCTION
D	DE	A	B	
H	H	H	L	Actively drive bus High
L	H	L	H	Actively drive bus Low
X	L	Z	Z	Driver disabled
X	OPEN	Z	Z	Driver disabled by default
OPEN	H	H	L	Actively drive bus High by default

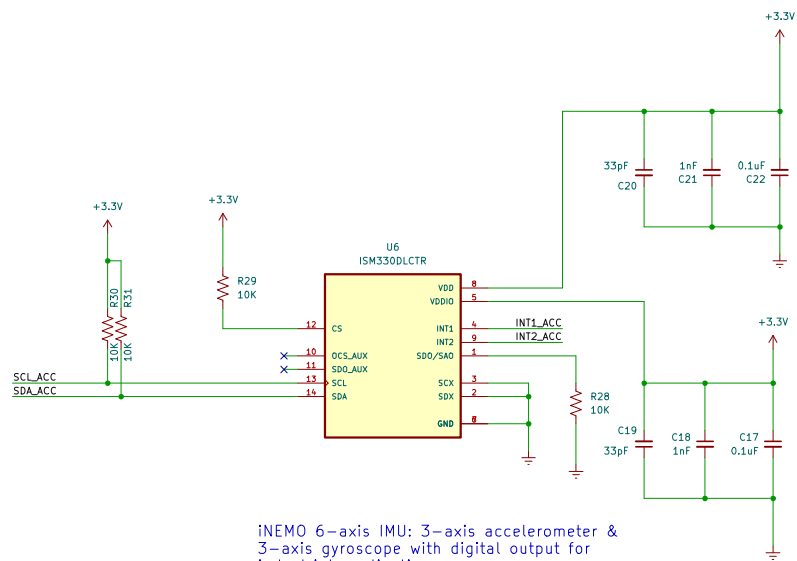
(1) H = high level; L = low level; Z = high impedance; X = irrelevant; ? = indeterminate



Receiver Functions

DIFFERENTIAL INPUT $V_{ID} = V_A - V_B$	ENABLE RE	OUTPUT R	FUNCTION
$V_{ID} > V_{IT+}$	L	H	Receive valid bus High
$V_{IT-} < V_{ID} < V_{IT+}$	L	?	Indeterminate bus state
$V_{ID} < V_{IT-}$	L	L	Receive valid bus Low
X	H	Z	Receiver disabled
X	OPEN	Z	Receiver disabled by default
Open-circuit bus	L	H	Fail-safe high output
Short-circuit bus	L	H	Fail-safe high output

(1) H = high level; L = low level; Z = high impedance; X = irrelevant; ? = indeterminate



iNEMO 6-axis IMU: 3-axis accelerometer & 3-axis gyroscope with digital output for industrial applications

LSb value is 0 (address b1101010, or, h0x6A)

