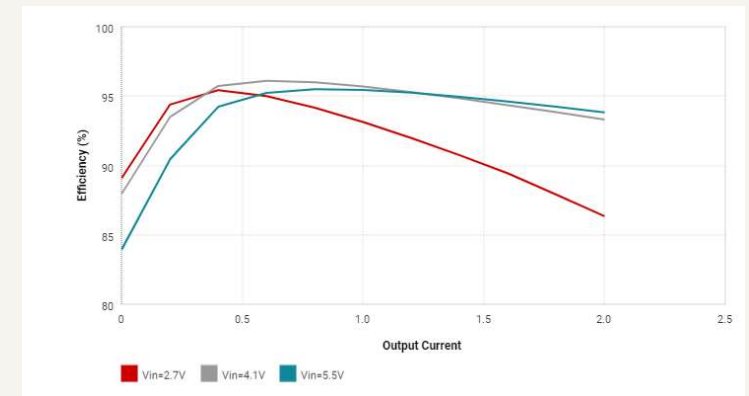


Switching Voltage Regulators 4A Switch
Sng-Ind Buck-Boost Converter



- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole

@Copyright & Reserved
Property of NOVO AI
<https://wm.com/>
Designed by Novo AI

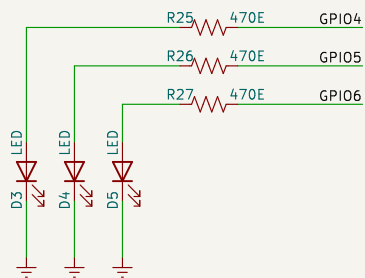
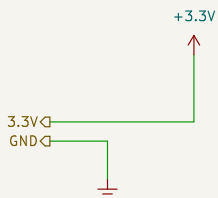
Novo AI

Sheet: /3.3V PSU & Input/
File: 3V3 PSU.kicad_sch

Title: AVA V1.2

Size: A4 Date: 2023-07-02
KiCad E.D.A. kicad 7.0.9

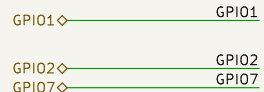
Rev: 02
Id: 2/8



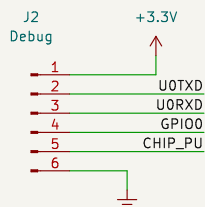
LED



SPI



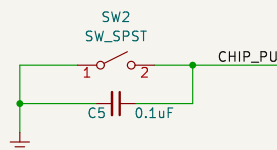
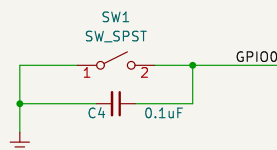
IOs



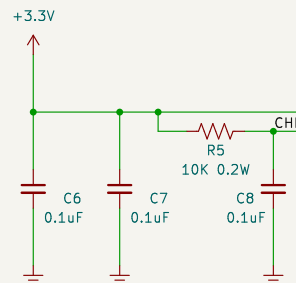
PROG PORT



USB-Wire

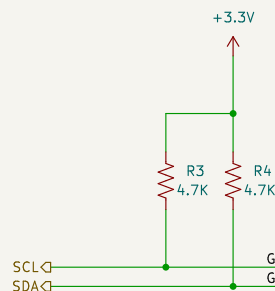


RST,BOOT Button

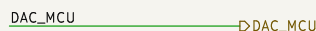


MCU Circuit

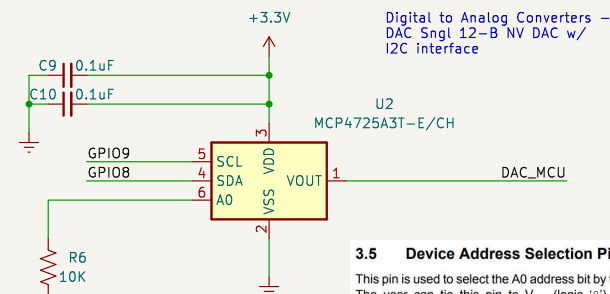
Multiprotocol Modules SMD Module,
ESP32-S3, 16 MB SPI Flash, PCB Antenna



I2C



DAC



3.5 Device Address Selection Pin (A0)

This pin is used to select the A0 address bit by the user. The user can tie this pin to V_{SS} (logic '0'), or V_{DD} (logic '1'), or can be actively driven by the digital logic levels, such as the I²C Master Output. See **Section 7.2 "Device Addressing"** for more details of the address bits.

©Copyright & Reserved
Property of NOVO AI
<https://wm.com/>
Designed by Novo AI

Sheet: /ESP32 S3/
File: esp32s3.kicad_sch

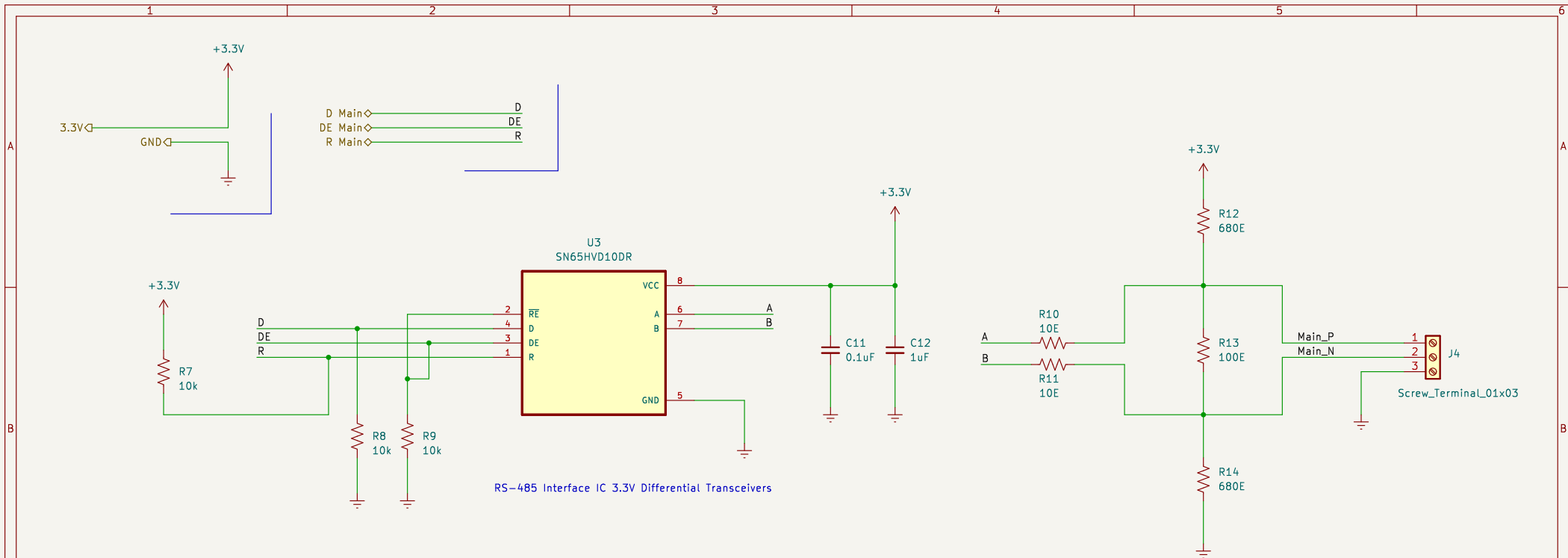
Title: AVA V1.2

Size: A4 Date: 2023-07-02

KiCad E.D.A. kicad 7.0.9

Rev: 02

Id: 3/8



Driver Functions

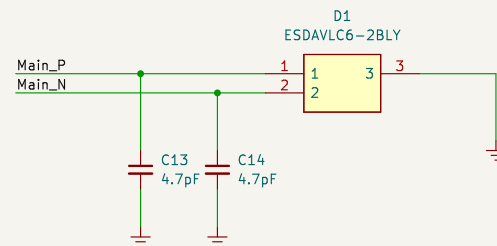
INPUT	ENABLE	OUTPUTS		FUNCTION
D	DE	A	B	
H	H	H	L	Actively drive bus High
L	H	L	H	Actively drive bus Low
X	L	Z	Z	Driver disabled
X	OPEN	Z	Z	Driver disabled by default
OPEN	H	H	L	Actively drive bus High by default

(1) H = high level; L = low level; Z = high impedance; X = irrelevant; ? = indeterminate

Receiver Functions

DIFFERENTIAL INPUT $V_{ID} = V_A - V_B$	ENABLE RE	OUTPUT R	FUNCTION
$V_{ID} > V_{IT+}$	L	H	Receive valid bus High
$V_{IT-} < V_{ID} < V_{IT+}$	L	?	Indeterminate bus state
$V_{ID} < V_{IT-}$	L	L	Receive valid bus Low
X	H	Z	Receiver disabled
X	OPEN	Z	Receiver disabled by default
Open-circuit bus	L	H	Fail-safe high output
Short-circuit bus	L	H	Fail-safe high output

(1) H = high level; L = low level; Z = high impedance; X = irrelevant; ? = indeterminate



@Copyright & Reserved
Property of NOVO AI
<https://wm.com/>
Designed by Novo AI
Novo AI

Sheet: /Transceivers/
File: transceivers.kicad_sch

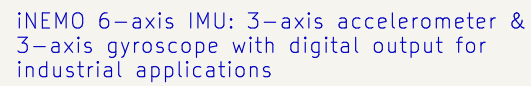
Title: AVA V1.2

Size: A4 Date: 2023-07-02

KiCad E.D.A. kicad 7.0.9

Rev: 02

Id: 4/8



SCL_ACC▷	SCL_ACC
SDA_ACC◊	SDA_ACC
INT1_ACC◁	INT1_ACC
INT2_ACC◁	INT2_ACC



Sheet: /Accelerometer Sensor - 2/
File: Accelerometer Sensor.kicad_sch

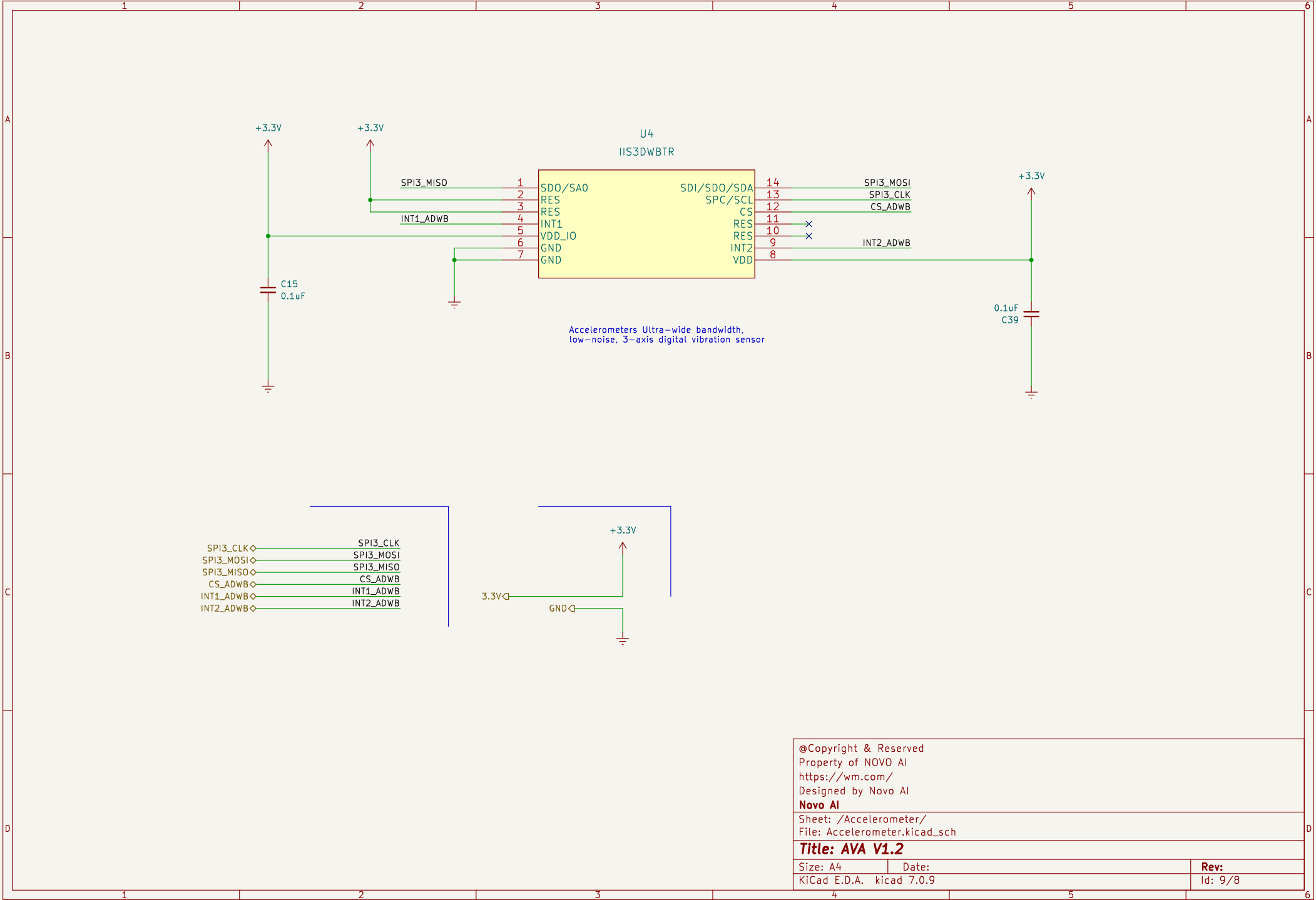
Title: AVA V1.2

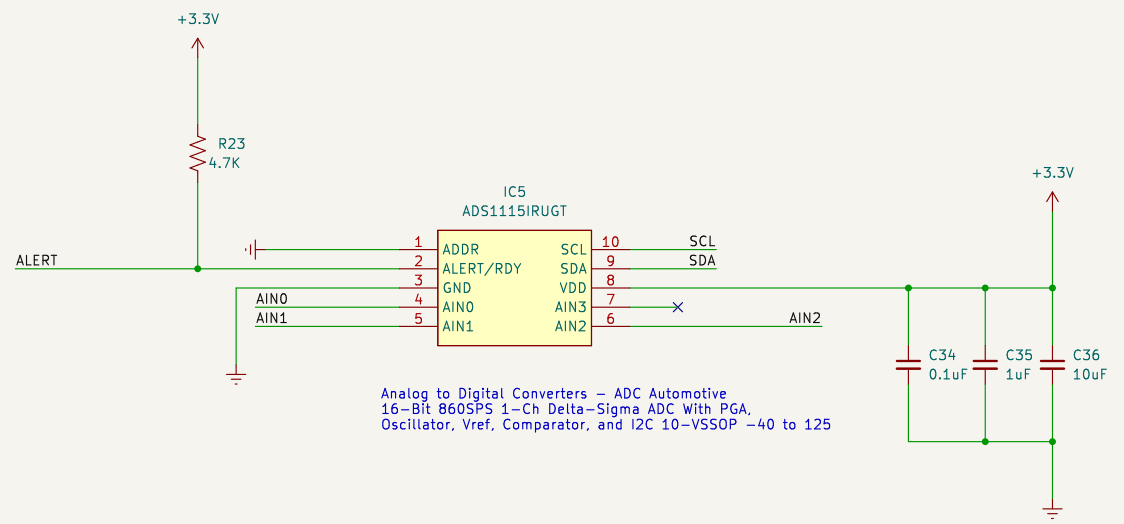
Size: A4	Date: 2023-07-02
----------	------------------

Size: A4	Date: 11/01/2025
KiCad E.D.A.	kicad 7.0.9

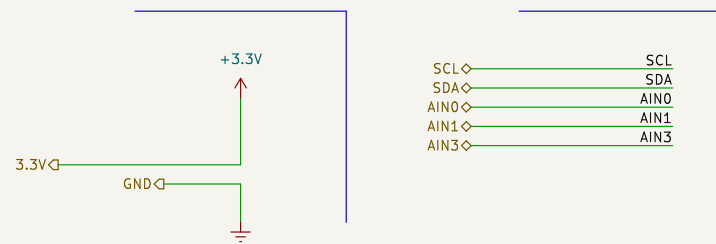
Rev: 02

Id: 6/8





Analog to Digital Converters – ADC Automotive
16-Bit 860SPS 1-Ch Delta-Sigma ADC With PGA,
Oscillator, Vref, Comparator, and I2C 10-VSSOP -40 to 125



SCL ◇ SCL
SDA ◇ SDA
AIN0 ◇ AIN0
AIN1 ◇ AIN1
AIN3 ◇ AIN3

©Copyright & Reserved Property of NOVO AI https://wm.com/ Designed by Novo AI Novo AI		
Sheet: /ADC CONVERTER/ File: ADC CONVERTER.kicad_sch		
Title: AVA V1.2		
Size: A4	Date: 2023-07-02	Rev: 02
KiCad E.D.A. kicad 7.0.9		Id: 9/8