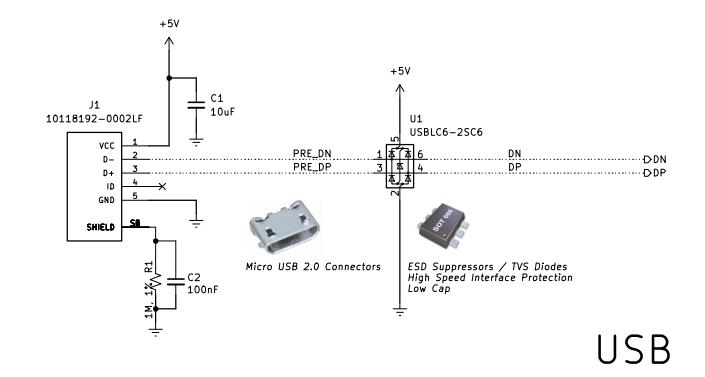
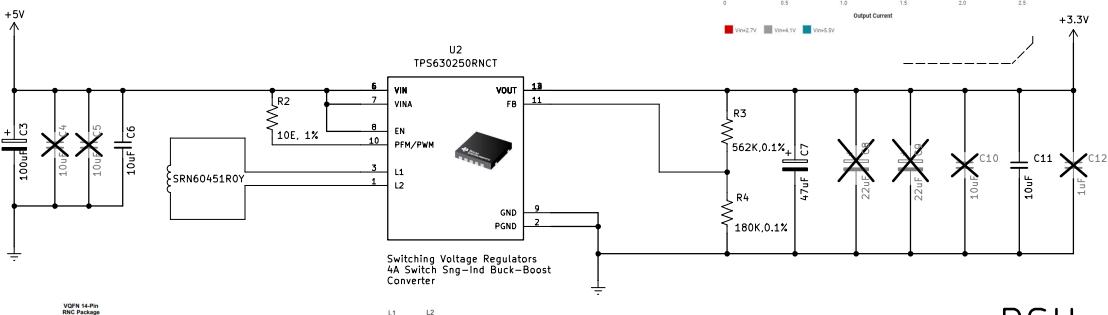
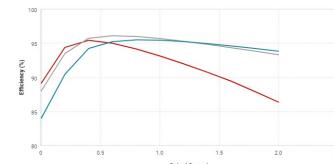


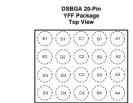
- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole



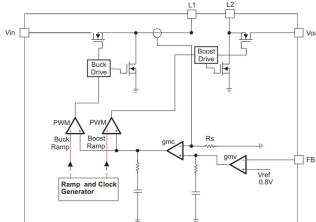
USB

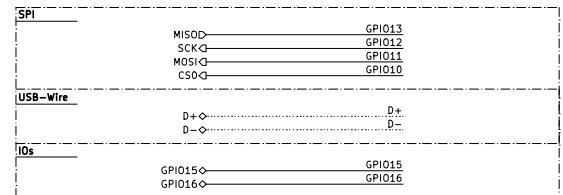
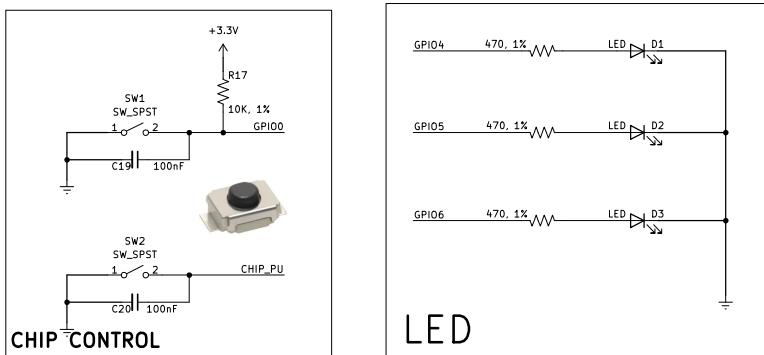
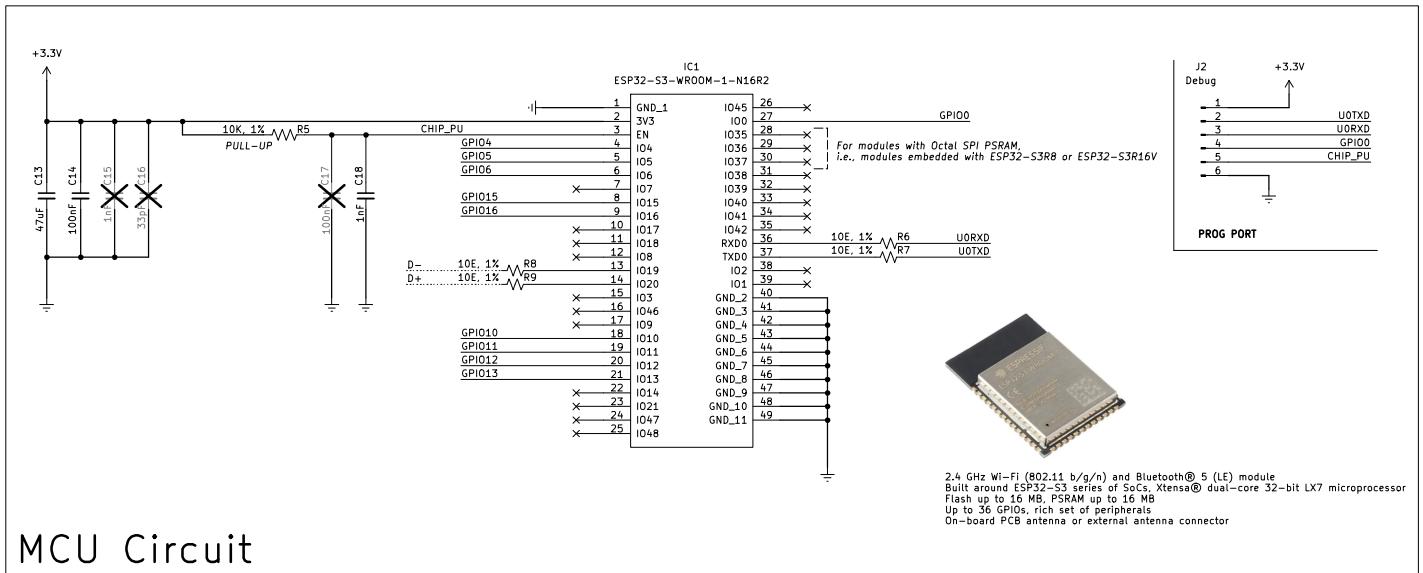


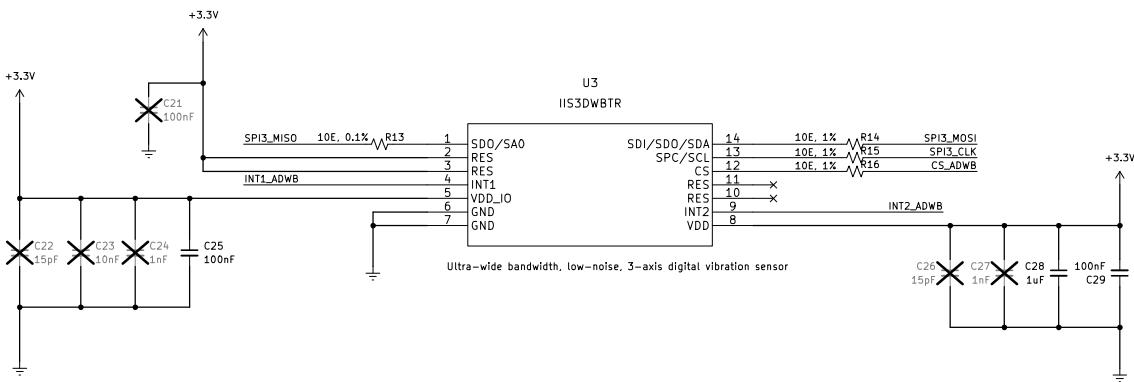
PSU



PIN	DESCR.	ID	DESCRIPTION
VOUT	DBGA 32-Pin YFB Package Top View	12, 13, 14	Buck-Boost converter output
FB	DBGA 32-Pin YFB Package Top View	A4	11 IN Voltage feedback of adjustable version, must be connected to VOUT on fixed output voltage
L2	WDFP 14-Pin BNC Package Top View	B1, B2, B3	1 PWR Connection for Inductor
PFM/PWM	WDFP 14-Pin BNC Package Top View	C1, C2, C3	10 Set for PFM mode or 11 for PWM mode, set high for forced PWM mode. It must not be left floating
PGND	WDFP 14-Pin BNC Package Top View	C4	9 PWR Analog Ground
EN	WDFP 14-Pin BNC Package Top View	D1, D2, D3	5 PWM Enable Input. Set high to enable and low to disable. It must not be left floating
VIN	WDFP 14-Pin BNC Package Top View	E1, E2, E3	4, 5, 6 PWR Supply voltage for power stage
VINA	WDFP 14-Pin BNC Package Top View	E4	7 PWR Supply voltage for control stage

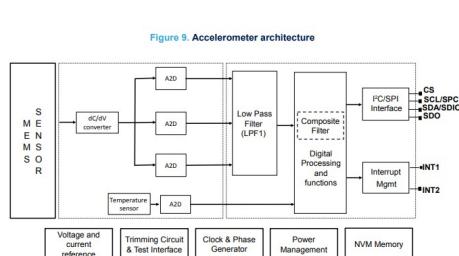






LGA-14L
(2.5 x 3.0 x 0.83 mm) typ.

Pin	Name	Description	Recommended connect state
1	SDO/SDA	SPI 4-wire mode serial data output (SDO) I²C least significant bit of the device address Push-up or pull-down to SDO_1V_1kpin is Application-specific	Push-up or pull-down to SDO_1V_1kpin is Application-specific
2	RES	Reserved	Connect to VDDIO or GND
3	RES	Reserved	Connect to VDDIO or GND
4	INT1	Programmable interrupt #1 Input with pull-down	Must be set to low until unconnected. After device power-up, connection is application specific
5	VDDIO	Power supply for IO pin	
6	GND	Ground	
7	GND	Ground	
8	VDD	Power supply	
9	INT2	Programmable interrupt #2 Output forced to GND	Application-specific
10	RES	Reserved	Connect to VDDIO or logic pin electrically unconnected and soldered to PCB
11	RES	Reserved	Connect to VDDIO or logic pin electrically unconnected and soldered to PCB
12	CS	I²C/SPI mode selection 1: SPI mode / I²C communication 0: I²C mode / SPI mode Pull-up disabled FINE I²C, DS4542H+ 0: Reg 0x8	Application-specific
13	SPI3_SDO	SPI 3-wire mode clock (SPO) I²C serial data input (SDI)	Input without pull-up
14	SDI/SDO/SDA	I²C serial data serial data output (SDO) I²C serial data (SDA)	Application-specific



Peripherals	
SPI3_CLK	◊ SPI3_CLK
SPI3_MOSI	◊ SPI3_MOSI
SPI3_MISO	◊ SPI3_MISO
CS_ADWB	◊ CS_ADWB
INT1_ADWB	◊ INT1_ADWB
INT2_ADWB	◊ INT2_ADWB