

Courses that were helpful

1. Digital Systems (EE)
2. Digital IC Design (EE) - Odd sem - <http://www.ee.iitm.ac.in/~janakiraman/teaching.html>
3. Advanced Topics in VLSI (EE) - Even sem  
<http://www.ee.iitm.ac.in/~janakiraman/teaching.html>
4. Computer Architecture - Kamakoti Sir lectures on Youtube
5. Digital Systems Testing and Testable Design (CS) - Kamakoti Sir - Odd Sem
6. CAD for VLSI (CS) - Kamakoti Sir - Even Sem

So, as far as preparation is considered I followed the following links and books

You can find almost all textbooks here →→→ <http://libgen.io/>

### Books

1. Digital Design by Morris Mano - very important, don't leave any chapter.
2. CMOS VLSI Design by Weste and Harris - reference book for Digital IC Design course
3. Digital Design and Computer Architecture by David Harris, Sarah Harris - a very good blend of digital design and computer architecture
4. Digital System Testing and Testable Design - Use this book as a reference if you want to read upon testing of digital circuits
5. <https://www.amazon.in/Cracking-Digital-VLSI-Verification-Interview-ebook/dp/B01CZ0Z08E> - A very good book for basics, if you are looking for a future in digital - helped me
6. Verilog HDL: A Guide to Digital Design and Synthesis by Samir Palnitkar

**Links** (Including some links to videos which have some good explanation for a particular topic)

1. <http://www.asic-world.com/> - Very good website - A goto place for almost all the stuff
2. <http://www.vlsi-expert.com/p/vlsi-basic.html> - A very good blog
3. <https://www.edn.com/design/analog/4371393/Understanding-the-basics-of-setup-and-hold-time> - Setup and Hold Time
4. <https://www.youtube.com/user/nesoacademy> - For basics
5. <https://www.youtube.com/watch?v=zj4NqHNEZ5M> - Clock gating
6. <http://inst.eecs.berkeley.edu/~cs150/fa08/Documents/Nets.pdf> - Diff b/w wire and reg
7. <http://inst.eecs.berkeley.edu/~cs150/fa08/Documents/>
8. <http://vlsi.pro/verilog-timescales/> - Timescale in verilog
9. [https://www.mikrocontroller.net/attachment/177198/Clock\\_Dividers\\_Made\\_Easy.pdf](https://www.mikrocontroller.net/attachment/177198/Clock_Dividers_Made_Easy.pdf) - for /3, /5 clock dividers
10. <https://www.geeksforgeeks.org/mealy-and-moore-machines/> - Mealy and Moore
11. <https://www.electronics-tutorials.ws/> - Basics for all general topics. It's essential for you have a general idea of these things
12. <https://boredzo.org/pointers/> - Pointers in C - topic asked in almost all the tests
13. Just keep a look out for blogs, there are a lot of good blogs for VLSI

All the placement tests are summarized in →→→ **Placement\_tests**

Interview questions are summarized in →→→ **Interview\_questions**

Some random questions that I came up with are listed in →→→ **Core\_questions**

Some previous interview questions passed onto me by seniors →→→ **AllinoneVLSI.pdf**

**End note:** At the end of the day, how much ever you prepare, just be thorough with the basics, because every interview starts with a basic question and then build up on that.

That's it from my side. All the very best.

-- Harish Reddy