Assignment-1

1. what is the difference between General purpose system and embedded system?

Sol; what es general purpose system

-) what is embedded system

-> General purpose sim vis embadded system.

* A General purpose computer is one that given the appropriate app

* personal computers, encluding diest tops, laptops, notebooks are all examples of general purpose computers

Embedded systems-

* An embedded system is a computer system with a electroted function within a longer system.

ET & carculator.

* It consists of both * Hardwore * software

parameters	composer	embedded system
1. purpase	* muitapospare	* single tunctioned
a.spze of system		* small
3. power.	* more second	a legs
4 cost of system	n + costy	* cheap
5. Memory	a Higher memory	a lower memory
6. Performance	s fast 4 better performance	- fored runtime

7) USET INTERFACES + Keyboard, preploy, + BUTTON,
mouse, touch screen (gas, PR)

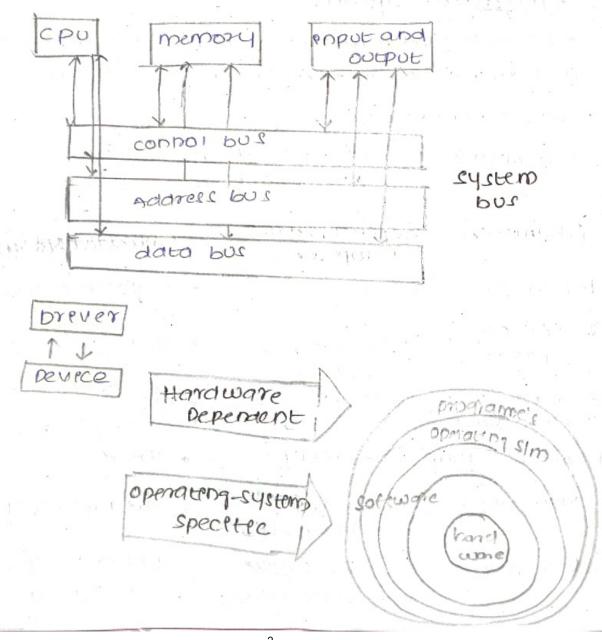
2. what are device drivers?

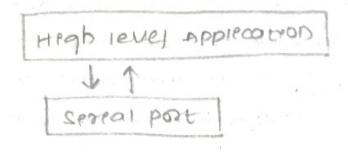
Sol: In computing, a device driver es a computer program that operates or conhois a particular type of device that is attached to a computer.

Applications & Appli

operating system

DENECE DRENGUE







3, How can bandware unamittand code that we written the embedded system?

embedded system. It enecutes inshoctrons sequentially, when you write code, it's eventually comprised
into machine code (bridgy inshoctrons) that the apu
can unanstand. These inshoctrons tell the apu what
operations to perform:

a. Memory: - Embedded systems have both program memory (where the code is stored) and data memory (for variables and nuntime data). The cpu fetches inshuctions from the program memory and operates on data in the data memory.

3. competation: you write code en abrigh-level programmeng language lete cor c++. The code es then competed using a competer that hanslates your code ento machine code (binary inshuttons). These machine code es what the cpu can execute directly.

4. Lenking: if your code envolved multiple source files or into ones, a lenker combines all the compiled code ento one broady file that can be loaded into memory and executed by the CPU.

the program memory of the embedded system, often through mechanisms the flashing or burning it into non-volatile memory (e.g., flash in the pob-)

one by one from the program memory. It performs operations leve another calculations, data manypulation, and connol flow based on these instructions.

To Ilo enterfaces? The embedded system entracts with the external world through Ilo enterfaces like GPIO (General purpose Input output), UART (Uneversal) Asynchronous Receiver / nansmitter), SPI (Sental perphenal interface), and others . your code includes enspections to read from write to these interfaces, allowing the hondware to communicate with sensors, octuators, despiays, and other perphenals.

In summary, hardware en embedded systems understands code through the mansiation of high-level code ento machine code, which is elevted by

the cpu. The code enteracts with memory and Ito enterfaces to perform specific tasks and control external devices based on your programs logic.

A. what es the difference between os and RTOS?

characteristic	9 RTOS	20
complexety	designed for ming- may overhead and reduced completify	more complex, supporting a wide variety of appli- cations and bard ware configuration
Application Doma in	commonly used in domains where timing and determinism are critical, such as appospace, automotive, industrial control simile, medical devices and robotics.	surrable for a broader range of apprecations, rociodeng destite computers, server mobele devices, and consumer electronics.
Resource of	preektos, vxworte, gnx, ecos, and kt- Lenux permesed for meni- nay resource usage and effectent memory nanogement.	windows, macos Lenux, and unex yarrants. optimized for efficient resource utilization and user experience
	trgh cost	cowcast

Determention betermentation wells
quaranteed
etiming and
doadleres

Real-time provides deterbehaviour ministic

behavrour

Non-determenistic
elecution with no
quarantees of temping
or deadlines

multitosicing and resource shoring.