

# Debuggers

Arvind S Raj  
(arvindsraj@am.amrita.edu)

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# Introduction

- Debugging assembly programs is hard: printf approach doesn't scale.
- Debuggers and few other CLI tools simplify task greatly.
- Agenda: Discuss few tools and some available extensions that simplify debugging programs.
- Will be useful for your assignments hopefully!

# What are debuggers?

- **Debuggers:** Allow inspecting process state during run-time.
- Hardware debuggers also exist. We discuss only software ones though. Eg: GDB.
- Enable stepping through each instruction in binary enabling close monitoring of process.
- Some also allow scripting during run-time: write programs to analyse the process!

# Outline

- ltrace for initial debugging.
- GNU debugger for more effective debugging of assembly programs.
- pwndbg: GDB configuration that provides front-end like and scripting capabilities. Improves GDB usage experience.
- Other GDB configuration and even front-ends exist! Feel free to experiment to find what you like.

# ltrace

- Library call tracer: Prints out dynamic library functions called along with their arguments.
- Useful to determine if library calls you make are not working as expected.
- Usage: **ltrace** < */path/to/binary* >.
- Provides more capabilities and highly configurable. Read man page of ltrace and ltrace.conf.

# GNU Debugger

- Debugger in GNU's binary utilities. Most popular Linux debugger.
- pwndbg: (*sic*) "A collection of python that makes debugging with gdb suck less". GDB utility for exploit development and RE. Improves GDB's user friendliness.
- Uses several third party libraries to improve debugging experience.

# GDB trivia

- Default AT&T syntax. Prefer Intel syntax: **set disassembly-flavor intel**. pwndbg uses this by default.
- Heavily configurable using **\$HOME/.gdbinit**.
- Support basic scripting functionality.
- pwndbg adds a Python scripting interface - get full power of Python libraries!

# Breakpoints

- **Breakpoint:** Pausing program execution at a point. Very useful for debugging if you know approximately where bug is caused.
- **Types:** Software and hardware. Latter requires hardware support and typically faster but only limited number possible.
- **b[reak] [addr]:** Set breakpoint at *addr*. If no address is specified, set breakpoint at next instruction to be executed.



# Breakpoints(cont.)

- **i[nfo] b[reakpoints]**: View current breakpoints.
- **dis[able] [num]**: Disable an active breakpoint. No argument  $\implies$  disable all.
- **en[able] [num]**: Enable a disabled breakpoint. No argument  $\implies$  enable all.
- **ig[nore] <num> <count>**: Ignore breakpoint *num* *count* times.

# Breakpoints(cont.)

- **del[ete] [num]**: Delete breakpoint *num*, if exists.  
No argument  $\implies$  delete all breakpoints.
- **tb[reak] [addr]**: Similar to *break* except breakpoint is deleted after one hit.
- **hb[reak] [addr]**: Similar to *break* but creates hardware breakpoint only.
- **thb[reak] [addr]**: Combination of *tbreak* and *hbreak*.

# Conditional Breakpoints

- **Conditional breakpoints:** Stop execution at a breakpoint if a specific condition is satisfied.
- Useful for isolating specific cases you are interested in (eg: when a variable has value 10).
- Creating a conditional breakpoint: **b <addr> if <expression>**. eg: b main if \$rdi==1.
- Adding condition to existing breakpoint: **cond[ition] <num> <expression>**. Eg: condition 7 \$rdi==1.

# Watchpoints

- **Watchpoints:** Similar to breakpoints for memory - interrupt execution if a particular memory location is accessed.
- Useful for monitoring changes made to variables in memory.
- **wa[tch]** <addr>: Interrupt if *addr* is written to.
- **rw[atc]** <addr>: Interrupt if *addr* is read.
- **awa[tch]** <addr>: Interrupt if *addr* is read or written to.

# Stepping through instructions

- **s[tep]i [num]**: Execute *num* (default: 1) instruction and stop at next. If current instruction is call, stop at first instruction of the function called.
- **n[ext]i [num]**: Similar to stepi except that if current instruction is call, the entire function is executed. Useful for skipping library functions.
- **fin[ish]**: Complete executing current function.

# Stepping through instructions

- **c[ontinue]**: Continue till next breakpoint/end of program.
- **nextjmp**: pwndbg command. Continue till next jmp instruction is encountered. Ignores breakpoints on the way.
- **nextc[all]**: pwndbg command. Continue till next call instruction is encountered. Ignores breakpoints on the way.

# Inspecting memory

- **x**: Display contents of memory in multiple formats(eg: binary), sizes(1, 2, 4 and 8 bytes) and types(eg: string).
- Eg: **x/10xg \$rdx**: Read 10 64-bit values from address *\$rdx* and print them as hex values.
- **tel[escope]**: pwndbg command. Display contents and dereferences them too. Eg: **tel \$rdx 10**.
- Program stack is also memory - above commands can be used and **stack/conte[xt] s[tack](pwndbg)**.

# Inspecting CPU registers

- **i[nfo] r[egisters]**: Display value of **all** registers.
- **i r \$r15 \$r12 \$r8**: Display values of registers r15, r12 and r8.
- **regs/conte[xt] r[egisters]**: pwndbg command. Display values of GPRs(RAX-RDX, RBP, RSP, RDI, RSI, RIP, R8-R15) and interpret contents(eg: dereference if pointer).
- Modify value of registers: **set \$rax = 1**.



# Viewing assembly instructions in binary

- **x/[0-9]+i <address>**. View N instructions starting at *address*. Registers, variables also allowed.
- **conte[xt] c[ode]**: pwndbg command; displays next few instructions that will be executed by evaluating conditional instructions, if any.
- **disas[semble] [address]**: View all instructions in a function. No argument  $\implies$  current function.
- **pdisass [address]**: pwndbg command. Similar to disas but coloured, nicer output.

# GDB

- Discussed few features: many more features exist but not important here.
- Has several front-ends: PEDA, GEF and even windowed DDD!
- Python scripting also supported.
- pwndbg features too not discussed in detail.
- Topics we discussed: not exhaustive but sufficient to find out more information.