



Place close to the chip

1. In this application, pin 40 (VREGIN) is the +5V input to the on chip +3.3V/1.8V regulator. Since the VREGIN is +5.0V, pin 39 (VCCD) becomes 3V3 output and supplies 3.3V to the VCCIO, VPLL, and VPHY.

2. The output of the on chip LDO +3.3V/1.8V regulator (+1.8V) drives pin 38, the FT232H core supply (VCORE) and pin 37, the VCCA.

