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OPEN-SOURCE SD.

Wednesday

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ASSIGNMENT # (2)

Logical Address also known as ~~virtual~~ virtual address is generated by CPU. Logical address is mapped into physical address in Memory by a hardware known as Memory Management Unit (MMU). MMU will generate a base register, its value is added to the logical address value and produce (sum) physical address. This process is called address binding.

Address binding can be done by following ways.

- Compile time
- Load time
- Run time-