Legend:
- DUT
- Re-Used UVCs
- Phase1 TBCs
- Phase2 TBCs
- TBCs with limited implementation Phase1 . Extended in Phase2.
- Scoreboard I/Ps
- Sequences
- Coverage Taps

Diagram labels:

Coverage Monitor

Config Struct

Monitor — MSNW Master

msnw_master_collector

master_xform

Monitor — MSNW Slave

msnw_slave_collector

MSNW

Clock & Reset Generator

Configuration inputs

MSNW2AXI
- MSNW Slave
- AXI Master
- MSNW Master
- AXI Slave

AXI

Scoreboard
- MSNW2AXI Checkers
- AXI2MSNW Checkers

Monitor — AXI Slave (AXI UVC)

Monitor — AXI Master (AXI UVC)

axi_slave_collector

axi_master_collector

axi_xform

Virtual Sequence Driver

Testcases