the Strings find Ction less

Pr' ted Pages: 2

Roll No.

Questions : 7

Sub. Code: 6 8 6 3

Exam. Code: 9 2 5

B.Engg. (Information Technology) 7th Semester

1125

COMPILER DESIGN Paper-IT-733

Time Allowed: Three Hours]

[Maximum Marks: 50

- **Note:** First question is compulsory. Attempt at least **two** questions each from Section A and Section B. All questions carry equal marks.
- I. (a) What are the various principle sources of code optimization?
 - (b) What do you mean by intermediate code? What are the advantages of generating it?
 - (c) Differentiate between synthesized and inherited attributes. 2
 - (d) Define handle. What are the issues involved in handle pruning?
 - (e) What do you mean by peephole optimization?

SECTION-A

- II. (a) What do you mean by phase of a compiler? Why the process of compilation is divided into various phases?
 - (b) Differentiate between token, lexeme and pattern. 5,5

6863/BHJ-32746

[Turn over

- b) y(n) = -0.1y(n-1) + 0.72y(n-2) + 0.7x(n) 0.252x(n-2)Explain the architecture of ADSP 21XX family of process
- Q.6a) What are the two main methods of IIR filter design? Also

III. Construct the LALR parsing table for the following grammau,

$$E \rightarrow E + T \mid T$$

$$T \to TF \mid F$$

$$F \rightarrow F^* |a|b$$
.

10

IV. What are predictive parsers? Write down the rules to construct the predictive parsing table. Consider the grammar:

$$S \rightarrow +SS \mid *SS \mid a$$

Is the grammar LL(1)? If yes, construct the predictive parsing table. Parse the string +*aaa.

SECTION-B

Let the synthesized attribute F.val give the value of the binary fraction V. generated by F in the grammar that follows:

$$F \rightarrow .L$$

$$L \rightarrow LB \mid B$$

$$B \rightarrow 0 \mid 1$$

Using only synthesized attributes, give a translation a scheme for the given grammar. Show translation of the input string .101 by decorating the parse tree.

VI. Describe in detail the common forms of intermediate code representation. Using each representation, represent the expression $a^* - (b + c)$. 10

VII. Write short notes on:

- Flow graphs (a)
- Storage allocation strategies. (b)

5,5

July- 2016

Exam Code: 0925 Sub. Code: 6863

B.E. (Information Technology) Seventh Semester IT-733: Compiler Design

Time allowed: 3 Hours

NOTE: Attempt five questions in all, including Question No. VII (Unit-III) which is compulsory and selecting two questions each from Unit 1- II.

UNIT-I

- a) List the errors generated by the syntax analysis phase of a compiler. Discuss error handling methods in the syntax analysis phase.
 - b) How top down and bottom up parser will parse the string 'bbd' using grammar A -> bA | d. Show all steps clearly. (5,5)
- a) Define an Operator Precedence Grammar. Also write down the rules to find II. relationship between each pair of terminal symbols.
 - b) Show that the following grammar is LR(1) but not LALR(1).

S->Aa | bAc | Bc | bBa

A->d

B->d

(5,5)

a) Explain lexical analysis phase of a compiler and, for a statement given below, write III. output of all phases (except of an optimization phase) of a compiler. Assume a, b and c of type float.

a = a + b * c * 2;

b) Draw Deterministic Finite Automata (DFA) for the following regular expressions:

i) (0±1) * 101(0+1)*

ii) 10 (0+1)*1

(5,5)

UNIT - III

- a) Discuss synthesized and inherited attributes using a suitable grammar. IV.
 - b) What is intermediate code? What is its importance? Discuss various representations of three address code. (5,5)
- a) How is static memory allocation scheme different from stack-based allocation V. scheme? Explain.
 - b) Explain how value parameters and reference parameters are treated during code generation. What effects does this have on the code generation process? P.T.O.

Exam.Code:0925 Sub. Code: 6865

1128

B.E. (Information Technology) Seventh Semester ITE-746: Complier Design

Time allowed: 3 Hours

Max. Marks: 50

NOTE: Attempt <u>five</u> questions in all, including Question No. I which is compulsory and selecting two questions from each Unit.

x-x-x

- I. Attempt the following:
 - a) What is Translator? Differentiate between Compiler and Interpreter.
 - b) Differentiate between DFA and NFA.
 - c) Write the need of Semantic analysis
 - d) Compare local optimization with global optimization
 - e) What is an LL(1) grammar? When the grammar is said to be LL(1) grammar? (5x2)

UNIT-I

- II. a) What is ambiguity? How an ambiguous Grammar can be converted to unambiguous Grammar.
 - b) Explain how LEX tool helps in performing the operation of Lexical Analysis. (5,5)
- III. a) Consider the following Grammar. Find LEADING and TRAILING for the following Grammar. $S \to E + T|T \quad T \to T \times F|F \quad F \to (F)|a|b$
 - b) Analyze the working of Compiler with the help of following statement. Z=A+B+Cx10 (5,5)
- IV. a) Design NFA for the Regular Expression (a+b)* a (a+b)
 - b) Discuss Recursive Descent Parsing.

UNIT-II

- V. a) Write Three Address code. Quadruple and Triples for the following expression: (a + b) x (c d x e)
 - b) Construct the DAG for the following expression (axb) + (c-d) x (axb) + b. (5,5)

P.T.O.

(5,5)

(2)

- VI. a) What is Code Optimization? Explain the difference between Local and Loop Optimization.
 - b) What is Symbol Table? Explain the use of Hash Table in Symbol Table. (5,5)
- VII. a) What is Peephole optimization? Explain its characteristics.
 - b) Write the different Storage Allocation Strategies.

(5,5)

Exam.Code:0925 Sub. Code: 6865

1127 B.E. (Information Technology) Seventh Semester ITE-746: Compiler Design (2015-16)

Time allowed: 3 Hours

NOTE: Attempt five questions in all, including Question No. I which is compulsory and selecting two questions from each Unit.

x-x-x

a) Which phase (or phases) of a compiler perform(s) constant folding and loop

b) Which phase (or phases) of a compiler is/are independent of the underlying

c) What is YACC?

2)

3)

4)

d) What is loop jamming?

e) What is basic block in compiler design?

(5x2=10)

UNIT-I

a) Diagram the likely components of a compiler and describe what each component does in the compiler.

b) Discuss in brief about the actions performed by a simple code generator while generating code for a typical three-address statement of a form x := y

a) What is lexical analysis? How are tokens specified and recognized in lexical analysis phase?

b) Regular expression (aa*)(bb*) is given. Construct NFA for the expression and convert this NFA to DFA. (5, 5)

a) What is the role of a parser? How parsing techniques are classified?

b) Construct LALR parsing table for the following grammar:

 $S \rightarrow S$ $S \rightarrow aAd \mid bBd \mid aBc \mid bAc$

(5, 5)

UNIT-II

- 5) What is run time memory management? How is static memory allocation scheme different from stack-based allocation scheme? Discuss and analyze about all the allocation strategies in run-time storage environment.
- 6) What are the main operations for a symbol table? Discuss the data structures associated with a symbol table maintained as a list of hash tables and how the operations of a symbol table are implemented in that case. Give an example of what your symbol table would look like for a sample program.

a) What is the difference between synthesized and inherited attributes in attribute grammars? Give an example for each.

b) Discuss any THREE popular code optimization techniques. Explain with appropriate examples for each of these techniques.

Exam. Code: 0918 Sub. Code: 6795

B. Engg. (Computer Science and Engineering)-6th Semester CS-604: Compiler Design

me allowed: 3 Hours

Attempt five questions in all, including Question No. 1 (Section-A) which is compulsory

a)What is the difference between compiler and assembler.	
a) What is the difference of intermediate and assembler. (b) Explain use of lex with a simple example.	
b) Explain ase various types of intermediate code	
c) What are various types of intermediate code representation schemes? d) Differentiate between top down and bottom up parsers.	
d) Differentiate between the following context-free grammas	
a) Differentiate bottom up parsers. e) Consider the following context-free grammar over the alphabet ∑ = {a, b, c} with S as the start symbol: S -> abScT abcT T -> bT b	
Write the language generated by above grammar.	
	5 X 2
Section B	5/2
Define compiler and explain phases of a compiler with a neat diagram.	5
(ii) Construct a transition diagram for recognizing unsigned numbers. Given the grammar S→(L) a L→L,S S	5
Given the granified changes to make it suitable for the	10
-Make required changes to make it suitable for LL(1) parsingConstruct FIRST and FOLLOW sets	1
-Construct the predictive parsing table	
-show the moves made by the predictive parser on the input(a, (a,a))	
Construct a SLR parsing table for the grammar given below.	
E-E+T,	10
E-T,	
T→T*F	
T→F,	
F -) id	2
F -) (E)	
Section C	
Explain following with code optimization:	
(i) Finding local common sub-expression (ii) Dead code elimination (iii) Loop unrolling (iv) Peep Hole	5
Optimization	
(b) Discuss following terms: i) Basic Block (ii) flow graph (iii) DAG (iv) Three Address code	5
a) For a given grammar	
E-> E+ T T E-T	7
T-> (E) id num	
Write an SDT to generate syntax tree for expression: a-4+c	2
Of Compare synthesized and inherited attributes.	3
, write a short note on	10
(d) storage allocation strategies	
(b) A simple code generator algorithm	

.ed Pages: 2 Questions

Roll No.

Sub. Code:

Exam. Code:

B.Engg. (Computer Science & Engg.) 7th Semester

COMPILER DESIGN

Paper-CSE-711

Time Allowed: Three Hours]

[Maximum Marks: 50

Note: - Attempt five questions in all. Selecting two questions from Sections A & B.

- Draw DFA with minimum number of after accepting 1. (a) $(a + b)^*$ abb over \sum containing $\{a, b\}$.
 - Differentiate SLR from CLR. (b)
 - Give the detailed process of register allocation in code (c) generation.
 - What is panic mode recovery? Where it is used? (d)
 - Which is more powerful CFG or regular expression and why? (e)

SECTION-A

Construct Predictive parsing table for the grammar:

 $E \rightarrow TE'$

 $E' \rightarrow + TE' \in$

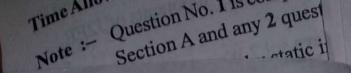
 $T' \rightarrow *FT' \in F \rightarrow (E)/id$

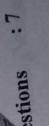
10

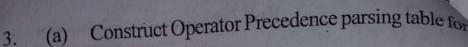
6821/BHJ-32829

Turn over

- D) UIVE SUME major Col
- a) Explain any code generation algorithm with example. b) Define basic blocks, flow graphs and then explain how VII. optimization.







$$S \rightarrow a \mid \uparrow \mid (R)$$

 $T \rightarrow S, T \mid S$

$$R \rightarrow T$$

- Give an overview of the lexical analyser. (b)
- Construct the SLR parsing table and then a DFA corresponding 4. to the grammar:

$$S \rightarrow L = R|R$$

 $L \rightarrow *R|id$

$$R \to L$$
.

10

SECTION-B

- Explain the concept of Syntax Directed Translation with 5. (a) examples.
 - Differentiate Synthesized attributes from inherited attributes. (b)
 - Give Syntax Directed Translation for Calculator. (c)
 - Draw annotated parse tree for 4 * 5 + 7n. (d)

 2.5×4

What are main issues in code generation? For the following code 6. segment generate intermediate code and identify the basic blocks:

For
$$i = 1$$
 to 10 do
For $j = 1$ to 10 do
a $[i, j] = 0.0$
For $i = 1$ to 10 do
a $[i,i] = 1.0$

10

- (a) Draw the DAG for a + a * (b-c) + (b-c) * d. Show all 7.
 - What is an activation record? Explain function of each item (b)

Exam. Code: 919 Sub. Code: 6820

1124

B. E./B.E. MBA (Computer Science & Engineering) 7th Semester

CSE-711: Compiler Design

ime Allowed: 3 hours

Max. Marks: 50

Note: Attempt five questions in all, including Question No. I which is compulsory and selecting at least two questions from each Unit I and II.

- a) Differentiate code optimization from code generation.
 - b) Which is more powerful and why: Regular expression and context free grammar.
 - c) Differentiate YACC from LEX by mentioning their functionality.
 - d) Explain by giving example of inherited attributes.
 - e) Explain peephole optimization process.

(5x2)

UNIT-I

- II. Differentiate recursive decent parser from predictive parser. Explain by giving all algorithms used and by tracing with an example the construction of predictive parsing table. (10)
- Differentiate SLR, CLR and LALR. Then explain the construction of CLR parsing III. table by giving all the algorithms used and then by taking an example. (10)
- IV. a) Explain with example the construction of operator precedance parsing table.
 - b) Give some errar recovery techniques for predictive parsers. LR parsers and operator precedances parsers. (5,5)

- V. a) Give some storage allocation strategies used by compiler.
 - b) Compare some data structure available for storing symbol table. (5,5)
- a) Explain with example -Intermediate code representation techniques. VI.
 - b) Give some major code optimization constructs. (5,5)
- a) Explain any code generation algorithm with example. VII.
 - b) Define basic blocks, flow graphs and then explain how they can be used in code optimization. (4,6)

(6820)

July-2016

Exam. Code: 919 Sub. Code: 6821

B.E.(Computer Science and Engineering) 7th Semester CSE-711: Compiler Design

Time Allowed: 3 hours

Max. Marks: 50

Note: Attempt five questions in all, including Question No. I which is compulsory and selecting two questions from each Unit I - II.

a) What is the need of generating intermediate code?

b) Differentiate LEX from YACC.

c) Differentiate parse tree from syntax tree.

d) What do you understand by binding of names?

e) Differentiate between LEADING () and FIRST ().

f) Give some applications of DAG.

g) What is loop jamming?

h) Name any two Bottom-up parser.

What is panic mode recovery? i)

j) What do you mean by Register allocation in compilation?

(10x1)

UNIT-I

a) Which one is more powerful: Regular expression or finite automata? Explain with II. examples.

(5,5)b) Giving algorithms for finding LEADING and TRAILING.

Give the complete process of generating Predictive parsing table by giving all the III. algorithms used. Also trace the same for generating parsing table for any assumed grammar.

IV. Give the complete procedure to generate the CLR parsing table by detailing all the algorithms used. Also trace the same for generating the parsing table for some assumed grammar.

UNIT-II

- a) Differentiate synthesized attributes from inherited attributes by giving examples. V.
 - b) Give some intermediate code representation techniques with examples. (5,5)

VI. a) What is a flow graph? Give the procedure to draw a flow graph along with some examples and its applications.

b) Give in detail any of the code generation algorithm. Also give examples to (5,5)explain the same.

a) Explain Peephole optimization in detail. VII.

b) Give some principle sources of optimization.

c) Explain any three storage allocation strategies.

d) Give any three symbol table storage data structures.

 $(4x2\frac{1}{2})$

(6821)