//Nick Harris CPE301 Assignment 2A

#include <iosstream>

#include <avr/io.h>

#include <util/delay.h>

using namespace *std*;

int main()

{

DDRB = 0xFF; //set port b as output port

DDRC = 0x00; //set port c as input port

PORTC |= (1<<2); //turn on port c pin 3 pull up

if (PINC & (1<<2)) //if pin c port 3 enabled

{

while(1)

{

PORTB |= (1<<1); //set port b pin 2

delay(); //delay for ~ 1.3 secs (each delay ~250ms)

delay();

delay();

delay();

PORTB &= ~(1<<1); //clear port b pin 2

if (PINC & ~(1<<2)) //check if pin c port 3 is enabled

{

break; //break if not enabled

}

}

}

}

void delay()

{

\_delay\_ms(250); //delay for 250 ms

}

;

; Harrin2\_DA\_2A.asm

;

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;

ldi r16, 0x00 ;load 0 into r16

ldi r17, 0xff ;loaf ff into r17

out DDRC, r16 ;port c is input

out DDRB, r17 ;port b is output

sbi PORTC,3 ;set pull up on portc pin 3

CHECK: sbis PINC, 3 //check if pin c pin 3 is enable

rjmp CHECK

ldi r17, 0x02 //load 2 into r17

out PORTB, r17 //enable port b pin 2

call DELAY ;delay for 250ms

call DELAY

call DELAY

call DELAY

cbi PORTB,2 ;clear portb pin2

END: rjmp END

DELAY:

ldi r18, 11

ldi r19, 38

ldi r20, 94

L1: dec r20

brne L1

dec r19

brne L1

dec r18

brne L1

rjmp PC+1