Nick Harris

DA\_2B

10/5/19

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; Harrin2\_DA\_2B.asm

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; Created: 10/5/2019 11:22:23 PM

; Author : Nick

;DA2B

.INCLUDE <M328DEF.INC>

jmp MAIN

.ORG 0x02 ;reset location

jmp DA\_TWO

MAIN:

ldi r16, 0x00 ;set portc as input

ldi r17, 0x03 ;set port b as output

out DDRC, r16

out DDRB, r17

sbi PORTC,3 ;set portc.3

ldi r20, 0x2 ;falling edge triggered

sts EICRA, r20

ldi r20,1<<INT0 ;enable int0

out EIMSK, r20

sei ;set interrupts

HERE:jmp HERE

DA\_TWO:

ldi r18, 11

ldi r19, 38

ldi r21, 94

L1:

dec r21

brne L1

dec r19

brne L1

dec r18

brne L1

ret

/\*

\* DA\_2B.c

\*

\* Created: 10/6/2019 1:19:13 AM

\* Author : Nick

\*///DA\_2B

#include <avr/io.h>

#include <avr/interrupt.h>

#define *F\_CPU* 8000000UL

int main(void)

{

DDRB = (1<<3); //pb3 set as output

PORTC = (1<<3); //pull-up activated

EICRA = 0x2; //falling edge triggered

EIMSK = (1<<INT0); //enable ext interrupt 0

sei(); //enable interrupts

while (1)

{

ISR(INT0\_vect)

{

PORTB ^= (1<<3); //toggle portb.3

}

}

}