

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Capacitors



File: Capacitors.kicad_sch

Power Gate



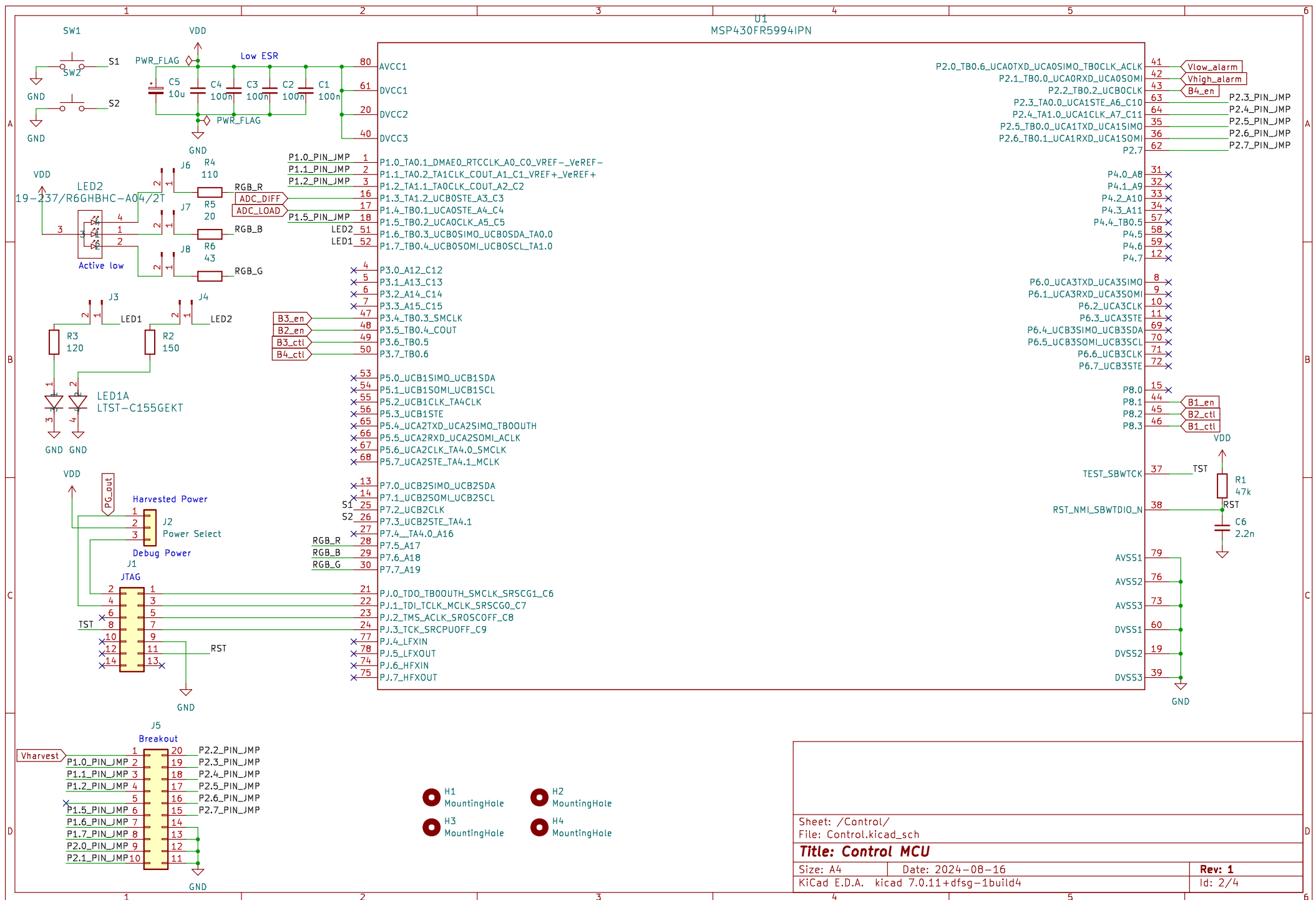
File: PowerGate.kicad_sch

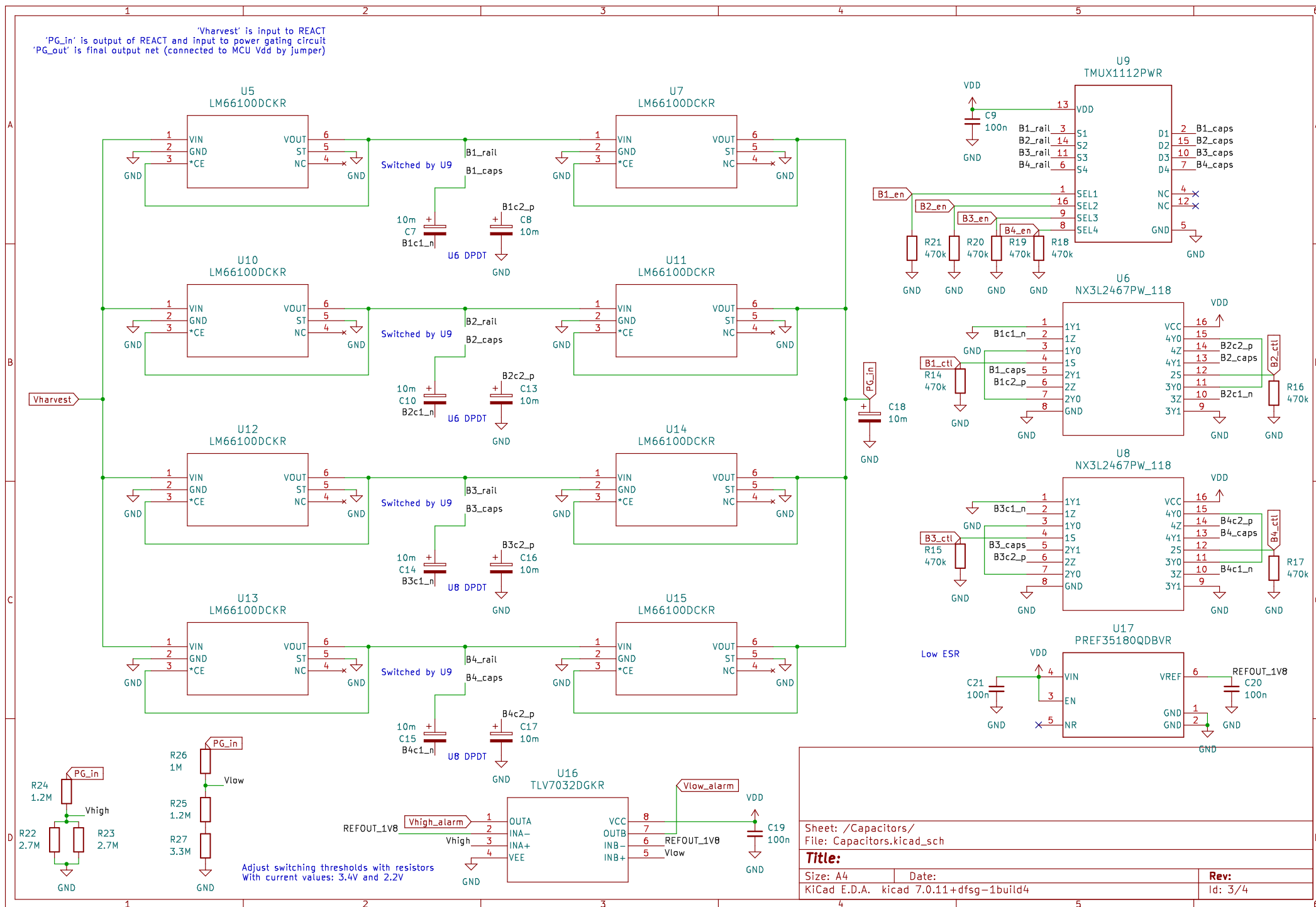
Control



File: Control.kicad_sch

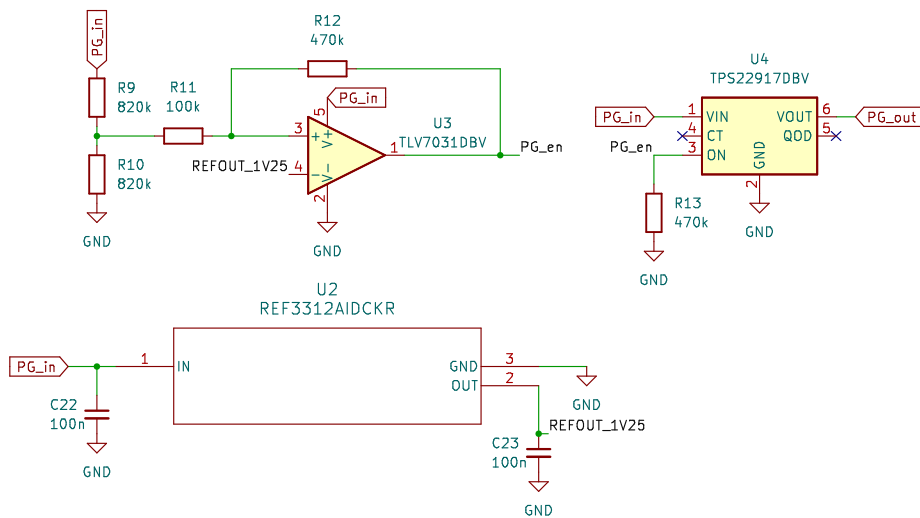
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File: ReactPcb.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.11+dfsg-1build4		Id: 1/4





'Vharvest' is input to REACT
'PG_in' is output of REACT and input to power gating circuit
'PG_out' is final output net (connected to MCU Vdd by jumper)

Load switch hysteresis enable  about 3.3V, disable  about 2.0V



Sheet: /Power Gate/
File: PowerGate.kicad_sch

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