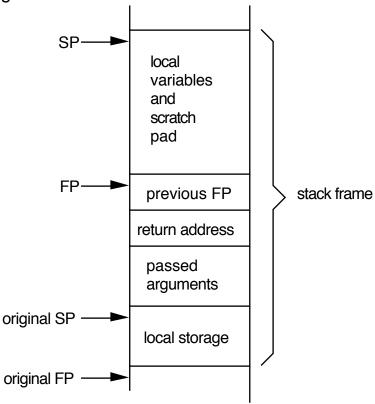
# STACK FRAMES

The MC68000 provides two special instructions to allocate and deallocate a data structure called a <u>frame</u> in the stack to make subroutines easier to code.

general structure of a frame:



where register An is used as the argument pointer.

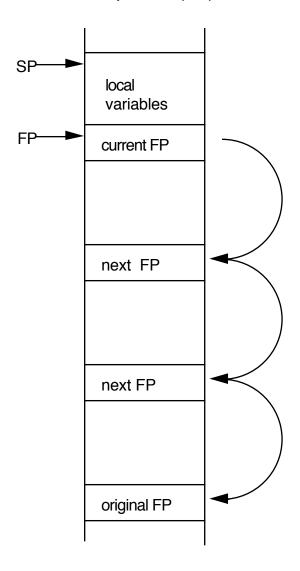
LINK An,d

1. put An at -(SP) Example:
decrement stack pointer and put
A0 on the stack.
2. put SP into An Example:
set A0 to point to this value.
3. change SP-d to SP, i. e.
decrement the SP
UNLK An

1. An → SP, change the value of
the SP to that contained in An

2. (SP)+  $\rightarrow$  An, put that value on the stack into An and deallocate that stack space.

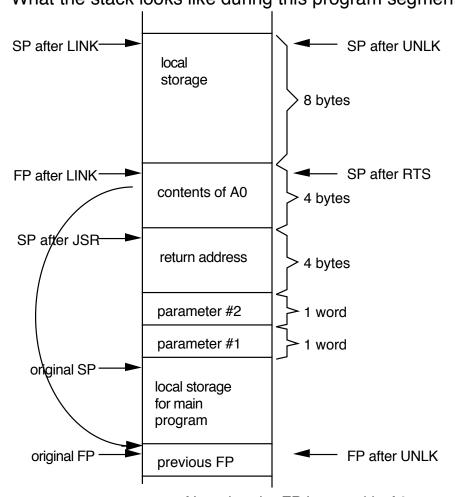
Return addresses <u>and</u> passed arguments are always <u>positive</u> relative to the frame pointer (FP).



# Example:

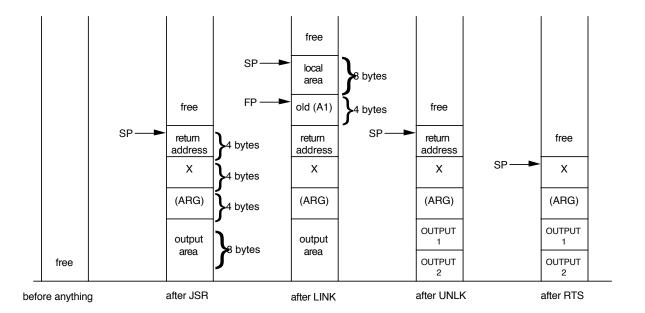
	MOVE.W MOVE.W JSR	D0,-(SP) D1,-(SP) SBRT	;push parameter #1 onto stack ;push parameter #2 onto stack ;jump to subroutine SBRT
SBRT	LINK · ·	A0,-#\$8	establish FP and local storage;
	MOVE.W	10(A0),D5	;retrieve parameter #1
	UNLK	A0	;FP for the calling routine re-established.  Deallocate stack frame

RTS ;return
What the stack looks like during this program segment:



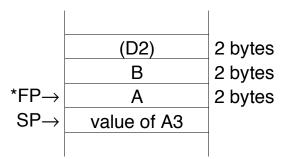
Note that the FP is stored in A0.

EXAMPLE ARG N M	: DC.L EQU EQU	8 8	;number ;8 bytes for output ;8 bytes for local variables
		#-N,SP ARG,-(SP) X	;put output area on stack ;put argument on stack ;put address of data table on stack
	JSR ADDA	SUBR #8,SP	;goto subroutine
	MOVE.L MOVE.L •	(SP)+,D1 (SP)+,D2	;read outputs
SUBR	LINK ·	A1,#-M	;save old SP
		LOCAL1,-4(A1) LOCAL2,-8(A1)	;save old variables ;
	ADD.L MOVEA.L	,	;change a local variable ;get X
	MOVE.L	OUTPUT,16(A1)	;push an output
	UNLK RTS	A1	
LOCAL1 LOCAL2 OUTPUT	DC.L DC.L DC.L	\$98765432 \$87654321 'ADCB'	;local variables output value



Program to compute the power of a number using a subroutine. Power MUST be an integer. A and B are signed numbers. Parameter passing using LINK and UNLK storage space on the stack.

MAIN	LINK MOVE	A3,#-6 A,-2(A3)	;sets up SP
	MOVE JSR LEA MOVE UNLK	B,-4(A3) POWR C,A5 -6(A3),(A5) A3	;call subroutine POWR
ARG A B C	EQU DC.W DC.W DS.W	* 4 2 1	
POWR	EQU MOVE MOVE.L	* -2(A3),D1 -4(A3),D2 #1,D3 *	;put A into D1 ;put B into D2 ;put starting 1 into D3
LOOP	EQU SUBQ BMI	#1,D2 EXIT	;decrement power ;if D2-1<0 then quit NOTE: this gives us A**0=1
EXIT	MULS BRA EQU MOVE RTS	D1,D3 LOOP * D2,-6(A3)	;multiply out power ;and repeat as necessary ;C=(D3)
	END	MAIN	



\*fixed while the SP changes

# Better way.

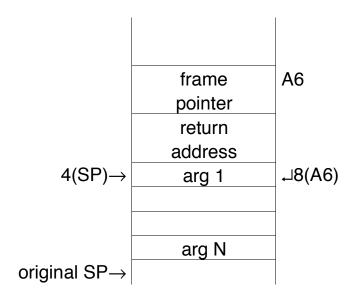
MAIN	MOVEA.L MOVE MOVE ADD.L JSR LEA MOVE	SP,A3 A,-(SP) B,-(SP) #2,SP POWR C,A5 -6(A3), (A5)	;save output area ;call subroutine POWR ;put answer somewhere
ARG A B C	EQU DC.W DC.W DS.W	* 4 2 1	
POWR	EQU LINK MOVE MOVE • • MOVE UNLK RTS	* A3,#-6 10(A3),D1 12(A3),D2  D2,8(A3) A3	''
	END	MAIN	

$SP {\rightarrow}$	C	save 6 bytes for
	В	next set of variables
	A	
FP (in A3)→	previous	4 bytes
	FP	
	return	4 bytes
	address	
	reserved for C	2 bytes
	В	2 bytes
	Α	
original SP,FP $\rightarrow$		

# Calling conventions for C or Pascal

Arguments are pushed onto the stack in the reverse order of their appearance in the parameter list.

Just after a subroutine call:



If the function begins with a LINK A6,#
High level language always generates LINK A6,# instructions

All arguments occupying just a byte in C are converted to a word and put in the low byte of the word, i.e.



Result, if any, is returned in D0 for function calls.

# IT IS THE PROGRAMMER'S RESPONSIBILITY TO REMOVE THE ARGUMENTS FROM THE STACK.

The C calling sequence looks like this:

MOVE \_\_\_\_,-(SP) ;last argument

•

•

•

MOVE \_\_\_\_\_,-(SP) ;first argument

JSR FUNCT

ADD #N,SP ;total size of arguments

Subroutine functions:

LINK A6,#N

•

•

•

MOVE ...,D0 UNLK A6

RTS

The Pascal calling sequence pushes arguments in left to right order, then calls the function. The result if any is left on the stack. An example looks like this:

SUB #N,SP ;save space for result

MOVE ...,-(SP) ;push first argument onto stack

•

•

MOVE ...,-(SP) ;last argument

JSR FUNCT

MOVE (SP)+,... ;store result

## Subroutine code:

LINK A6,#N

•

<code>

•

UNLK A6

MOVE (SP)+,A0 ;return address

ADD #N,SP ;total size of arguments

MOVE ...,(SP) ;store return result

JMP (A0)

Symbols defined in assembly routines with the DS directive and exported using XDEF and XREF can be accessed from C as external variables. Conversely, C global variables can be imported and accessed from assembly using the XREF directive.

Miscellaneous comments about subroutines.

Parameter passing via MOVEM (move multiple registers)

If you have a small assembly language program this instruction allows you to save the values of registers <u>NOT</u> used to pass parameters.

```
Example:
SUBRTN EQU *
MOVEM D0-D7/A0-A6,SAVBLOCK

MOVEM SAVBLOCK,D0-D7/A0-A6
```

where SAVBLOCK is local memory. This is bad practice since SAVBLOCK can be overwritten by your program.

```
MOVEM has two forms
```

MOVEM register\_list,<ea>
MOVEM <ea>,register\_list

More common to save registers on stack

MOVEM is often used for re-entrant (subroutines that can be interrupted and re-entered) procedures.

The MOVEM instruction always transfers contents to and from memory in a predetermined sequence, regardless of the order used to specify them in the instruction.

address register indirect with pre- transferred in the order A7 $\rightarrow$ A0, decrement then D7 $\rightarrow$ D0

for all control modes and address transferred in reverse order register indirect with postincrement transferred in reverse order  $D0 \rightarrow D7$ , then  $A0 \rightarrow A7$ 

This allows you to easily build stacks and lists.

# Six methods of passing parameters:

- 1. Put arguments in D0 thru D7 <u>before</u> JSR (good only for a few arguments)
- 2. Move the addresses of the arguments to A0-A6 before JSR
- 3. Put the arguments immediately after the call. The argument addresses can be computed from the return address on the stack.
- 4. Put the addresses of the arguments immediately after the call in the code.
- 5. The arguments are listed in an array. Pass the base address of the array to the subroutine via A0-A6.
- 6. Use LINK and UNLK instructions to create and destroy temporary storage on the stack.

#### JUMP TABLES

- are similar to CASE statements in Pascal
- used where the control path is dependent on the state of a specific condition

#### **EXAMPLE:**

This subroutine calls one of five user subroutines based upon a user id code in the low byte of data register D0. The subroutine effects the A0 and D0 registers.

	RORG	\$1000	;causes relative addressing (NOTE 1)
SELUSR	EXT.W CHK LSL	D0 #4,D0 #2,D0	;extend user id code to word ;invalid id code ? (NOTE 2) ;NO! Calculate index=id*4 since all long word addresses
	LEA MOVEA.L	UADDR,A0 0(A0,D0.W),A0	;load table addresses ;compute address of user specified subroutine and put correct caling address into A0
	JMP  • •	(A0)	;jump to specified routine
UADDR	DC.L	USER0,USER1,	USER2,USER3,USER4

## NOTES:

- 1. The RORG is often used when mixing assembly language programs with high level programs. It causes subsequenct addresses to be relative.
- 2. The CHK is a new instruction. In this case it checks if the least significant word of D0 is between 0 and 4 (2's complement). If the word is outside these limits, a exception through vector address \$10 is initiated. The CHK instruction checks for addresses outside assigned limits and is often used to implement subscript checking.

## EXAMPLE RECURSIVE PROCEDURE USING STACK

DATA	EQU	\$6000	
PROGRM	EQU	\$4000	
	ORG	DATA	
NUMB	DS.W	1	;number to be factorialized
F_NUMB	DS.W	1	;factorial of input number
	ORG	PROGRM	
MAIN	MOVE.W	NUMB,D0	get input number;
	JSR	FACTOR	;compute factorial
	MOVE.W	D0,F_NUMB	;save the answer

<sup>\*</sup> SUBROUTINE FACTOR

<sup>\*</sup> OUTPUT: D0.W=120

FACTOR	MOVE.W	D0,-(SP)	;push current number onto
			stack
	SUBQ.W	#1,D0	;decrement number
	BNE.S	F_CONT	;not end of factorial
			computations
	MOVE.W	(SP)+,D0	;factorial=1
	BRA.S	RETURN	
F_CONT	JSR	FACTOR	
	MULU	(SP)+,D0	
RETURN	RTS		

<sup>\*</sup> PURPOSE: Determine the factorial of a given number.

<sup>\*</sup> INPUT: D0.W = number whose factorial is to be computed

<sup>\*</sup>  $0 \le D0.W \le 9$ 

<sup>\*</sup> OUTPUT: D0.W = factorial of input number

<sup>\*</sup> REGISTER USAGE: No registers except D0 effected

<sup>\*</sup> SAMPLE CASE: INPUT: D0.W=5

$\rightarrow$	1	subtract 1, equal to zero so pop stack
	return	
	address	
	2	
	return	
	address	
	3	
	return	
	address	
	4	
	return	
	address	
	5	put D0 (current onto stack)
	return	
	address	
original SP $\rightarrow$		

# **EXAMPLE**

This is a simplified version of TUTOR's "DF" command. It uses the stack to display register contents.

START	MOVEM.L	TESTREGS,D0-D7/A0-A6 ;assign values to registers
	MOVE,L JSR MOVE.L ADDQ.L JSR TRAP	#-1,-(SP) ;put something on stack PRINTR ;print all registers (SP)+,D0 ;retrieve it
SAVESP	EQU	60
PRINTR RMSGS:		;data for PRINTREGS
	DC.B	' D0 D1 D2 D3 D4 D5',0
	DC.B DC.B	' D6 D7 A0 A1 A2 A3',0 ' A4 A5 A6 SP SR PC',0
;		I< 55 characters long>I
SPACES	DC.B	' ',0 ;2 blanks
CONBUF	DS.B	10
ENDLINE	DC.B	\$0D,\$0A,0
; data for p	_	
CH	DS.B	1
TOTOLO	DS.W	1
TSTREG	DC.L	1,2,3,4,5,6,7,8,\$A,\$AA,\$AAA
	DC.L END	\$AAAA,\$AAAAA,\$AAAAAAA

PRINTR	MOVE.W PEA	6(SP)	;save SR on stack ;save original SP on stack
	MOVEM.L	D0-D7/A0-A6,-(S	,
	1401/50	<b>"0 D 4</b>	registers
	MOVEQ	#2,D4	;D1 counts # of rows in printout
	MOVEA.L	SP,A1	;use A1 to point to beginning of data
	LEA	RMSGS,A2	;use A2 to point to row headings
MLOOP			;output routine for heading
	MOVEA.L	A2,A0	;set pointer to beginning of header to be printed
	JSR	PrintString	;output heading
	MOVEQ	#5,D5	;output six registers this line
RLOOP	TST.W	D4	tests for SR to be printed;
	BNE.S	NOT_SR	;SR requires special routine
	CMP.W	#1,D5	;as it is only word length
	BNE.S	NOT_SR	;register
	LEA	SPACES,A0	;load addresses of spaces
	JSR	PrintString	;print spaces with no new line
	MOVE.W	(A1)+,D0	;put SR word into D0
	JSR	PNT4HX	;unimplemented routine to convert 4 hex digits in D0 to
			ascii code for printing
	JSR	PrintString	print hex contents
	LEA	SPACES,A0	;load address of spaces
	JSR BRA.S	PrintString ENDRPL	;print them with no line feed
NOT_SR	MOVE.L	(A1)+,D0	;put register contents into D0
	JSR	PNT8HX	;unimplemented routine to convert 8 hex digits in D0 to ascii code for printing

ENDRPL	DBF	D5,PRLOOP	;decrement register counter, started at 5
	LEA	ENDLINE,A0	;print CR+LF
	JSR	PrintString	
	ADDA.L	#55,A2	;increment heading pointer
	DBF	D4,MLOOP	goto another line;
	MOVEM.L	(SP)+,D0-D7/A0-	-A6
	ADDQ.W	#4,SP	skip over A7 to point to SR
	RTR		;return and restore registers

$SP {\rightarrow}$	D0	
	D1	
	0	
	0	
	0	
$\rightarrow$	0	
	<b>A</b> 5	
	A6	
after MOVEM $\rightarrow$	original SP	⇔do this since this is only way to
	address	save original value of A7
after ADD #4 $\rightarrow$	SR	←the RTR pops this and the
	return address	return address
$\rightarrow$	-1	put D0 (current onto stack)