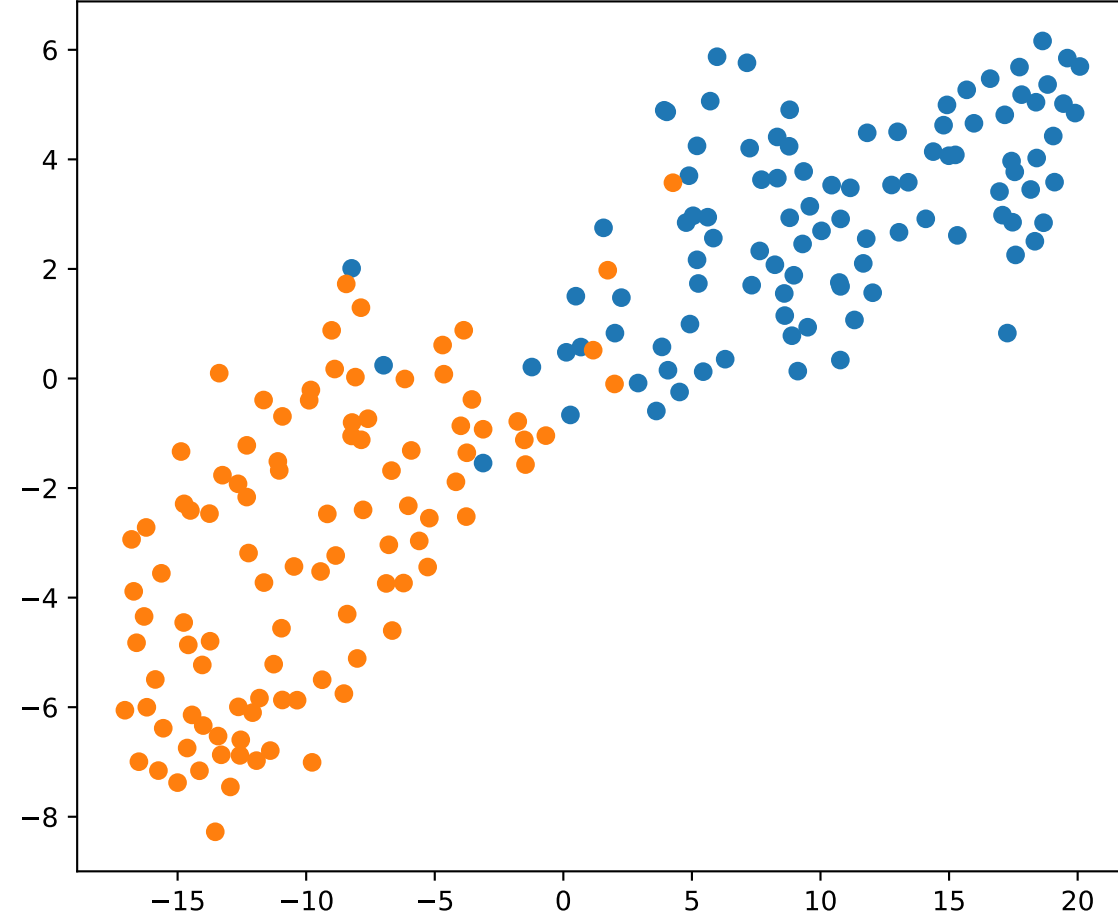
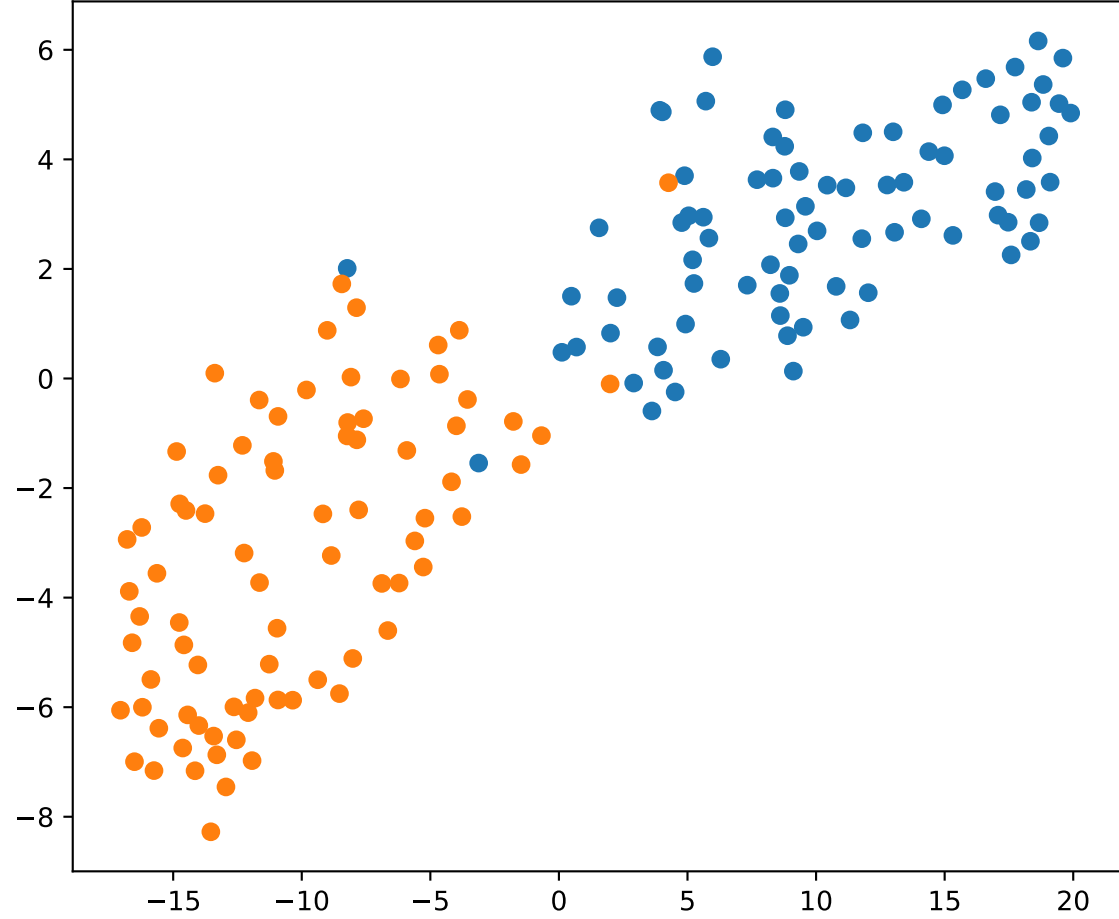


all



dwac_emb10.merged10

train



valid

