Airoha IoT SDK Open Source Software Guide

Version: 3.26

Release date: 18 July 2023

Document Revision History

Revision	Date	Description
1.0	7 March 2016	Initial release
2.0	2 May 2016	Added the FatFs module.
3.0	30 June 2016	Added CMSIS and LZMA.
3.1	2 September 2016	Added the illustration of FreeRTOS memory configuration and settings.
3.2	13 January 2017	Added the mDNS and WebSocket.
3.3	5 May 2017	Updated DHCPD
3.4	1 August 2017	Updated license information
3.5	15 September 2017	Added AWS IoT SDK support
3.6	7 January 2019	Added micro-ecc and OPUS OGG Codec. Updated license information.
3.7	7 March 2019	Added a table for modules on different chips
3.8	4 September 2019	Added support modules on AG3335
3.9	19 September 2019	Added nanopb support
3.10	26 September 2019	Updated cJSON, MQTT, nghttp2
3.11	23 October 2019	Added SRP
3.12	22 November 2019	Fixed link error
3.13	2 June 2020	Added nanopb & mSBC encoder support
3.14	29 July 2020	Updated RTOS software license in Table 1 Added pre_libloader and Gsensor-key support
3.15	6 July 2021	Updated nanopb/Fatfa/mbedtls/LZMA
3.16	24 December 2021	Added support modules on AG3352
3.17	5 January 2022	Added support modules on AB158x
3.18	21 April 2022	Added nanopb, newlib, newlib-nano and Libgcc license information Removed Wi-Fi related open source modules
3.19	26 August 2022	Updated FatFs license disclaimer link
3.20	13 October 2022	Updated FreeRTOS license and version information in Table 1 & Table 2 Updated mbedtls/CMSIS/pre_libloader version in Table 2
3.21	08 December 2022	Updated lwIP and mbedtls content
3.22	06 January 2023	Updated for hearing aid lib
3.23	16 March 2023	Modify for AG3353
3.24	22 March 2023	Modify for AB157x
3.25	16 May 2023	Modify for AB1627
3.26	18 July 2023	Added STM IMU sensor support

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Lists of tables and figures

1. Overview

This guide provides information about the open-source software bundled in the SDK. It provides information about the open-source software packages and guides the developers in designing, prototyping, and implementing projects in a convenient environment.

As an open-source software package, the information is already available in various sources. This guide is an easy reference to module descriptions, including the official web site and the hardware or software integration versions. Developers can access the latest source code from the official website and merge or replace the code in the package with a corresponding version. From the footprint statistics result, developers can easily estimate the code size for setting up the flash memory layout. Before commencing your own IoT project or application development, look for example applications and their source code. Finally, the troubleshooting and limitations chapter provides more detailed information on reported issues that may be encountered during the application development.

1.1. Open source software resources for the SDK

The open-source software packages in the SDK are listed in Table 1 and for developer's reference only. The developer must acknowledge that such listed open-source software may be supplemented or amended by Airoha from time to time. The developer must also comply with all licensing terms applicable to such open-source software. Airoha makes the following disclaimers regarding the open-source software on behalf of itself, and the copyright holders, contributors, and licensors of the listed open-source software: TO THE FULLEST EXTENT PERMITTED UNDER APPLICABLE LAW, THE OPEN SOURCE SOFTWARE ARE PROVIDED BY THE COPYRIGHT HOLDERS, CONTRIBUTORS, LICENSORS, AND AIROHA "AS IS" AND ANY REPRESENTATIONS OR WARRANTIES OF ANY KIND, WHETHER ORAL OR WRITTEN, WHETHER EXPRESS, IMPLIED, OR ARISING BY STATUTE, CUSTOM, COURSE OF DEALING, OR TRADE USAGE, INCLUDING WITHOUT LIMITATION THE IMPLIED WARRANTIES OF TITLE, MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT, ARE DISCLAIMED. IN NO EVENT WILL THE COPYRIGHT OWNER, CONTRIBUTORS, LICENSORS, OR AIROHA BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION), HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THE OPEN SOURCE SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

Table 1. Open source packages

Module	Open source software licenses	Comments
RTOS	FreeRTOS License: • Modified GPL (for v8.2.0) • MIT license (for v10 and later version) Refer to license disclaimer in kernel/rtos/FreeRTOS/Source/include/FreeRTOS.h For Cadence FreeRTOS porting layer, refer to license disclaimer in dsp/kernel/service/context_switch/<IC_CONFIG>/inc/xtensa_context.h	Market leading, de-facto standard OS for embedded systems.

Module	Open source software licenses	Comments
	Note: FreeRTOS V10 and later version are distributed under the MIT license. http://www.freertos.org/a00114.html	
TCP/IP	lwIP License: BSD License http://lwip.wikia.com/wiki/License	lwIP (lightweight IP) is a widely used open source TCP/IP stack designed for embedded systems.
Mbed TLS	Mbed TLS License: Apache 2.0 License http://www.apache.org/licenses/LICENSE-2.0	Easy to use SSL/TLS library including cryptographic and SSL/TLS capabilities with small footprint.
XML	Mini-XML License: LGPLv2 with static linking exception http://michaelrsweet.github.io/mxml/	A small XML library to read and write XML documents.
FatFs	FatFs License: BSD-style license http://elm-chan.org/fsw/ff/doc/appnote.html#license	FatFs is a generic FAT file system module for small embedded systems. For compatibility and practical considerations, the SDK currently supports two FatFs versions, R0.12b and R0.14b.
CMSIS	CMSIS License: BSD License (for v4.0.0) https://developer.mbed.org/blog/entry/CMSIS-Components-BSD-Licensed/ Apache 2.0 License (for v5.7.0) https://arm-software.github.io/CMSIS_5/General/html/index.html#License	CMSIS is a vendor-independent hardware abstraction layer for the Cortex-M processor series and defines generic tool interfaces.
LZMA decoder	LZMA License: public domain http://www.7-zip.org/sdk.html	LZMA decoder is extracted from the LZMA SDK for further use.
Micro-ecc	micro-ecc License: BSD 2-Clause "Simplified" https://github.com/kmackay/micro-ecc	A small and fast ECDH and ECDSA implementation for 8-bit, 32-bit, and 64-bit processors.
OPUS Codec	OPUS License: The 3-Clause BSD License https://opus-codec.org/license/	The audio codec library to support OGG file and opus file.
nanopb	nanopb License: zlib License https://jpa.kapsi.fi/nanopb/	A C implementation of Google's Protocol Buffers
mSBC encoder	SBC encoder License: Apache 2.0 License http://www.apache.org/licenses/LICENSE-2.0	The source code is from Android source tree located in a Git repository hosted by Google.
Gsensor-key	Gsensor-key License: The 3-Clause BSD License	A module used to realize single tap and double tap, part of the source code is

Module	Open source software licenses	Comments
	https://opensource.org/licenses/BSD-3-Clause	from STMicroelectronics's MEMS standard C driver on github.
pre_libloader	pre_libloader License: MIT license https://opensource.org/licenses/MIT	A module that allows dynamically loading and unloading code to execute on an Xtensa processor without the aid of an operating system.
newlib	libm.a & libnosys.a License: Refer to COPYING.NEWLIB https://chromium.googlesource.com/native_client/nacl-newlib/+/refs/heads/master/COPYING.NEWLIB	It is a C-standard library implementation intended for use on embedded systems.
newlib-nano	libc_s.a License: Refer to src\newlib\COPYING.NEWLIB, in gcc-arm-none-eabi-9-2019-q4-major-src.tar.bz2 (MD5: dec65fe8c14aae90512310dd5fe88bf1).	The newlib-nano is an open-source C library (libc) targeting embedded microcontrollers (MCU).
Libgcc	libgcc.a License: GNU GPL plus the GCC Runtime Library Exception https://www.gnu.org/licenses/gcc-exception-3.1.html Refer to Part 6 in license.txt under mcu\tools\gcc9.2.1\linux\gcc-arm-none-eabi\share\doc\gcc-arm-none-eabi\ folder.	GNU Arm Embedded Toolchain
STM IMU sensor	STM IMU sensor driver License: BSD-3-Clause https://opensource.org/licenses/BSD-3-Clause	Sensor driver for STM lsm6dsow

The SDK open-source packages are implemented by the active open-source community with widely available online resources and forum support. The list of online resources is provided for each module in Table 2. The integrated versions are also included for developers to merge hot bug fixes or new features directly from the official release links.

Table 2. Online resources for each module in the SDK

Module	Official website	Integrated version	Online API reference
RTOS	http://www.freertos.org/RTOS.html	For MT2523/MT2533: v8.2.0 For AM255x/AB155x/AB1565/AB1568/AG3335/AG3352/AG3353: v10.1.1 For AB158x/AB157x/AB1627: v10.4.5	http://www.freertos.org/a00106.html
TCP/IP	http://savannah.nongnu.org/projects/lwip/ http://lwip.wikia.com/wiki/LwIP_Wiki	2.1.2	No online API. Exported API with comments can be found at `<sdk_root>/middleware/third_party/`

Module	Official website	Integrated version	Online API reference
			lwip/src/include/lwip/sockets.h.
Mbed TLS	https://tls.mbed.org/	For AB1565/AB1568/AB158x/AB157x/AB1627: v3.1.0 For MT2523/MT2533/AM255x/AB155x: v2.26.0 For AG3335/AG3352/AG3353: v2.25.0	https://tls.mbed.org/api/
XML	https://www.msweet.org/mxml/	Mini-XML 2.9	https://www.msweet.org/mxml/mxml.html
FatFs	http://elm-chan.org/fsw/ff/00index_e.html	R0.14b(1)	http://elm-chan.org/fsw/ff/00index_e.html
CMSIS	https://developer.arm.com	For MT2523/MT2533/AM255x/AB155x/AB1565/AB1568/AG3335/AG3352/AG3353/AB157x/AB1627: 4.0.0 For AB158x: 5.7.0	https://arm-software.github.io/CMSIS_5/Core/html/modules.html
LZMA decoder	http://www.7-zip.org/sdk.html	For AM255x/AB155x/AB1565/AB1568/AB158x/AB157x/AB1627: 21.02 For AG3335/AG3352/AG3353: 15.08	http://www.7-zip.org/sdk.html
Micro-ecc	https://github.com/kmackay/micro-ecc	https://github.com/kmackay/micro-ecc/commits/master 1.0.0	https://github.com/kmackay/micro-ecc
OPUS Codec	http://www.xiph.org/ http://opus-codec.org/	1.1.4	No online API. Exported API with comments can be found at <sdk_root>/prebuilt/middleware/third_party/audio/celt_codec/inc/opuscelt_api.h. <sdk_root>/prebuilt/middleware/third_party/audio/ogg_codec/inc/oggcelt_api.h.
nanopb	https://jpa.kapsi.fi/nanopb/	0.4.5(2)	https://jpa.kapsi.fi/nanopb/docs/reference.html
mSBC encoder	https://android.googlesource.com/platform/system/bt/+/refs/tag	5.1.0	https://android.googlesource.com/platform/system/bt/+/refs/tags/android-wear-

Module	Official website	Integrated version	Online API reference
	s/android-wear-5.1.0_r1/embdrv/sbc/encoder		5.1.0_r1/embdrv/sbc/encoder
Gsensor-key	https://github.com/STMicroelectronics/STMems_Standard_C_drivers/tree/master/lis2dw12_STdC https://github.com/STMicroelectronics/STMems_Standard_C_drivers/tree/master/lis2ds12_STdC	1.0.0	https://github.com/STMicroelectronics/STMems_Standard_C_drivers/tree/master/lis2dw12_STdC https://github.com/STMicroelectronics/STMems_Standard_C_drivers/tree/master/lis2ds12_STdC
pre_libloader	NA(From Cadence)	For AM255x/AB155x/AB156x (SDK 2.x): RG-2019.12 For AB158x/AB156x (SDK 3.x)/AB157x/AB1627: RI-2021.8	No online API. The exported API is at <sdk_root>\dsp\kernel\service\pre_libloader\inc\preloader_pisplit.h
newlib	https://developer.arm.com/downloads/-/gnu-rm	For MT2523/MT2533/AM255x AG3335/AG3352/AG3353/AB155x: v2.1.0 For AB1565/AB1568/AB158x/AB157x/AB1627: V3.1.0	https://developer.arm.com/downloads/-/gnu-rm
newlib-nano	https://developer.arm.com/downloads/-/gnu-rm	For MT2523/MT2533/AM255x AG3335/AG3352/AG3353/AB155x: v2.1.0 For AB1565/AB1568/AB158x/AB157x/AB1627: V3.1.0	https://developer.arm.com/downloads/-/gnu-rm
Libgcc	https://developer.arm.com/downloads/-/gnu-rm	For MT2523/MT2533/AM255x AG3335/AG3352/AG3353/AB155x: 2.23.2.20140731 For AB1565/AB1568/AB158x/AB157x/AB1627: 2.33.1.20191025	https://developer.arm.com/downloads/-/gnu-rm
STM IMU sensor	https://github.com/STMicroelectronics/STMems_Standard_C_drivers	V1.0.0	No online API.

(1) FatFs version is R0.12b before SDK version 2.7.0 and support added for R0.14b after SDK version 2.7.0.

(2) GSound library (add-on release) also includes nanopb (version v0.4.3) in its library.

(3) libawha_nr.a (add-on release, hearing aid feature) includes noisePowProposed (Noise Power Estimation Based on the Probability of Speech Presence), version 1.0, BSD license.

Figure 1 and Figure 2 show the location of the modules listed in Table 2. The FreeRTOS for this SDK release is under the `kernel` directory. Other middleware can be found under the `middleware` directory.

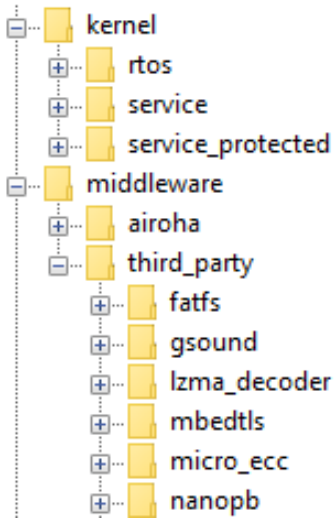

Figure 1. Source location of Airoha IoT SDK for BT Audio and Smart MCU open source software

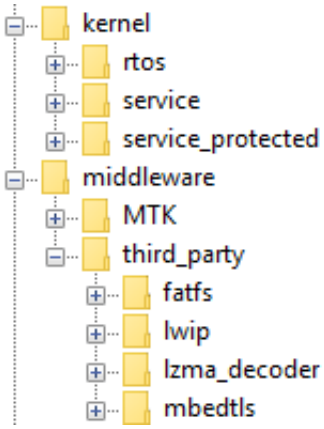

Figure 2. Source location of Airoha IoT SDK for Location open source software

The supported modules on different chips are listed in Table 3; chips of each product line are shown below.

- Airoha IoT SDK for Smart MCU: MT2523/MT2533/AM255x
- Airoha IoT SDK for BT Audio: AB155x/AB1565/AB1568/AB158x/AB157x/AB1627
- Airoha IoT SDK for Location: AG3335/AG3352/AG3353

Table 3. Modules based on different chips

Module	Airoha IoT SDK for Smart MCU	Airoha IoT SDK for BT Audio	Airoha IoT SDK for Location
RTOS	✓	✓	✓

Module	Airoha IoT SDK for Smart MCU	Airoha IoT SDK for BT Audio	Airoha IoT SDK for Location
TCP/IP			✓
Mbed TLS		✓	✓
XML	✓		
FatFs	✓	✓	✓
CMSIS	✓	✓	✓
LZMA decoder	✓	✓	✓
Micro-ecc		✓	
OPUS Codec		✓	
nanopb		✓	
mSBC encoder		✓	
Gsensor-key	✓	✓	
pre_libloader	✓	✓	
newlib	✓	✓	✓
newlib-nano	✓	✓	✓
Libgcc	✓	✓	✓
STM IMU sensor		✓	

The following sections provide a more detailed description of each module.

2. RTOS: FreeRTOS

FreeRTOS is a real-time OS that manages a multitasking and multiprocessing system environment. It has a scheduler to manage user-created tasks. FreeRTOS provides APIs for you to control, synchronize, and communicate among various tasks.

2.1. Features

FreeRTOS supports the following five features to accomplish event/task scheduling and multitasking:

- Task and Scheduler – Each application consists of tasks or threads controlled by the operating system. The multitasking operation is implemented with a scheduler. The scheduler is in the kernel and manages the task execution at a specific time. The kernel can suspend and resume a task many times during lifecycle of the task execution.
- Queue – Queues are the primary forms of inter-task communications. In most cases they are used as thread safe first-in-first-out (FIFO) buffers.
- Semaphore – Threads use semaphores to control the access to shared resources.
- Software Timer – A software timer allows a function to be invoked at a predefined time. The timer callback functions are executed within the timer service task. It is essential to ensure the timer callback functions perform lightweight operations and return as quick as possible to avoid blocking the system resources.
- Event Group (enabled after FreeRTOS version 8.0.0) – An event group is a set of event bits. Individual event bits within an event group are referenced by a bit number. Event bits are used to indicate if an event has already occurred or not. Event groups can also be used to synchronize tasks.
- Refer to the official website for more information on FreeRTOS and its features.

2.2. Memory usage

Brief details on the memory usage can be found in FreeRTOS FAQ - Memory Usage, Boot Times & Context Switch Times website, under the following sections:

- How much RAM does FreeRTOS use?
- How much ROM/Flash does FreeRTOS use?

2.3. Heap service

`heap_4.c` is used as the heap service on the SDK. The implementation is extended with more algorithms that are not supported by the official FreeRTOS. More details can be found in the header file `<sdk_root>\kernel\rtos\FreeRTOS\Source\include\portable.h`.

Table 4 shows the descriptions for several APIs.

Table 4. Heap service APIs

Prototype	Description
`void *pvPortCalloc(size_t nmemb, size_t size)`	Provides the same functionality as the ISO C function `calloc()`.

Prototype	Description
	Allocates memory for an array "nmemb" with elements in bytes and returns a pointer to the allocated memory. The memory is set to zero. This function is available in SDK v1 and later.
void *pvPortRealloc(void *pv, size_t size)	Provides the same functionality as the ISO C function realloc(). Changes the size of the memory block pointed by "pv" to "size" in bytes. The contents will be unchanged in the range from the start of the region up to the minimum of the old and new sizes. If the new size is larger than the old size, the added memory will not be initialized. This function is available in SDK v1 and later.
void *pvPortMallocNC(size_t xWantedSize)	This function is similar to pvPortMalloc(). Allocates "xWantedSize" bytes and returns a pointer to the allocated memory, except the allocated memory is in non-cacheable region. This function is available in SDK v3 and later.
void vPortFreeNC(void *pv)	This function is similar to vPortFree(). Frees the memory space pointed by "pv". All cache related operations are directly implemented in the API and is transparent to users. This function is available in SDK v3 and later.

Heap is in cacheable region by default for performance critical operations, such as task stack. The functions pvPortMallocNC() and vPortFreeNC() are implemented for temporary non-cacheable memory requirements, such as when using DMAs.

- For convenient migration of third-party software and libraries, the SDK also supports C standard library heap implementation, including malloc(), calloc(), realloc() and free(). However, these functions are wrapped in FreeRTOS heap service functions pvPortMalloc(), pvPortCalloc(), pvPortRealloc() and vPortFree(), and require building the project with a FreeRTOS module.

2.4. Examples

The source code and API documentation of the FreeRTOS can be found here.

2.5. Customization

You can provide custom configuration for the FreeRTOS in FreeRTOSConfig.h header file. The configurable parameters include OS tick frequency, maximum priorities, disabled functions, etc.

Refer to the online documentation for more information.

2.6. Limitations

Airoha does not impose any limitation on the integrated FreeRTOS. Refer to the official online resources for more detailed information.

2.7. Developer notes

- There is no software restriction on the number of tasks to create.
- The tasks can share the same priority.
- If the configuration `USE_PORT_OPTIMISED_TASK_SELECTION` is enabled, the maximum number of task priorities will be 32 (0 to 31) in the ARM Cortex-M4 with floating point porting.
- Only API functions that end in "`FromISR`" can be called from within an interrupt service routine. Refer to Open RTOS API documentation for more details.
- APIs that can potentially cause a context switch must not be called while the scheduler is suspended.
- APIs that can potentially cause a context switch must not be called from within a critical section.

3. TCP/IP: lwIP

TCP/IP (Transmission Control Protocol/Internet Protocol) is a communication internet protocol and can also be used in a private network, either an intranet or an extranet. It provides specifications on how data should be packetized, addressed, transmitted, routed, and received at the destination. The current IoT standard is moving towards IP communication and transporting data over various physical layers such as Wi-Fi, IEEE 802.15.4, and Bluetooth.

3.1. Features

lwIP is a widely used open-source TCP/IP stack designed for embedded systems. It includes the IP, ICMP, TCP, UDP, IGMP, ARP, AutoIP, DHCP, DNS, and SNMP protocols. The SDK provides the following supported features for these protocols.

- IPv4 (LWIP_IPV4).
- UDP (LWIP_UDP) – User Datagram Protocol, the widely adopted connectionless transmission protocol.
- TCP (LWIP_TCP) – Transmission Control Protocol, a widely used transport protocol providing reliable and in-order delivery.
- ARP (LWIP_ARP).
- ICMP (LWIP_ICMP).
- DHCP (LWIP_DHCP).
- DNS (LWIP_DNS).
- NETCONN (LWIP_NETCONN).
- Socket (LWIP_SOCKET).
- lwIP provides only the memory manager and the pbuf module to the lower layer for the location chip.

3.2. Memory usage

The MEM_SIZE parameter defines the size of the heap memory. PBUF_RAM, stores the sent and received data. If the application requires more data to send, the value of the parameter must be set higher.

The values of different control blocks are configurable in lwIP. For example, MEMP_NUM_NETDB sets the concurrent Domain Name Resolution connections. Table 5 lists the current configuration values of these blocks in the SDK. These pools are configured and allocated from a buffer reserved only for the lwIP. The values are set to pass internal performance test and are expected to fulfill most common use cases. However, the number of configured control blocks could limit the maximum concurrent connections created by the network applications in the system. Developers can configure these values for a specific use case.

Table 5. Number of control blocks and buffers in lwIP

Name	Current value
MEMP_NUM_UDP_PCB	4
MEMP_NUM_TCP_PCB	8
MEMP_NUM_TCP_PCB_LISTEN	16
MEMP_NUM_REASSDATA	5

Name	Current value
MEMP_NUM_NETBUF	2
MEMP_NUM_NETCONN	10
MEMP_NUM_TCPIP_MSG_API	8
MEMP_NUM_TCPIP_MSG_INPKT	8
MEMP_NUM_SYS_TIMEOUT	16
MEMP_NUM_NETDB	1
MEMP_NUM_PBUF	16
PBUF_POOL_SIZE	10

The required code size of lwIP is listed in Table 6. This information is gathered from an ARM Cortex M4 targeted configuration using the `gcc -Os` optimization. The feature set is IPv4, TCP, UDP, DHCP client, ICMP, RAW, NETCONN and Sockets and DNS client. The footprint is shown below.

Table 6. Footprint of the lwIP

Static footprint	ROM (bytes)	RAM (bytes)
Debug release	45541	51861

Note: If the options `LWIP_DEBUG`, `LWIP_ERROR`, `LWIP_ASSERTS` or `LWIP_STATS` are enabled, then the application has a significantly larger code footprint. Similarly, if the application is built with the compiler `-O0` optimization flag on, the footprint is again significantly affected.

3.3. Examples

None

3.4. Customization

None

3.5. Limitations

None.

3.6. Developer notes

None

4. Mbed TLS

Mbed TLS is a C library that implements cryptographic primitives, X.509 certificate manipulation, and the SSL/TLS and DTLS protocols. Its small code footprint makes it suitable for embedded systems. Mbed TLS includes a reference implementation of the PSA Cryptography API.
Starting from the version 2.1.0, Mbed TLS is released under Apache 2.0 license and enables developers to use Mbed TLS in both open source and closed source projects.

4.1. Features

Mbed TLS is an open-source and commercial SSL library licensed under ARM Limited. This library easily integrates with new and existing (embedded) devices and applications and provides the building blocks for secure communication, cryptography, and key management. The cryptographic algorithms enabled in the SDK include:

1) Symmetric encryption algorithms: AES, Triple-DES (3DES), DES.
2) Modes of operation: Cipher Block Chaining Mode (CBC), Cipher Feedback mode (CFB), Counter Block Cipher mode (CTR), Output Feedback mode (OFB), Xor-encrypt-xor with ciphertext stealing mode (XTS).
3) Hash algorithms: MD5, SHA-1, and SHA-256.
4) RSA/PKCS#1 v1.5.
5) Random number generation: CTR_DRBG.
6) ECDH, ECDSA.

4.2. Memory usage

Airoha offers basic configuration in the BT Audio release package via `config-vendor-fota-race-cmd.h`. The configuration includes minimal algorithm support for key exchange, cipher and hash algorithms.

Refer to the sample configuration file `config-vendor-fota-race-cmd.h` under `<sdk_root>/middleware/third_party/mbedtls/configs/`.

`config-vendor-fota-race-cmd.h` enables some commonly used cryptographic algorithms. Table 7 shows the ROM size that is required for these two default configurations.

Table 7. Footprint of Mbed TLS

Memory (Bytes)	config-vendor-fota-race-cmd configure
ROM	36088
RAM	32

4.3. Examples

Mbed TLS is equipped with test cases. You can use them as a reference to develop and learn how to use the APIs. Several example applications are under `<sdk_root>/mcu/middleware/third_party/mbedtls/programs`.

4.4. Customization

`config-vendor-fota-race-cmd.h` is the default configuration file for Mbed TLS in the SDK release and is under `<sdk_root>/middleware/third_party/mbedtls/configs/config-vendor-fota-race-cmd.h`. You can put your custom configuration file under `<sdk_root>/middleware/third_party/mbedtls/configs` and set the file name to the variable

MTK_MBEDTLS_CONFIG_FILE in the project's feature.mk makefile, for example, <sdk_root>/mcu/project/<chip>/apps/earbuds_ref_design/GCC/feature.mk.

You must define MBEDTLS_DEBUG_C in the configuration file to enable debugging and error handling.

You can switch the feature options in the configuration file and define some of the values or parameters. The configuration file is well documented. Refer to the comments in <sdk_root>/middleware/third_party/mbedtls/include/mbedtls/mbedtls_config.h.

4.5. Limitations

None.

4.6. Developer notes

1) In order to keep Mbed TLS thread safe, it is important to keep a few things in mind.
2) Most functions use an explicit context. As long as the context is not shared among the threads, the application is thread safe. However, sometimes a context is shared indirectly. For example, an SSL context can point to an RSA context (the private key).
3) The rule of thumb is that a context should only be used or accessed by a single thread at a time, unless:
   a) The function is documented explicitly that it is thread safe to access the shared context, or
   b) You have applied an explicit locking mechanism, such as a mutex, to protect a critical section through a wrapper function.

5. XML: Mini-XML

Extensible Markup Language (XML) is a markup language that defines a set of rules for encoding documents in a format, which is both human and machine-readable. It is defined by the W3C's XML 1.0 specification and by several other related specifications, all of which are free open standards.

Although the design of XML focuses on documents, it is widely used for the representation of arbitrary data structures such as those used in web services.

5.1. Features

The Mini-XML module is a small XML library that enables parsing the XML and XML-like data in the application without requiring large non-standard libraries. It supports reading of UTF-8 and UTF-16 and writing of UTF-8 encoded XML strings. Data is stored in a linked-list tree structure, preserving the XML data hierarchy and arbitrary element names, attributes and attribute values are supported with no preset limits, just available memory.

5.2. Memory usage

The XML library of the SDK has two build types, full and basic with optimized and reduced memory footprint.

The XML module provides several compilation options to disable unused sub-modules. Overall, the ROM size of most basic version is 13052 bytes. It is 21940 bytes if all of the sub-modules are enabled, see section 5.4, “Customization”.

5.3. Examples

1) File: `<sdk_root>/project/<chip>/apps/xml/src/main.c.`
2) Application Name: XML sample application.
3) Application Overview: The sample application is a reference to parse a typical XML file by means of this XML library. The developers can refer to this simple application and reuse the functions in their applications.

5.4. Customization

The XML module provides compile options to enable or disable sub-features. Table 8 shows the details.

Table 8. Options and footprint of XML

Compilation option	Function	ROM size (bytes)
MXML_SUPPORT_ENTITY	Convert the reserved characters in XML to corresponding character entities.	4584
MXML_SUPPORT_GET_FUNCTIONS	Get the attribute and content of an element	892
MXML_SUPPORT_SET_FUNCTIONS	Set the attribute and content of an element.	1040
MXML_SUPPORT_INDEX	Create the index with all elements, the elements are sorted by element name or attribute value.	1560
MXML_SUPPORT_SEARCH	Find the element by name.	812

5.5. Limitations

For the platforms that do not support file system, such as LinkIt 7687 development board, the file system related API sets of `mxml*Fd()` and `mxml*File()` are not accessible, and the XML objects can only be loaded and saved by `mxml*String()` APIs.

5.6. Developer notes

None.

6. File System: FatFs

FatFs is a generic FAT (File Allocation Table) file system that manages the access to storage devices for small embedded systems. FatFs is written in compliance with ANSI C (C89) and is completely separated from the disk I/O layer.

6.1. Features

To facilitate the data (files or directories) management in the storage device, FatFs provides the following features:

- Windows OS compatible FAT file system.
- Platform independent, easy to port to any target platform.
- Small footprint for code and work area (file system objects, file objects, etc.).
- Switches character encoding (ANSI/OEM and UTF-16 for R0.12b and R0.14b, UTF-8 and UTF-32 only for R0.14b) for the file name on the API.
- RTOS support for multi-tasking.
- Multiple sector size support up to 4kB.
- Read-only, optional API, I/O buffer and other features.

More information on the FatFs and its features can be found here.

6.2. Memory usage

The memory usage varies depending on the configuration options, as described in section 6.4, “Customization”. The FatFs module provides several compilation options to disable unused sub-modules. Overall, according to the configuration in the SDK release, the ROM size of the current release is 12367 bytes, the RAM size is 25 bytes. The memory size may be different for different versions of the SDK.

6.3. Examples

The source code and API documentation of the FatFs can be found at official website.

6.4. Customization

You can customize the configuration for the FatFs in `ffconf.h` header file. The configurable parameters include the volume to support, the sector size, etc.

More details can be found in the online documentation.

6.5. Limitations

- The list of feature limitations for the FatFs is provided below.
- Multiple sector size support up to 4kB.
- FAT sub-types - FAT12, FAT16 and FAT32.
- Number of open files - unlimited depending on the available memory.
- Number of volumes - up to 10.

- File size - up to 4GB minus 1 byte. (FAT specifications.)
- Volume size - up to 2TB at 512 bytes per sector. (FAT specifications.)
- Cluster size - up to 64kB at 512 bytes/sector. (FAT specs.)
- Sector size - 512, 1024, 2048 and 4096 bytes. (FAT specs.)

6.6. Developer notes

FatFs integrated in current SDK version only supports file system on the SD (Secure Digital Memory Card) or eMMC (Embedded Multi Media Card) without flash.

- FatFs is not enabled by default in order to provide the you with the flexibility to select whether to use this file system or a different file system.
- Due to changes in API and feature options of configuration files from R0.12b to R0.14b, in order to be compatible with existing customers, the current SDK supports multiple versions (R0.12b and R0.14b).
- R0.14b can support two partitions on storage disk with a capacity less than 500KB, while R0.12b requires at least 1MB. In addition, R0.14b adds API support for UTF-8 and UTF-32.
- The feature option prefix of R0.14b is “FF_”, while R0.12b is “_”.
- Developers can add Fatfs module.mk to the Makefile to import FatFs support, and specify FATFS_VERSION in the Makefile to select different versions. The currently supported versions are R0.12b and R0.14b.
- During compilation, developers can use the __FATFS_VERSION__ macro to confirm the actual FatFs version used to write the compatible code. If __FATFS_VERSION__ is 201609L, then the current version is R0.12b and if __FATFS_VERSION__ is 202104L, then the current version is R0.14b.

7. CMSIS

The CMSIS (Cortex Microcontroller Software Interface Standard) is a vendor-independent hardware abstraction layer for the Cortex-M processor series and defines generic tool interfaces. The CMSIS enables consistent device support and simple software interfaces to the processor and the peripherals, real-time operating systems and middleware components simplifying software re-use, reducing the learning curve for microcontroller developers and reducing the time to market for new devices.

The CMSIS is intended to enable the combination of software components from multiple middleware vendors.

7.1. Features

The CMSIS components are:

- CMSIS-CORE – Implements basic run-time APIs for a Cortex-M device and provides convenient access to the processor core and the device peripherals.
- CMSIS-DSP – This library provides a suite of common signal processing functions to apply on Cortex-M processor based devices.

Airoha IoT development platform only includes CMSIS-CORE and CMSIS-DSP.

7.2. Memory usage

CMSIS-CORE APIs are implemented in header files, so the memory usage is added to the source file. The library size of the CMSIS-DSP is up to 5.4MB and the actual memory usage depends on which and how many APIs are in use.

7.3. Examples

CMSIS API examples can be found at official website.

7.4. Customization

No customization or configuration is required to use CMSIS-CORE and CMSIS-DSP, apply them directly in your implementation.

7.5. Limitations

None.

7.6. Developer notes

Airoha IoT Development Platform only includes CMSIS-CORE and CMSIS-DSP.

- We strongly recommend that you visit the official ARM website to find more detailed information about CMSIS.

8. LZMA: LZMA Decoder

The LZMA is an algorithm performing lossless data compression. The LZMA SDK provides a high compression ratio and fast decompression. The Airoha IoT SDK only uses the LZMA decoder algorithm.

8.1. Features

The LZMA decoder provides functions to decompress the compressed data encoded by LZMA encoder.
LZMA decoder has the following features:

- Small memory requirements: 8 to 32kB + Dictionary Size.
- Small code size: 2 to 8kB, depending on speed optimizations.

More information on the LZMA and its features can be found here.

8.2. Memory usage

The memory usage is shown in Table 9.

Table 9. LZMA decoder memory usage

ROM (bytes)	RAM, static analysis (bytes)
6338	40*1024 (suggested decoding buffer)

8.3. Examples

LZMA decoder module is used to decode FOTA package file in bootloader only. Because Cortex-M4 binary size is usually too large to have enough buffer for the whole file data, there is a wrapper function `lzma_decode2flash()` provided in `<sdk_dir>/middleware/third_parity/lzma_decoder/inc/lzma_decoder_interface.h` header file to decode data block by block, then call HAL flash API to write the output data to the specified address on NOR flash.

More information on the LZMA decoder interface can be found in the bootloader module's source file (`<sdk_dir>/driver/board/<chip>/bootloader/core/src/bl_fota.c`).

8.4. Customization

None.

8.5. Limitations

None.

8.6. Developer notes

LZMA decoder integrated in current version of the SDK only supports bootloader decoding.

9. Micro-ecc

A small and fast ECDH and ECDSA implementation for 8-bit, 32-bit, and 64-bit processors.

9.1. Features

- Resistant to known side-channel attacks.
- Written in C, with optional GCC inline assembly for AVR, ARM, and Thumb platforms.
- Support for 8-bit, 32-bit, and 64-bit architectures.
- Small code size.
- No dynamic memory allocation.
- Support for five standard curves: secp160r1, secp192r1, secp224r1, secp256r1, and secp256k1.
- BSD 2-clause license.

9.2. Memory usage

None

9.3. Examples

None

9.4. Customization

None

9.5. Limitations

None

9.6. Developer notes

None

10. OPUS OGG Codec

Audio Codec library to support to OGG file and OPUS file

10.1. Features

- Support encoder and decoder.
- Only support 16K sampling rate
- Support 16K~256K bitrate
- Frame size : 20ms
- Support Speech/music
- Only support mono.

10.2. Memory usage

Table 10. OPUS OGG Codec usage

	ROM (bytes)	RAM, static analysis (bytes)
OPUS OGG	136366	12000

10.3. Examples

- File: `<sdk_root>/project/<chip>/apps/iot_sdk_demo/opus_proc.c.`
- Feature Name: ./build.sh iot_sdk_demo -f=feature_ogg_opus.mk
- Feature Overview: refer to iot_sdk_demo/readme.txt

10.4. Customization

None

10.5. Limitations

Not support multi thread.

10.6. Developer notes

None

11. nanopb

Nanopb is an ANSI-C library for encoding and decoding messages in Google's Protocol Buffers format with minimal requirements for RAM and code space. It is primarily suitable for 32-bit microcontrollers.

11.1. Features

- Pure C runtime
- Allows specifying maximum size for strings and arrays, so that they can be allocated statically.
- No malloc needed: everything can be allocated statically or on the stack. Optional malloc support available.
- You can use either encoder or decoder alone to cut the code size in half.
- Support for most protobuf features, including: all data types, nested submessages, default values, repeated and optional fields, oneofs, packed arrays, extension fields.
- Callback mechanism for handling messages larger than can fit in available RAM.
- Extensive set of tests.

11.2. Memory usage

- Small code size (5–10 kB depending on processor and compilation options, plus any message definitions)
- Small ram usage (typically ~300 bytes stack, plus any message structs)

11.3. Examples

None

11.4. Customization

None

11.5. Limitations

- Some speed has been sacrificed for code size.
- Encoding is focused on writing to streams. For memory buffers only it could be made more efficient.
- The deprecated Protocol Buffers feature called "groups" is not supported.
- Fields in the generated structs are ordered by the tag number, instead of the natural ordering in .proto file.
- Unknown fields are not preserved when decoding and re-encoding a message.
- Reflection (runtime introspection) is not supported. E.g. you cannot request a field by giving its name in a string.
- Numeric arrays are always encoded as packed, even if not marked as packed in .proto.
- Cyclic references between messages are supported only in callback and malloc mode.

- Nanopb does not have a stable ABI (application binary interface) between versions, so using it as a shared library (.so / .dll) requires extra care.

11.6. Developer notes

None

12. mSBC: mSBC Encoder

Modified version of the SBC codec (hereafter called mSBC) is mandatory if Wide Band Speech is supported in Bluetooth HANDS-FREE Profile. The original SBC codec is specified in A2DP (Advanced Audio Distribution Profile). The changes to the A2DP SBC are limited to the frame header syntax and semantics. All other parts of the SBC definition remain un-modified.

The Airoha IoT SDK only uses the mSBC encoder of open source software.

12.1. Features

- Available bitrate is 63 kb/s only
- Sampling rate is 16 kHz only
- Frame size is 15 ms only
- Support for speech and music

12.2. Memory usage

For the mSBC encoder library, ROM size is 2.3 Kbytes.

12.3. Examples

None

12.4. Customization

None

12.5. Limitations

None

12.6. Developer notes

None

13. Gsensor-key

A module used to realize single tap and double tap by Gsensor.

13.1. Features

- Support single tap and double tap
- BSD 3-clause license

13.2. Memory usage

None

13.3. Examples

None

13.4. Customization

None

13.5. Limitations

None

13.6. Developer notes

None

14. Pre_libloader

A module that allows dynamically loading and unloading code to execute on an Xtensa processor without the aid of an operating system.

14.1. Features

- A mechanism which dynamically loading and unloading code to execute.
- Provides code and data memory sizes about loadable library.
- Relocated the dynamical library when loading other memory region.
- Loadable lib is a standalone program.

14.2. Memory usage

ROM size is 18863 bytes for the pre_libloader driver library.

14.3. Examples

The source code and API documentation of the pre_libloader can be found.

14.4. Customization

None

14.5. Limitations

The dynamical library must include the –fpic and -mlongcalls options (which are not included in the static library) when building library.a. You must generate the dynamic library from a static library. Otherwise, it may not operate as expected.

14.6. Developer notes

If you need to use the function of pre_libloader, follow the operation flow of DSP API reference document.

15. STM IMU sensor

A module used to obtain the data from IMU sensors.

15.1. Features

- Support STMicroelectronics LSM6DSOW sensor

15.2. Memory usage

None

15.3. Examples

None

15.4. Customization

None

15.5. Limitations

None

15.6. Developer notes

None