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| FPGA device selected: | xc6slx100 |
| Total Number of occupied Slices: | 15822 |
| Total Number of Slice Flip Flops: | 126576 |
| Total Number of Slice LUTs: | 63288 |
| Cortex-M0 Number of occupied Slices: | 1045 / 1076 |
| Cortex-M0 Number of Slice Flip Flops: | 868 / 826 |
| Cortex-M0 Number of Slice LUTs: | 3147 / 3168 |
| Bridge Number of occupied Slices: | 7 / 6 |
| Bridge Number of Slice Flip Flops: | 4 / 4 |
| Bridge Number of Slice LUTs: | 5 / 4 |
| Best possible clock frequency: | 77.863MHz |