



"Think Big...
Think Beyond"

ITM(SLS) BARODA UNIVERSITY
SCHOOL OF COMPUTER SCIENCE, ENGINEERING AND TECHNOLOGY (SOCSET)
ODD SEMESTER
<CET-1> SYLLABUS

PROGRAM: <B. TECH>

BRANCH: CSE/IT/AI/CSN

SEMESTER: III

SUB-CODE: C2310C2

SUB-NAME: COMPUTER ARCHITECTURE

Unit # (As per University Syllabus)	Unit Name	Topics
1	Data storage and register transfer operations	Register Transfer and Micro-operations: Register Transfer language, Register Transfer, Bus and Memory Transfers, Arithmetic Micro operations, Logic Micro-Operations, Shift Micro-Operations, Arithmetic logical shift unit. -binary addition and subtraction -Logic gates, half adder and full adder
2	Basic Computer Organization and Design	Instruction codes, Computer registers, computer instructions, Timing and Control, Instruction cycle, Memory-Reference Instructions, Register- reference instruction, Input-output instruction Design of Basic computer, Design of Accumulator Unit.

% Syllabus covered: 30%