



# SRI KRISHNADEVARAYA UNIVERSITY

ANANTAPURAM - 515003, ANDHRA PRADESH, INDIA



Sl. No.: 001899

## CONSOLIDATED MARKS MEMO

Name : Boddu Harshavardhan

Reg. No. : 1210210

Course : B.Tech - Electronics & Communication Engineering

Month & Year of Pass: APRIL 2019

Year of Admission : 2012

Class Awarded : SECOND CLASS

S.No.	Course Title	Int. Marks	Ext. Marks	TOTAL	Credits	S.No.	Course Title	Int. Marks	Ext. Marks	TOTAL	Credits
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### I YEAR Total:1000 Marks

1	English	21	42	63	4	7	Engineering Drawing	22	48	70	6
2	Engineering Physics	20	37	57	4	8	Programming Lab	23	45	68	4
3	Engineering Chemistry	24	25	49	4	9	Engineering and IT Workshop	22	35	57	4
4	Mathematics-I	22	25	47	6	10	Engg. Physics & Engg. Chemistry Lab	20	45	65	4
5	Intro. To Problem Solving and Programming	22	35	57	6	11	English Communication Skills Lab	22	40	62	4
6	Mathematical Methods	20	25	45	6		Total			634	

### II YEAR 1<sup>st</sup> SEMESTER Total :750 Marks

1	Mathematics - III	27	39	66	4
2	Environmental Studies	17	30	47	4
3	Electronic Devices and Circuits	26	51	77	4
4	Signals and Systems	17	34	51	4
5	Network Analysis & Synthesis	17	48	65	4
6	Switching Theory & Logic Design	17	30	47	4
7	Electronic Devices and Circuits Lab	23	38	61	2
8	Basic Simulation Lab Using MATLAB	22	39	61	2
	Total			475	

### II YEAR 2<sup>nd</sup> SEMESTER Total :750 Marks

1	Managerial Economics & Financial Analysis	17	36	53	4
2	Electrical Technology	19	35	54	4
3	EM Waves and Transmission Lines	13	28	41	4
4	Electronic Circuits Analysis	22	29	51	4
5	Pulse and Digital Circuits	14	33	47	4
6	Probability Theory & Stochastic Processes	14	26	40	4
7	Electrical Circuits Analysis Lab	16	32	48	2
8	Electrical Engineering Lab	22	42	64	2
	Total			398	

### III YEAR 1<sup>st</sup> SEMESTER Total :750 Marks

1	Analog Communications	15	34	49	4
2	Object Oriented Programming and Data Structures	18	26	44	4
3	Antennas and Wave Propagation	15	40	55	4
4	Analog IC Applications	19	31	50	4
5	Digital IC Applications	18	36	54	4
6	Control Systems	17	36	53	4
7	Analog & Digital IC Applications Lab	17	22	39	2
8	Pulse and Digital Circuits Lab	16	24	40	2
	Total			384	

### III YEAR 2<sup>nd</sup> SEMESTER Total :750 Marks

1	Digital Signal Processing	22	48	70	4
2	Microprocessors & Microcontrollers	15	28	43	4
3	Computer Organization and Architecture	19	26	45	4
4	Microwave Engineering	15	32	47	4
5	Electronic Measurements and Instrumentation	15	32	47	4
6	Digital Communications	19	34	53	4
7	Analog & Digital Communications Lab	21	35	56	2
8	Microprocessors & Microcontrollers Lab	16	33	49	2
	Total			410	

### IV YEAR 1<sup>st</sup> SEMESTER Total : 800 Marks

1	Optical Communications	23	31	54	4
2	Digital Image Processing	21	30	51	4
3	VLSI System Design	19	43	62	4
4	Management Science	23	32	55	4
5	Embedded Systems	19	31	50	4
6	Cellular and Mobile Communications	25	38	63	4
7	Microwave & Optical Communications Lab	20	39	59	2
8	DSP & DIP Lab	18	41	59	2
9	Mini Project	**	40	40	2
	Total			493	

### IV YEAR 2<sup>nd</sup> SEMESTER Total : 500 Marks

1	Radar Engineering	22	27	49	4																																																
2	Computer Networks	20	37	57	4																																																
3	Project Work	40	75	115	12																																																
4	Seminar	40	**	40	2																																																
5	Comprehensive Viva-Voce	**	77	77	4																																																
	Total			338																																																	
<table> <tr> <th></th><th colspan="3">Maximum marks</th><th colspan="2">Minimum for Pass</th></tr> <tr> <th></th><th>INT</th><th>EXT</th><th>TOTAL</th><th>EXT</th><th>TOTAL</th></tr> <tr> <td>THEORY :</td><td>30</td><td>70</td><td>100</td><td>25</td><td>40</td></tr> <tr> <td>PRACTICAL :</td><td>25</td><td>50</td><td>75</td><td>18</td><td>30</td></tr> <tr> <td>PROJECT :</td><td>50</td><td>150</td><td>200</td><td>50</td><td>-</td></tr> <tr> <td>MINI PROJECT :</td><td>50</td><td>**</td><td>50</td><td>-</td><td>-</td></tr> <tr> <td>SEMINAR :</td><td>50</td><td>**</td><td>50</td><td>-</td><td>-</td></tr> <tr> <td>COM. IVE VIVA-VOCE</td><td>**</td><td>100</td><td>100</td><td>50</td><td>-</td></tr> </table>							Maximum marks			Minimum for Pass			INT	EXT	TOTAL	EXT	TOTAL	THEORY :	30	70	100	25	40	PRACTICAL :	25	50	75	18	30	PROJECT :	50	150	200	50	-	MINI PROJECT :	50	**	50	-	-	SEMINAR :	50	**	50	-	-	COM. IVE VIVA-VOCE	**	100	100	50	-
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SEMINAR :	50	**	50	-	-																																																
COM. IVE VIVA-VOCE	**	100	100	50	-																																																

Aggregate Marks Secured for best : 2498 (OUT OF 4300) PERCENTAGE : 58.09

Dated : 26-11-2020

Asst.

Supdt.

APDR

CONTROLLER OF EXAMINATIONS