Module 1

0 I

1. Explain the internal architecture of Intel 8085 microprocessor.
2. Describe the block diagram of Intel 8085 microprocessor.
3. What are the functional details of the pins in Intel 8085 microprocessor?
4. How does external address/data bus multiplexing and demultiplexing work in Intel 8085 microprocessor?
5. Discuss the internal bus organization of Intel 8085 microprocessor.
6. What control signals are used in Intel 8085 microprocessor?0 I:

1. What is the internal architecture of Intel 8085 microprocessor?
2. Explain the block diagram of the Intel 8085 microprocessor.
3. What are the registers used in Intel 8085 microprocessor?
4. How is the internal bus organization of Intel 8085 microprocessor structured?
5. What are the functional details of pins in Intel 8085 microprocessor?
6. Explain the control signals used in Intel 8085 microprocessor.
7. How is the external address/data bus multiplexing and demultiplexing done in Intel 8085 microprocessor?0 I:
1. What is the internal architecture of the Intel 8085 microprocessor?
2. What are the functional details of the pins of the Intel 8085 microprocessor?
3. How does the external address/data bus multiplexing and demultiplexing work?
4. What are the control signals of the Intel 8085 microprocessor?0 I:

1. What is the internal architecture of Intel 8085 microprocessor?
2. Explain the block diagram of Intel 8085 microprocessor.
3. What are the registers used in the Intel 8085 microprocessor?
4. How is the internal bus organized in the Intel 8085 microprocessor?
5. Explain the functional details of pins in the Intel 8085 microprocessor.
6. What are the control signals used in the Intel 8085 microprocessor?
7. Explain the external address/data bus multiplexing and demultiplexing in the Intel 8085 microprocessor.0 I:
1. What is the internal architecture of the Intel 8085 microprocessor?
2. What are the components of the block diagram of the Intel 8085 microprocessor?
3. What are the functional details of the pins of the Intel 8085 microprocessor?
4. How is the external address/data bus multiplexed and demultiplexed in the Intel 8085 microprocessor?

Module 2

1 II

1. What is the 8085 instruction set and how is it classified?
2. Describe the different addressing modes of the 8085 microprocessor.
3. Provide programming examples for 8085 instruction set.
4. How is instruction timing determined in the 8085 microprocessor?
5. What is the difference between I/O mapped I/O and memory mapped I/O in the context of 8085 microprocessor?
6. Explain interrupts in the 8085 microprocessor.1 II:

1. What is the instruction set of Intel 8085 microprocessor?
2. How are the instructions for Intel 8085 microprocessor classified?
3. What are the different types of addressing modes used in Intel 8085 microprocessor?
4. Provide programming examples for Intel 8085 microprocessor.
5. How is the instruction timing calculated for Intel 8085 microprocessor?
6. What are the different techniques for I/O mapped and memory mapped I/O in Intel 8085 microprocessor?
7. What is the role of interrupts in Intel 8085 microprocessor?1 II:
1. What is the instruction set of the Intel 8085 microprocessor?
2. How are the instructions of the Intel 8085 microprocessor classified?
3. What are the addressing modes of the Intel 8085 microprocessor?
4. What are the programming examples of the Intel 8085 microprocessor?
5. What are the I/O mapped I/O and memory mapped I/O techniques?
6. What are the interrupts of the Intel 8085 microprocessor?1 II:

1. What is the 8085 instruction set?
2. How are the instructions of Intel 8085 microprocessor classified?
3. Explain the addressing modes used in the Intel 8085 microprocessor.
4. Give some programming examples for the Intel 8085 microprocessor.
5. What is the instruction timing in the Intel 8085 microprocessor?
6. Explain the I/O mapped I/O and memory mapped I/O techniques used in the Intel 8085 microprocessor.
7. What are the interrupts of the Intel 8085 microprocessor?1 II:
1. What is the 8085 instruction set?
2. What are the different classifications of instructions in the 8085 microprocessor?
3. What are the different addressing modes available in the 8085 microprocessor?
4. How can we program using the 8085 microprocessor instruction set?
5. What are the different I/O techniques used in the 8085 microprocessor?

6. What are the different memory mapped I/O techniques in the 8085 microprocessor?
7. What are the different interrupt techniques available in the 8085 microprocessor?

Module 3

2 III

1. What is the 8086 microprocessor and what is its architecture?
2. Explain the different addressing modes of 8086 microprocessor.
3. What are assembler directives and how do they work in 8086 programming?
4. Discuss the minimum mode and maximum mode configurations of 8086 hardware design.
5. Describe the bus structure of 8086 microprocessor.
6. What are interrupts in 8086 microprocessor?2 III:

1. What is the introduction of 8086 microprocessor?
2. Explain the architecture of 8086 microprocessor.
3. What are the various addressing modes used in 8086 microprocessor?
4. What is the instruction set for 8086 microprocessor and how is programming done using it?
5. What are the assembler directives used for 8086 microprocessor?
6. Explain the minimum mode and maximum mode configurations for 8086 microprocessor.
7. How is the bus structure, bus buffering, latching, and system bus timing done with diagrams for 8086 microprocessor?
8. What is the role of interrupts in 8086 microprocessor?2 III:
1. What is the architecture of the Intel 8086 microprocessor?
2. What are the addressing modes of the Intel 8086 microprocessor?
3. What is the instruction set of the Intel 8086 microprocessor?
4. What are the assembler directives of the Intel 8086 microprocessor?
5. What are the hardware designs of the Intel 8086 microprocessor?
6. What are the interrupt of the Intel 8086 microprocessor?2 III:

1. What is the 8086 architecture?
2. Explain the addressing modes used in the Intel 8086 microprocessor.
3. What is the instruction set and programming of the Intel 8086 microprocessor?
4. What are the assembler directives used in the Intel 8086 microprocessor?
5. Explain the minimum mode and maximum mode configurations of the Intel 8086 microprocessor.
6. How is the bus structure of Intel 8086 microprocessor designed?
7. Explain the interrupt of the Intel 8086 microprocessor.2 III:
1. What is the 8086 microprocessor architecture?
2. What are the different addressing modes available in the 8086 microprocessor?
3. How can we program using the 8086 microprocessor instruction set?
4. What are the different assembler directives in the 8086 microprocessor?
5. What are the different hardware configurations available in the 8086 microprocessor?
6. How do we handle interrupts in the 8086 microprocessor?

Module 4

3 IV

1. How is memory interfacing achieved using 8085 and 8086 microprocessors?
2. Describe the process of I/O interfacing using 8085 and 8086 microprocessors.
3. Explain the use of parallel communication interface (8255) in relation to 8085 and 8086 microprocessors.
4. Discuss the role of timer (8253/8254), keyboard/display controller (8279), interrupt controller (8259), and DMA controller (8257) in 8085 and 8086 microprocessors.3 IV:

1. How is the I/O and memory interfacing done using 8085 microprocessor?
2. Explain memory interfacing and I/O interfacing with 8085 microprocessor.
3. What are the different parallel communication interfaces used in 8085 microprocessor and explain their functions?
4. What are the different timers used in 8085 microprocessor and explain their functions?
5. How is keyboard/display controller (8279) used in 8085 microprocessor and explain its function?
6. What is the role of interrupt controller (8259) in 8085 microprocessor?
7. What is the role of DMA controller (8257) in 8085 microprocessor?3 IV:
1. How is memory interfacing accomplished using the Intel 8085 and 8086 microprocessors?
2. How is I/O interfacing accomplished using the Intel 8085 and 8086 microprocessors?
3. What is the Parallel Communication Interface (8255)?
4. What is the Timer (8253/8254)?
5. What is the Keyboard/Display Controller (8279)?
6. What is the Interrupt Controller (8259)?
7. What is the DMA Controller (8257)?3 IV:

1. How is memory interfacing done using Intel 8085 and 8086 microprocessor?
2. What is I/O interfacing and how is it done using Intel 8085 and 8086 microprocessor?
3. Explain the parallel communication interface (8255).
4. What is the function of the timer (8253 / 8254)?

5. How does the Keyboard / Display controller (8279) work?
6. Explain the interrupt controller (8259).
7. How is the DMA controller (8257) used?3 IV:
1. How can we interface I/O and memory using the 8085 and 8086 microprocessors?
2. What are the different memory interfacing techniques available using the 8085 and 8086 microprocessors?
3. How can we interface peripheral devices such as the Parallel Communication Interface (8255), Timer (8253/8254), Keyboard/Display controller (8279), Interrupt controller (8259), and DMA controller (8257) using the 8085 and 8086 microprocessors?