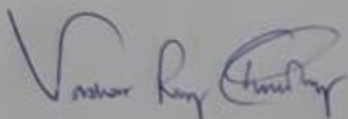


INDIAN INSTITUTE OF TECHNOLOGY ROORKEE

Date: Nov 29, 2017

TO WHOM IT MAY CONCERN

This is to certify that **Mr. Aniket singh, Mr. Harshit Bansal, Mr. Keshav Tiwari, Mr. Kundan Kumar, Mr. Samriddh Bhalla, Mr. Shivanshu Raj Shrivastava** students of B. Tech. in Electronics and Communication Engineering, IIT Roorkee have successfully completed a **project on Design and simulation (using VHDL) of a cache memory with advanced features (such as pre-fetching) for providing faster access**, under my guidance, as a part of **CSN-221 Computer Architecture and Microprocessor**.



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