## COL215L: Digital Logic & System Design

Lecture 15: Binary Arithmetic (Cont.)

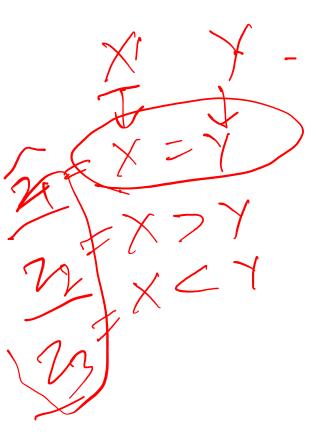


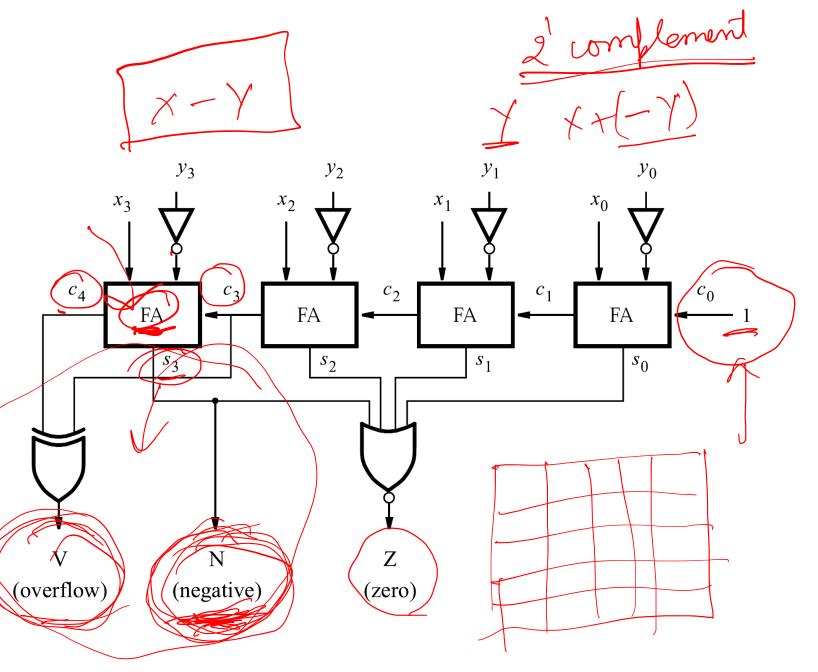
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September 10, 2021

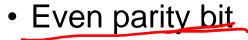
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## Comparator



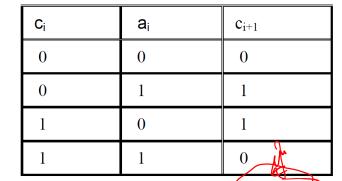


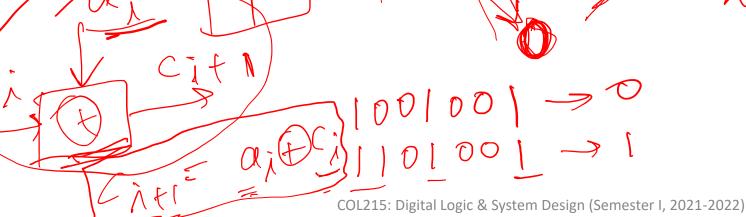
Parity Generation



• 1 if number of 1's in the data bits is even

an-1 an-2 -- a, a0



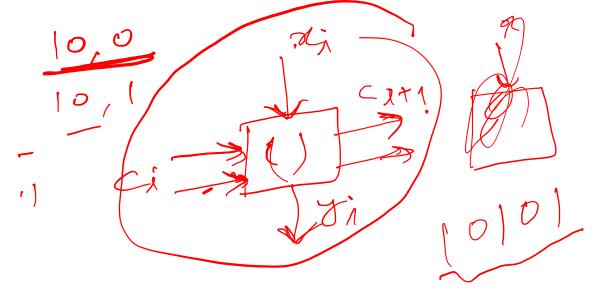




## Pattern Recognition

- Example
  - Detect 101
- Basic Method
  - Input 101101101010
  - Output: 00010010000
  - Another Method (Define  $c_i$ )
    - 00 (if no bit match till now)
    - 01 (if one-bit match till now)
    - 10 (if two-bit match till now)
    - 11 (if three-bit match till now)

(- 1	1	. )
	N/ W	
//	0	
1		



	$(c_i)$	Xi	$c_{i+1}$	y <sub>i</sub> -
	00 -	0_	00	0_7
	00 =	1	01	04
7	01.	• 0	10	0 /
	01	Í	01	0
	10	0	•00	0
	10	1	11	0
	Щ	0.	10	0 /
(	11)	1	01	
1			13	

To slot

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