COL215L: Digital Logic & System Design

Lecture 7: Combinational Circuits (Cont.)

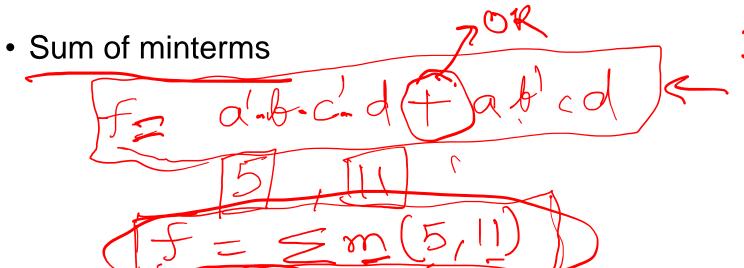


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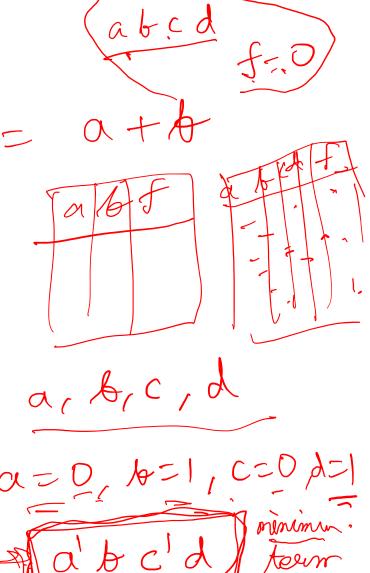
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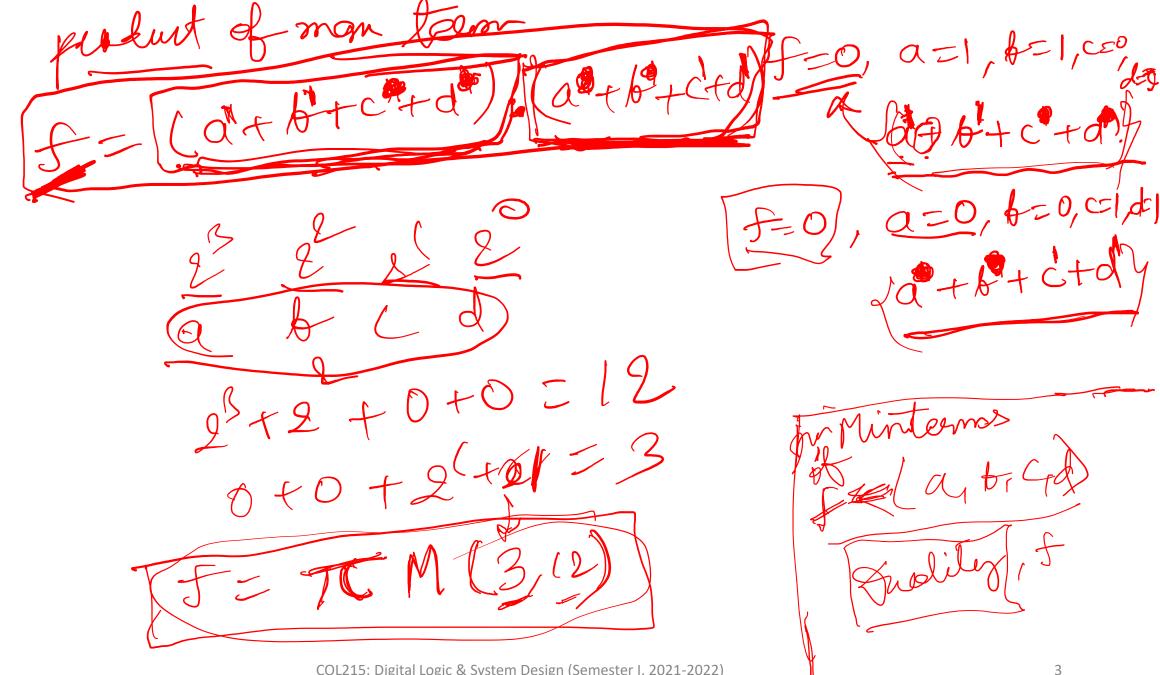
Canonical Representation



Product of maxterms

Journal of the College System Design (Semester I, 2021)
$$\frac{3}{5}$$

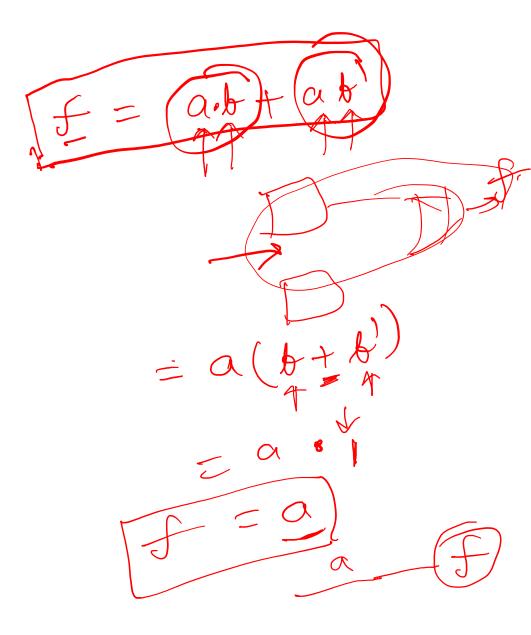




COL215: Digital Logic & System Design (Semester I, 2021-2022)

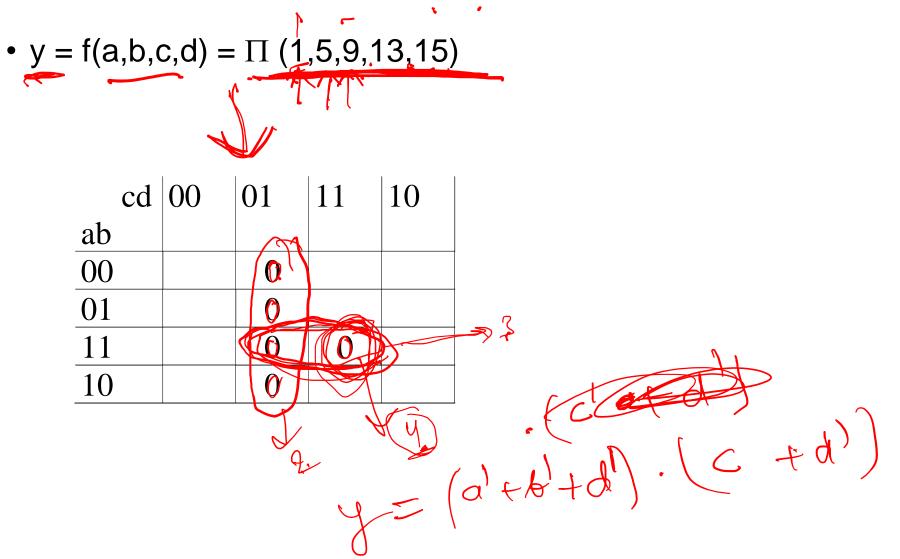
Minimization Objectives

- Cost/area
- Time



Karnaugh Map • $y = f(a,b,c,d) = \Sigma(0,3,4,7,8,11,15)$

Karnaugh Map (Cont.)



Challenges with K-Map

- K-Map
 - A graphical method and thus not suitable for large no. of variables
 - Not suitable for programming
- QM (Quine-Mcluskey) method
 - Does not suffer from these disadvantages