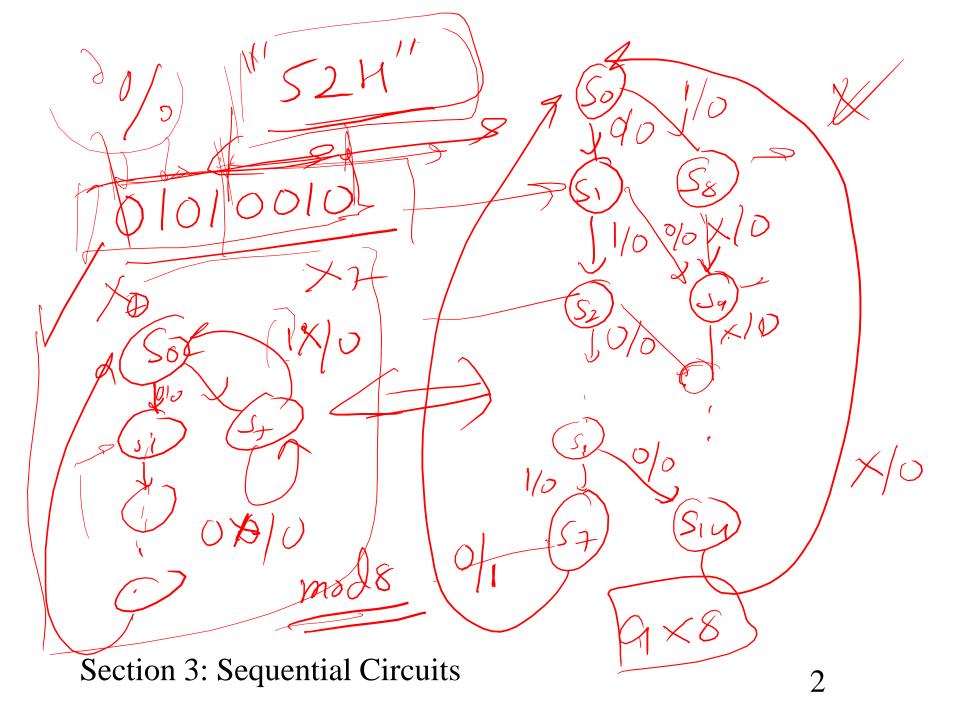
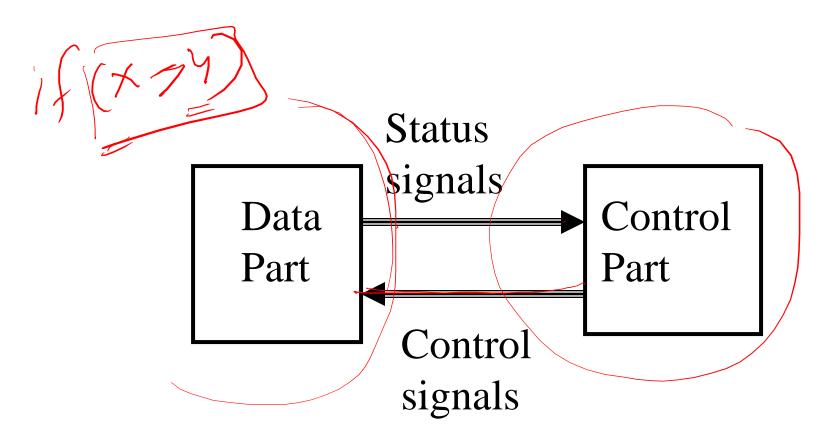
Lecture 24 System Design Case Studies

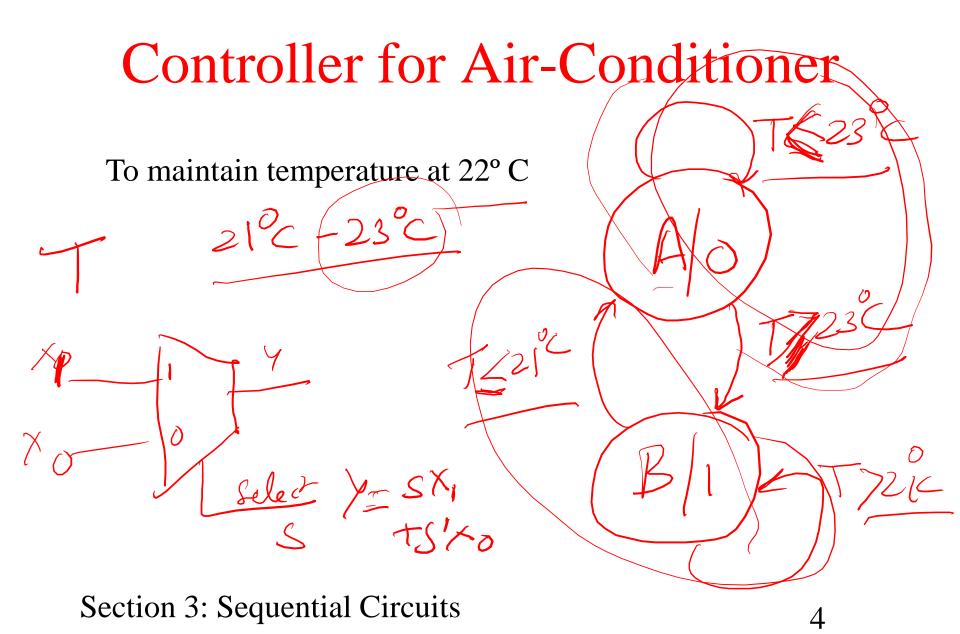
M. Balakrishnan
Dept. of Comp. Sci. & Engg.
I.I.T. Delhi

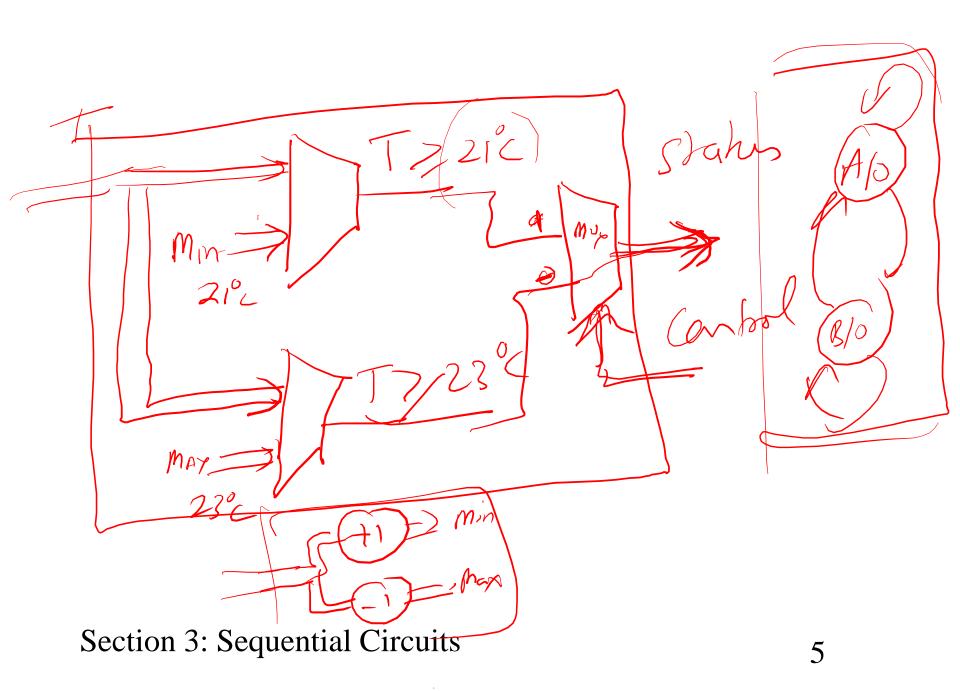


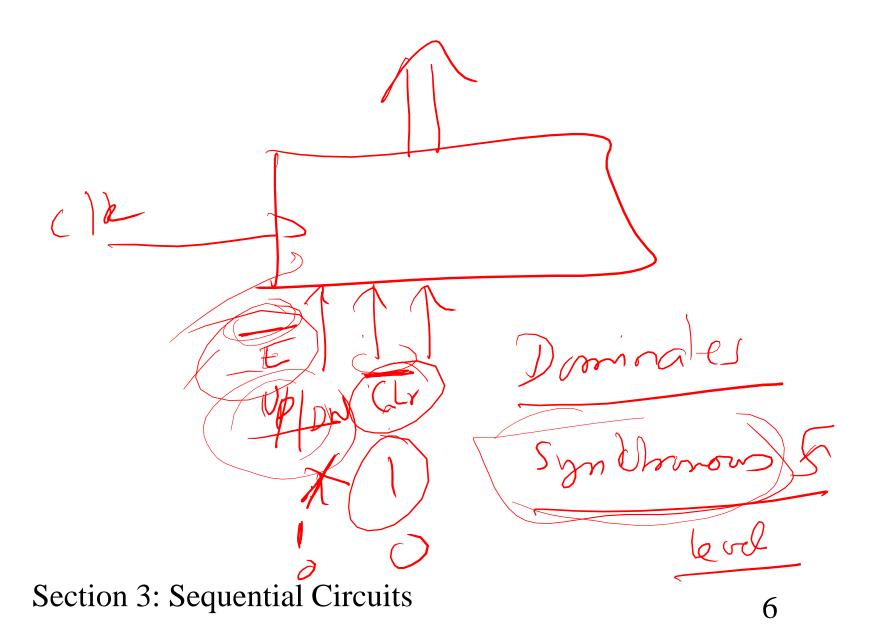
Data-Control Partition



Section 3: Sequential Circuits





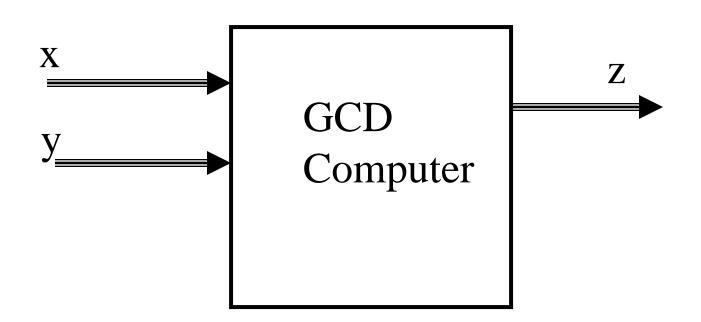


Steps in System Design

Choose an algorithm

- Identify the data modules (operators & storage)
- Identify the control signals & Shahn signals
- Extract the state machine for control
- Implement the state machine to complete the design

Case Study1: GCD Computer



GCD Algorithm

```
Input x, y;
while (x \neq y) do
                  then x := x - y
                  else y \neq y - x
   endif;
endwhile;
z := x;
end.
```

Section 3: Sequential Circuits

Jes & Calch 57

Modified GCD Algorithm (RTL)

S0:R1:= x, R2:= y;

S1: while $(R1 \neq R2)$ do

S2. if (R1 > R2)

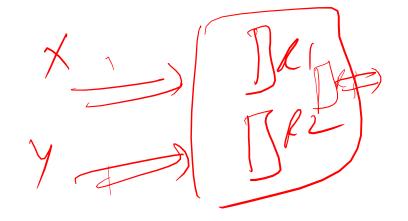
S3: then R1:= R1 - R2

S4: else R2:= R2 - R1 endif;

endwhile;

S5; R3:= R1;

Section 3: Sequential Circuits



GCD Computer: Data Part

R1

R2

Comp

R3

SUB