## **DECLARATION**

Processors in F Training ( )	e that the Summer Training Report entitled (Implementation of Softcore RISC FPGA) is an authentic record of work completed as requirements of Summer during the period from 21 st June'24 to 02 nd Aug'24 in Instruments Research & Establishment DRDO, under the supervision of <b>Mr. Vaibhav Saini (Scientist Jun)</b>
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