

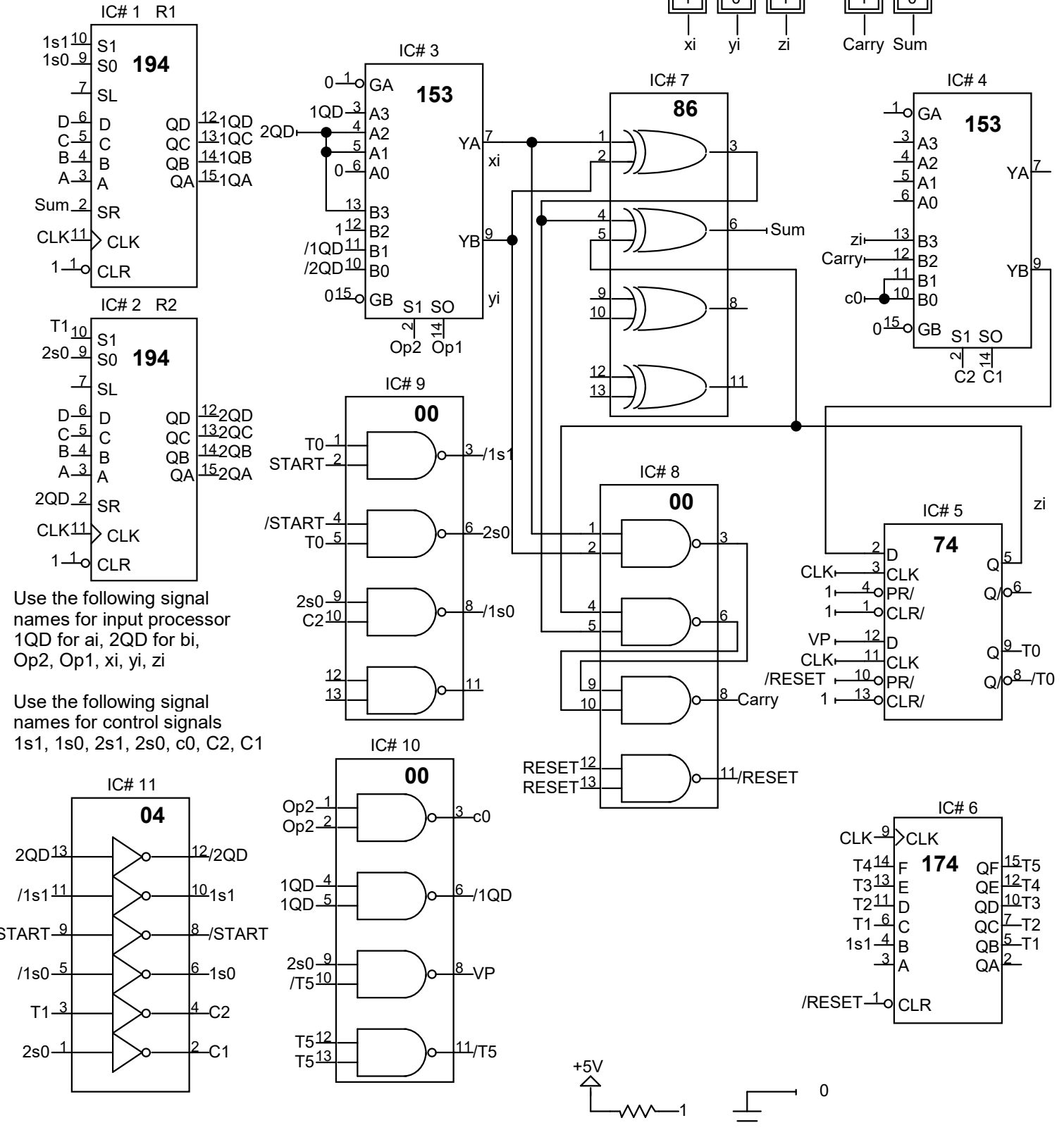
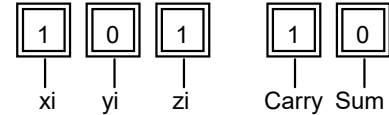
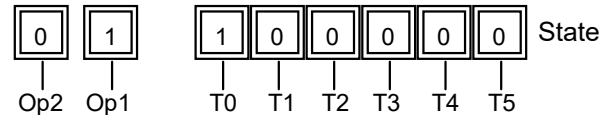
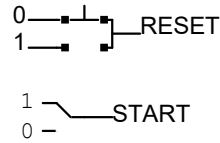
16.265 Logic Design
 Experiment 5: Arithmetic Processor
 Name: Harsh Patel
 Student Logic Number: 209
 Function Set Number: 11

4-bit data
 input
 ABCD

0	1	2	3
4	5	6	7
8	9	A	B
C	D	E	F

0	1	2	3
4	5	6	7
8	9	A	B
C	D	E	F

Op2 Op1



Use the following signal names for input processor
 1QD for ai, 2QD for bi,
 Op2, Op1, xi, yi, zi

Use the following signal names for control signals
 1s1, 1s0, 2s1, 2s0, c0, C2, C1