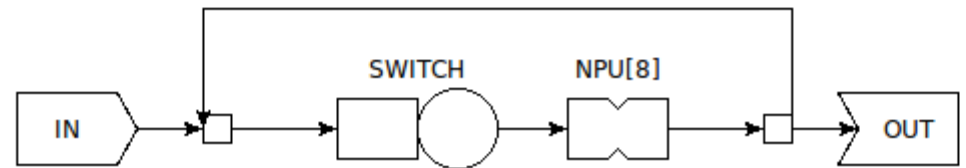


HARDWARE MODELS

Main hardware



NPU submodel

