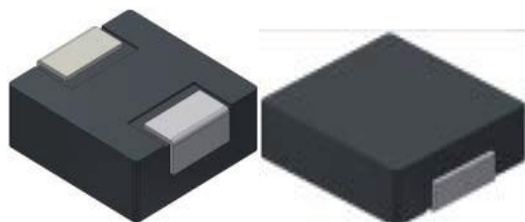


## High Current, Power Inductors

LPCA-1350-XXX-M Power Choke**Description**

- Halogen Free
- 125°C maximum total temperature operation
- 13.8x12.9x 5.0mm maximum surface mount package
- Powder iron core material
- Magnetically shielded, low EMI
- High current carrying capacity, Low core losses
- Frequency range up to 5MHz
- RoHS compliant

**Applications**

- Voltage Regulator Module (VRM)
- Multi-phase regulators
- Point-of-load modules
- Smart phone POL modules
- SSD modules
- Notebook regulators
- Battery power systems
- Graphics cards
- Data networking and storage systems

**Environmental Data**

- Storage temperature range: -55°C to +125 °C
- Operating temperature range: -55°C to +125°C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020D compliant

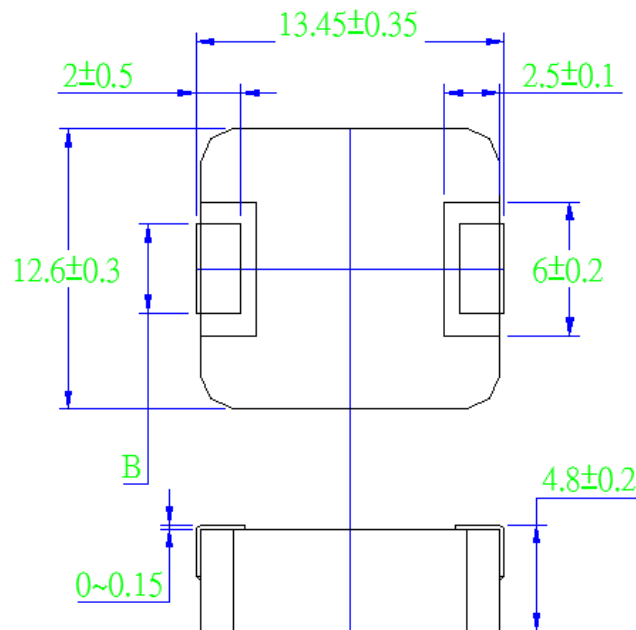
Description											
LPCA-1350-1R0-M				1.0μH				±20 %			
Model				Inductance Value				Inductance Tolerance			
Global Part Number											
L	P	C	A	1	3	5	0	1	R	0	M
Product Series				Dimensions				Inductance Value		Tol.	

Part No.	Inductance	DC Resistance		Heating Rating Current	Saturation Current
	L0 (μH)	DCR (mΩ)		Idc (A)	Isat (A)
	±20 %, 100 kHz, 1V	TYP.	MAX.	TYP.	TYP.
LPCA-1350-R22-M	0.22	0.5	0.7	50.0	75.0
LPCA-1350-R36-M	0.36	0.74	0.85	42.0	50.0
LPCA-1350-R50-M	0.5	1.1	1.15	38.0	48.0
LPCA-1350-R68-M	0.68	1.35	1.55	33.0	46.0
LPCA-1350-R82-M	0.82	1.45	1.67	30.0	39.0
LPCA-1350-1R0-M	1.0	1.9	2.2	26.0	35.0
LPCA-1350-1R5-M	1.5	2.8	3.2	23.0	33.0
LPCA-1350-2R2-M	2.2	4.0	5.0	15.0	24.0
LPCA-1350-3R3-M	3.3	5.9	7.0	14.0	22.0
LPCA-1350-4R7-M	4.7	8.2	9.0	13.0	21.0
LPCA-1350-6R8-M	6.8	14.5	18.0	12.0	16.0
LPCA-1350-100-M	10.0	19.0	22.0	9.0	12.0
LPCA-1350-150-M	15.0	23.0	30.0	8.0	10.0
LPCA-1350-220-M	22.0	51.0	58.0	4.5	6.5
LPCA-1350-330-M	33.0	75.0	84.0	3.5	6.0
LPCA-1350-470-M	47.0	116.0	130.0	3.0	5.0

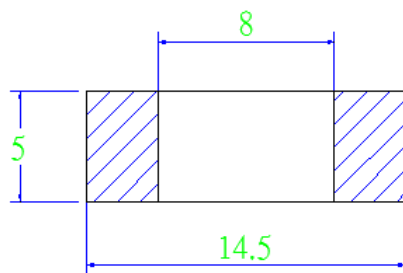
### Notes

1. All test data is referenced to 25 °C ambient
2. Operating temperature range - 55 °C to + 125 °C
3. Idc(A):DC current (A) that will cause an approximate ΔT of 40 °C(reference ambient temperature is 25 °C)
4. Isat(A):DC current (A) that will cause L0 to drop approximately 30 %
5. The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

### •Dimensions-mm



Code	Dimensions	
	R36/R50/R68 1R0/1R5/2R2	3R3/100/150 220/330/470
B	$3.85 \pm 0.5$	$5.0 \pm 0.5$



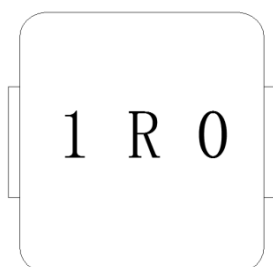
Recommend Land Pattern Dimensions

## •Marking

The inductor is marked with a 3-digit code

Example - -1.0→1R0

Note : Using Ink for marking



## Performance Graphs

### Test Instruments

Wayne kerr 3260B/G LCR Meter

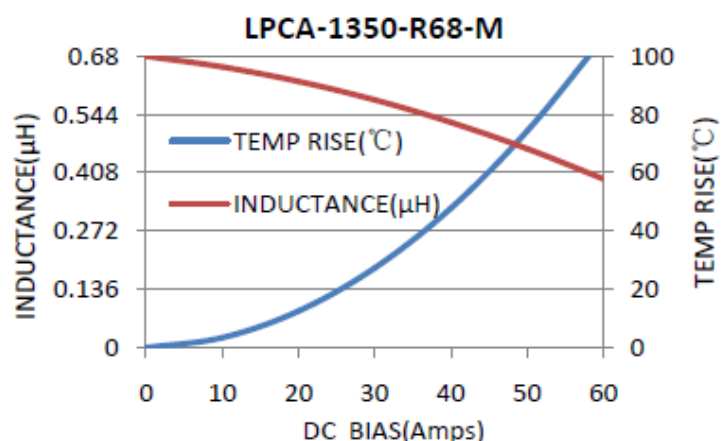
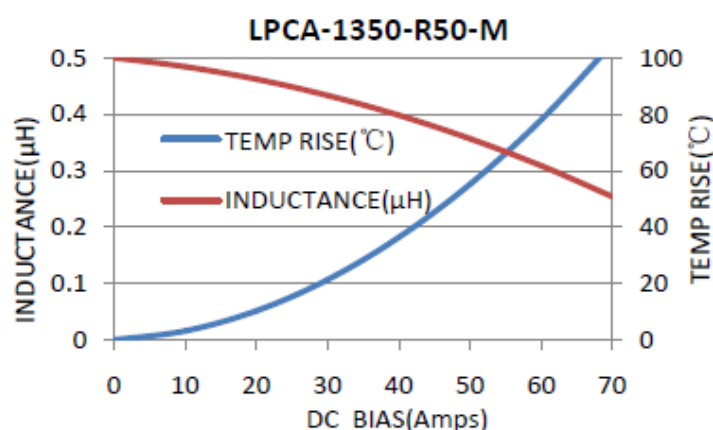
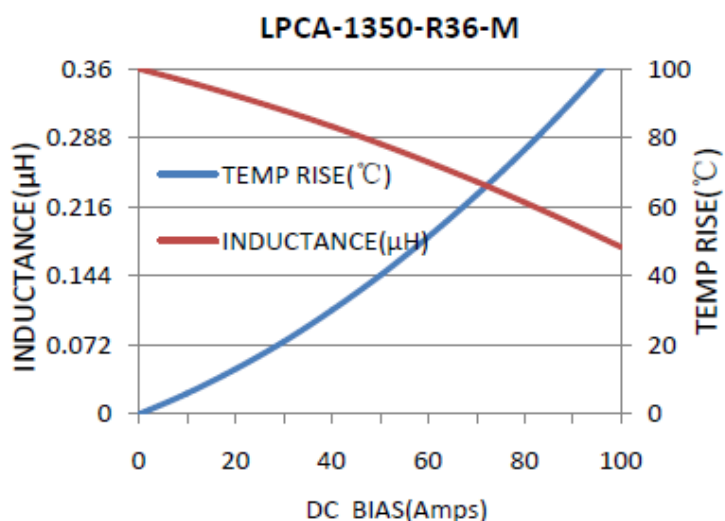
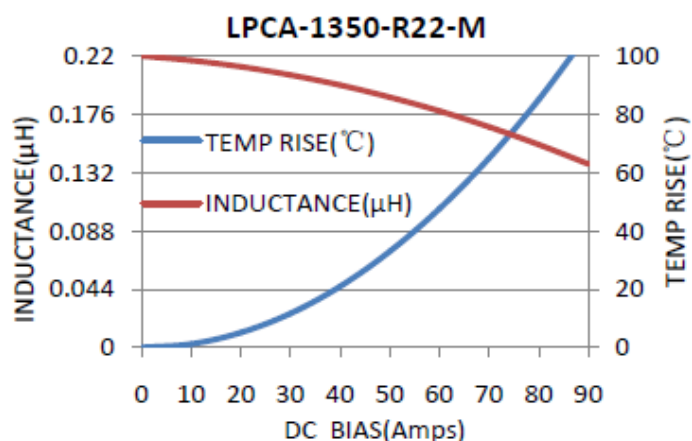
Wayne kerr 3265B Bias Current Source

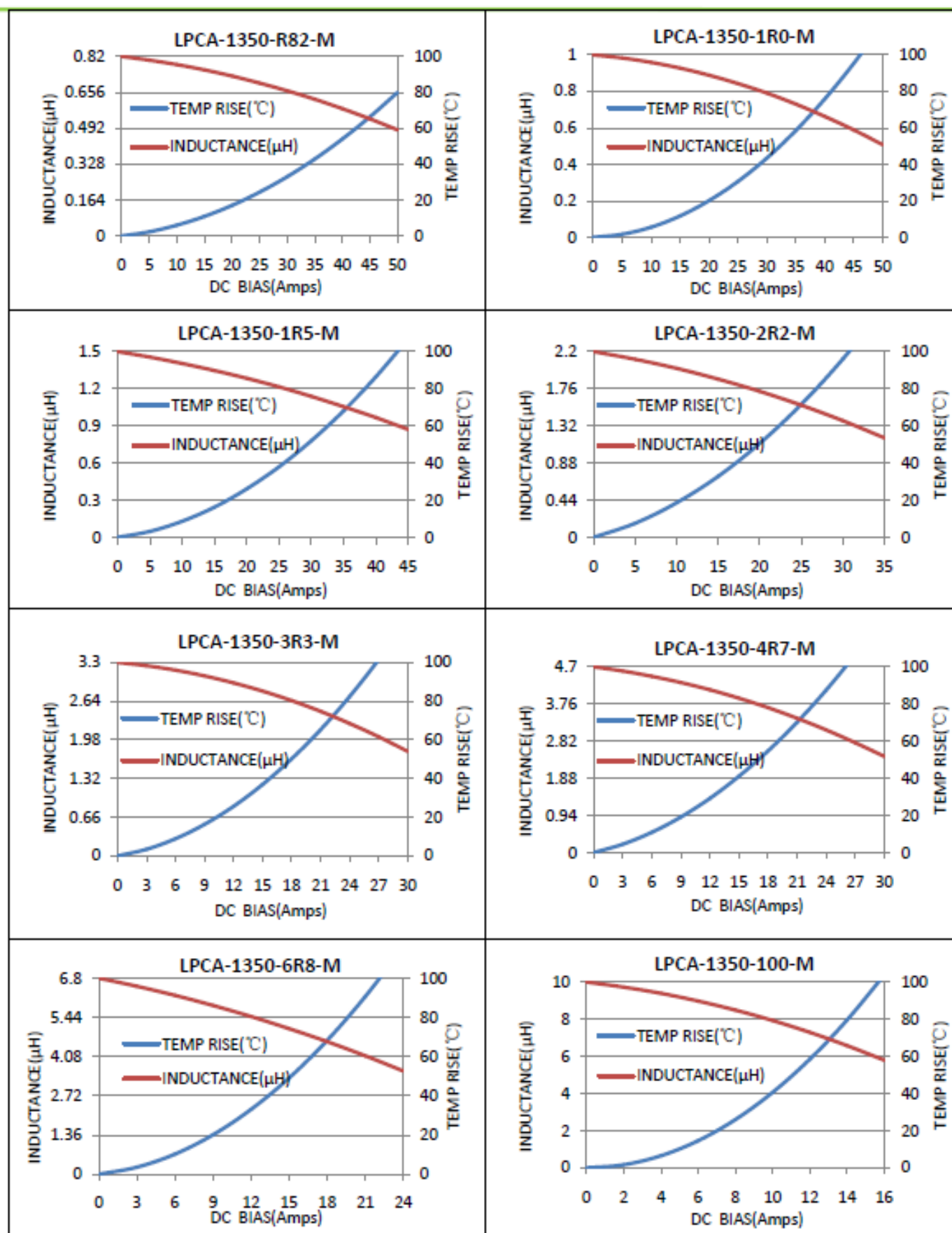
### Test Condition

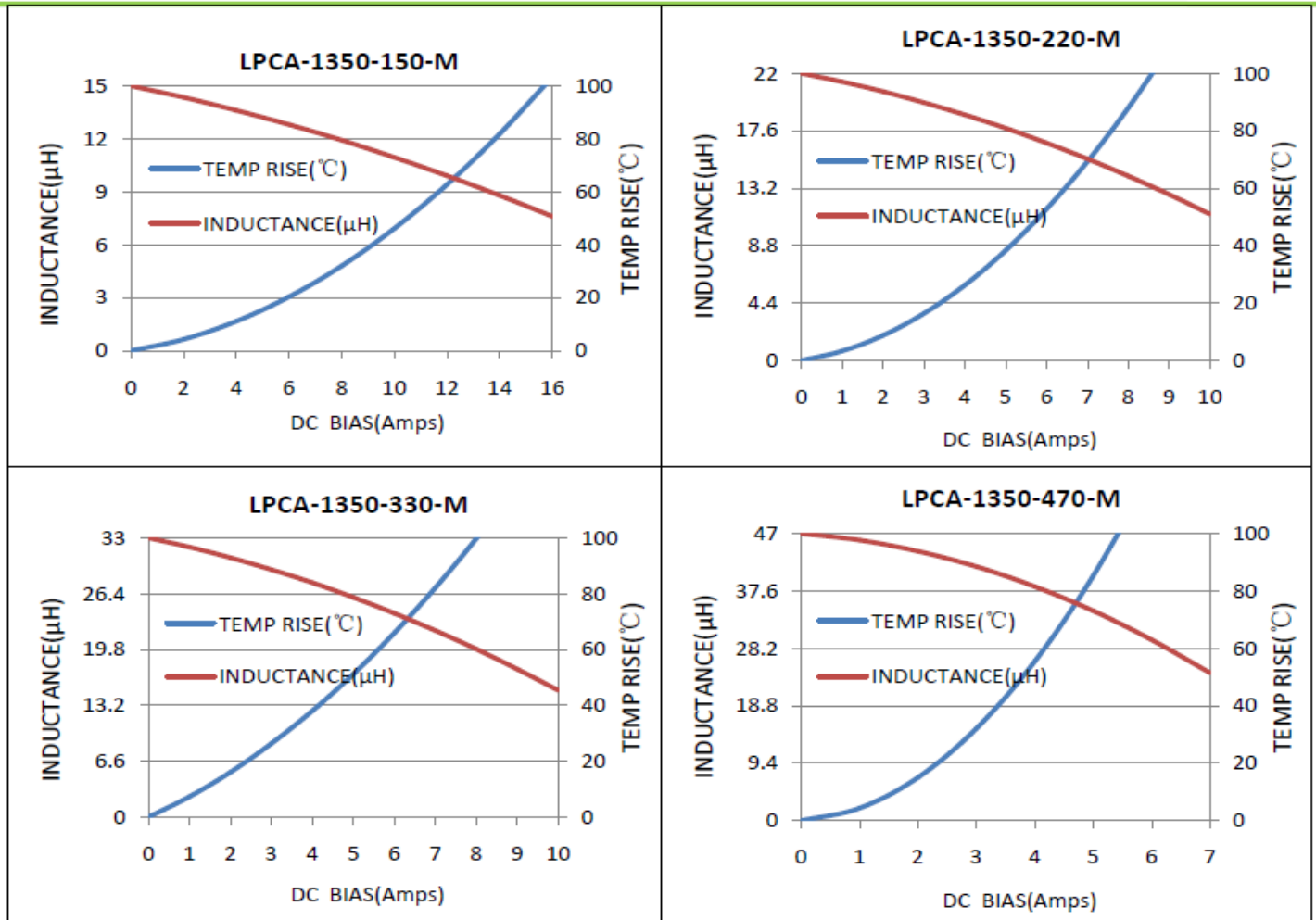
Temperature:  $26 \pm 3^{\circ}\text{C}$

Humidity: < 70% RH

Frequency: 100 KHz, 1.0V







鴻達電能科技股份有限公司