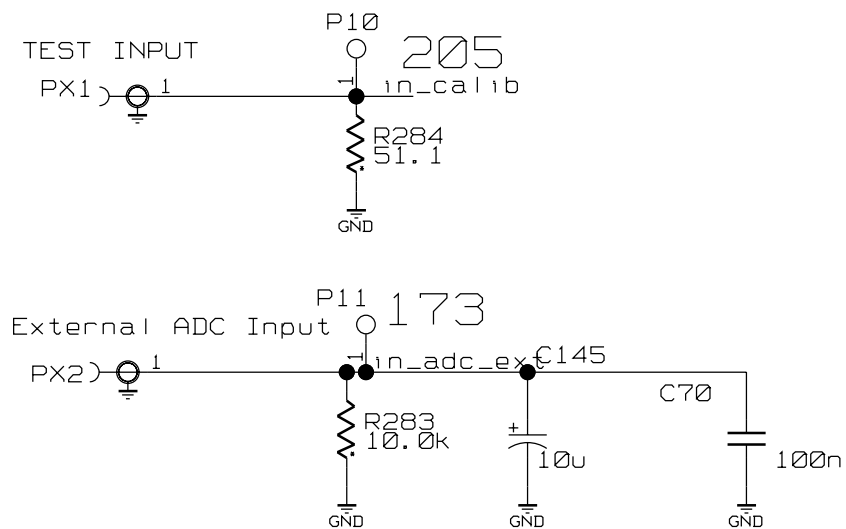
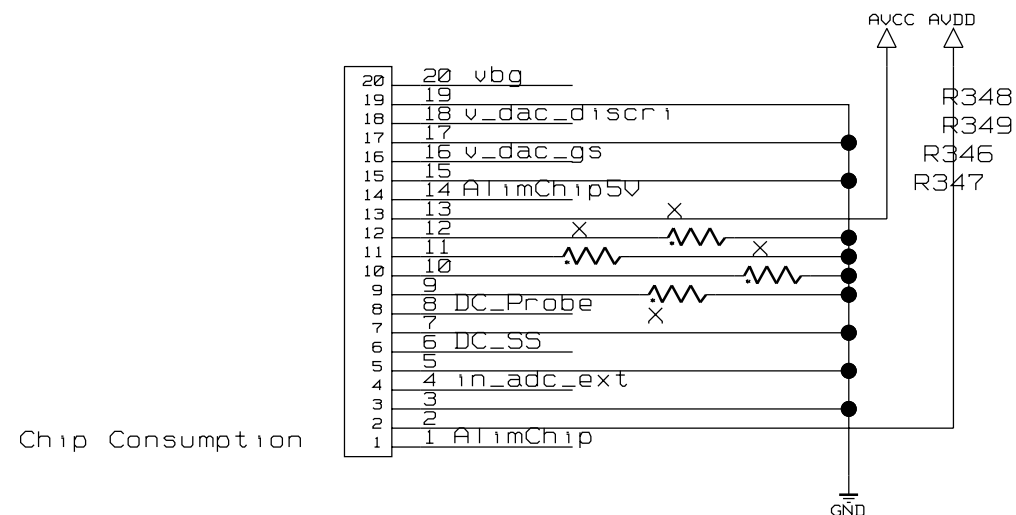


ANALOG INPUTS

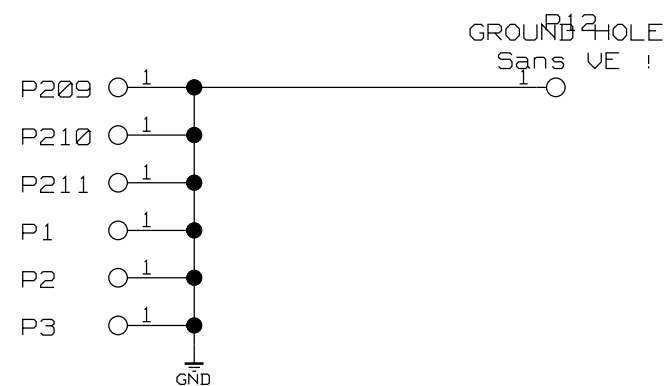


KEITHLEY DMM 2000 - CONNECTOR

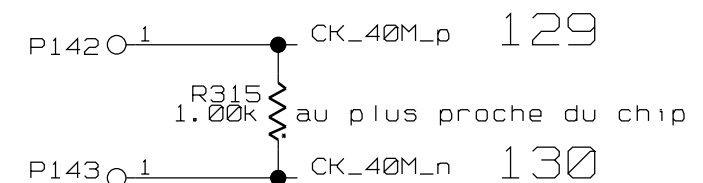
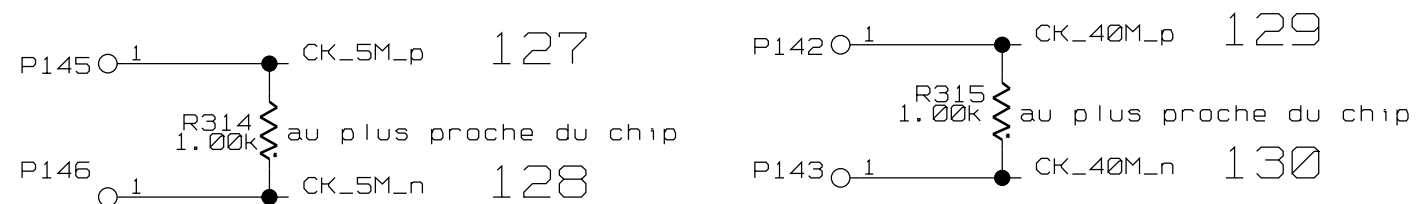
J9



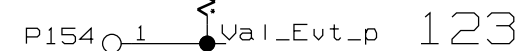
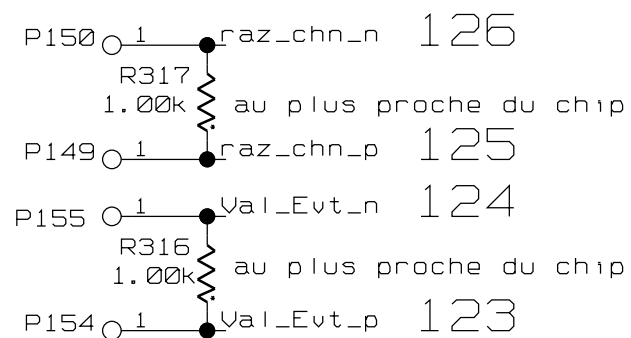
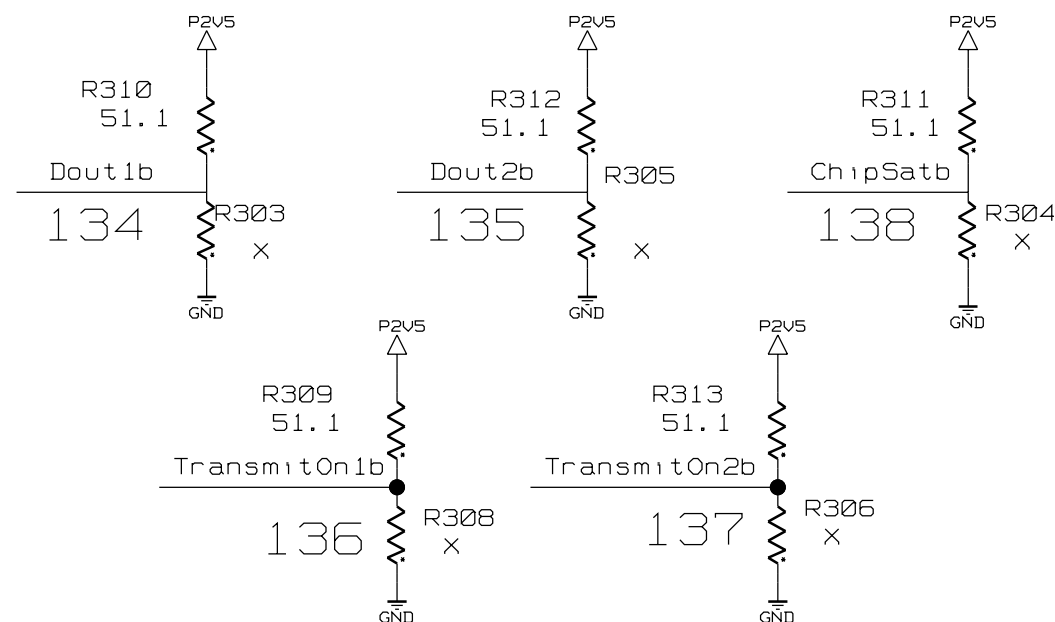
PASTILLES MECANIQUE
POUR ENTRETOISES



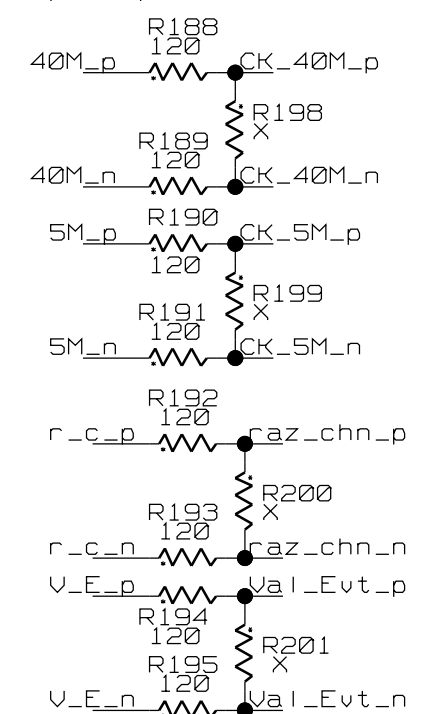
CLOCKS INPUTS



I/O ASIC



au plus proche de l'altera



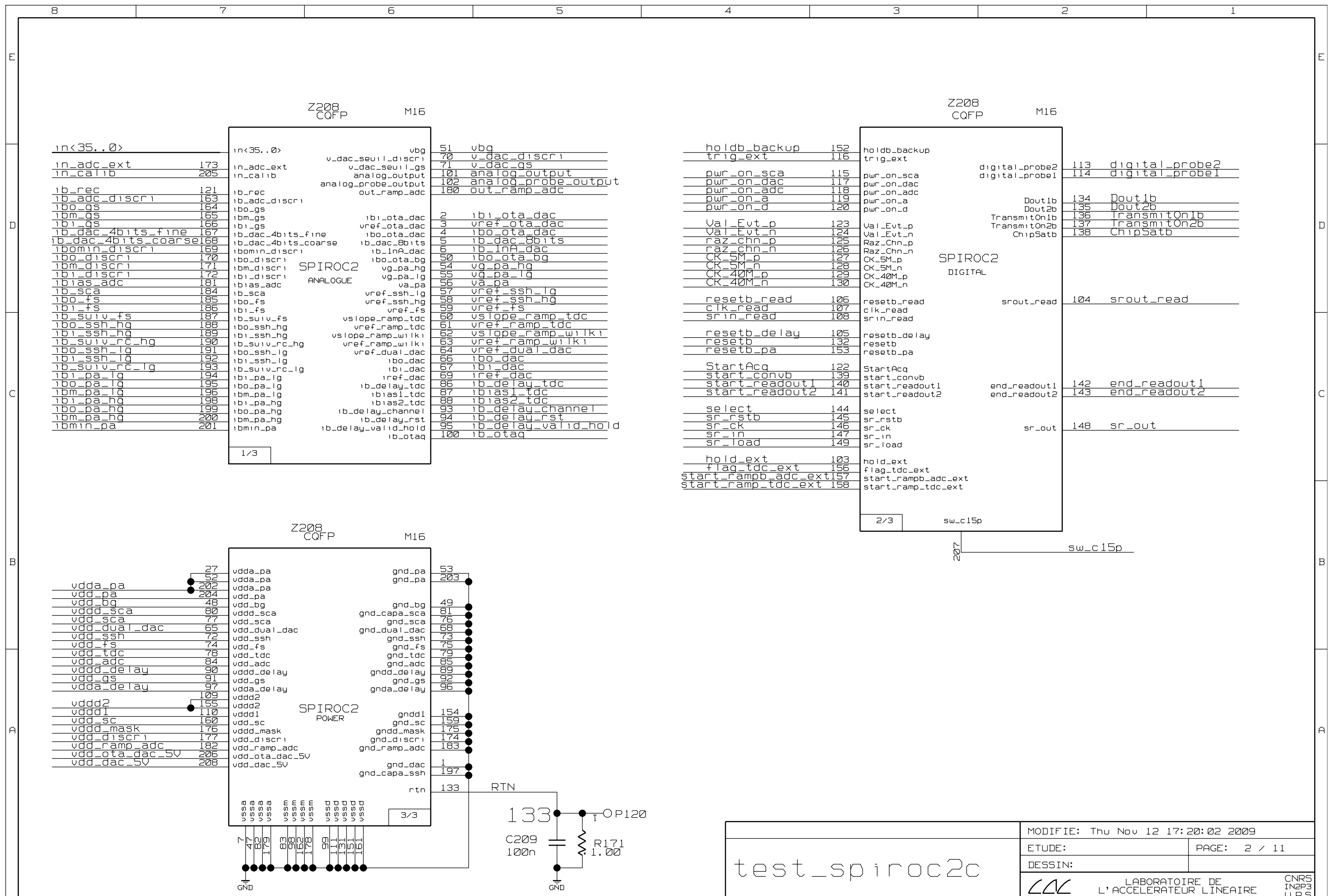
test_spiroc2c

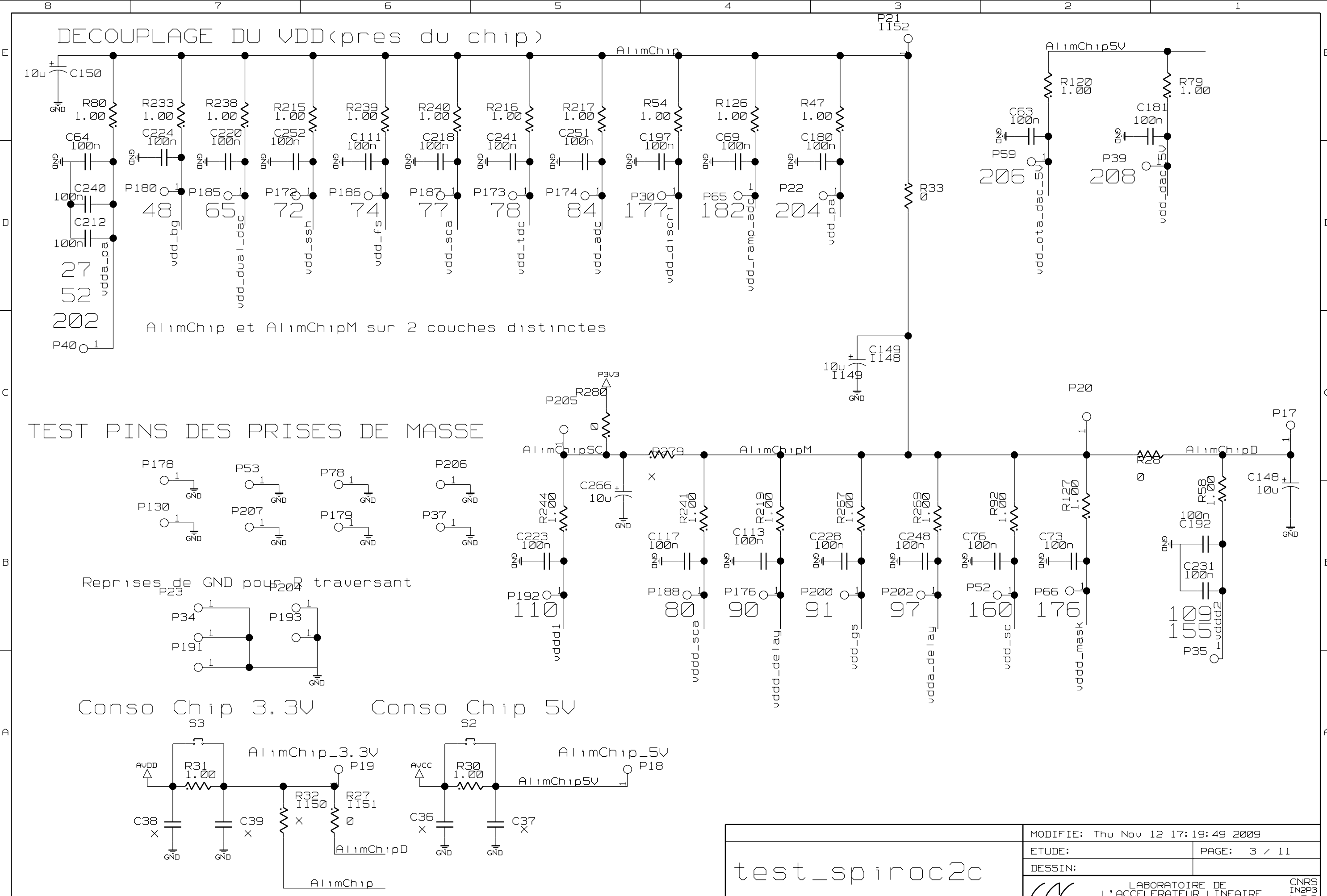
MODIFIE: Thu Nov 12 17:19:41 2009

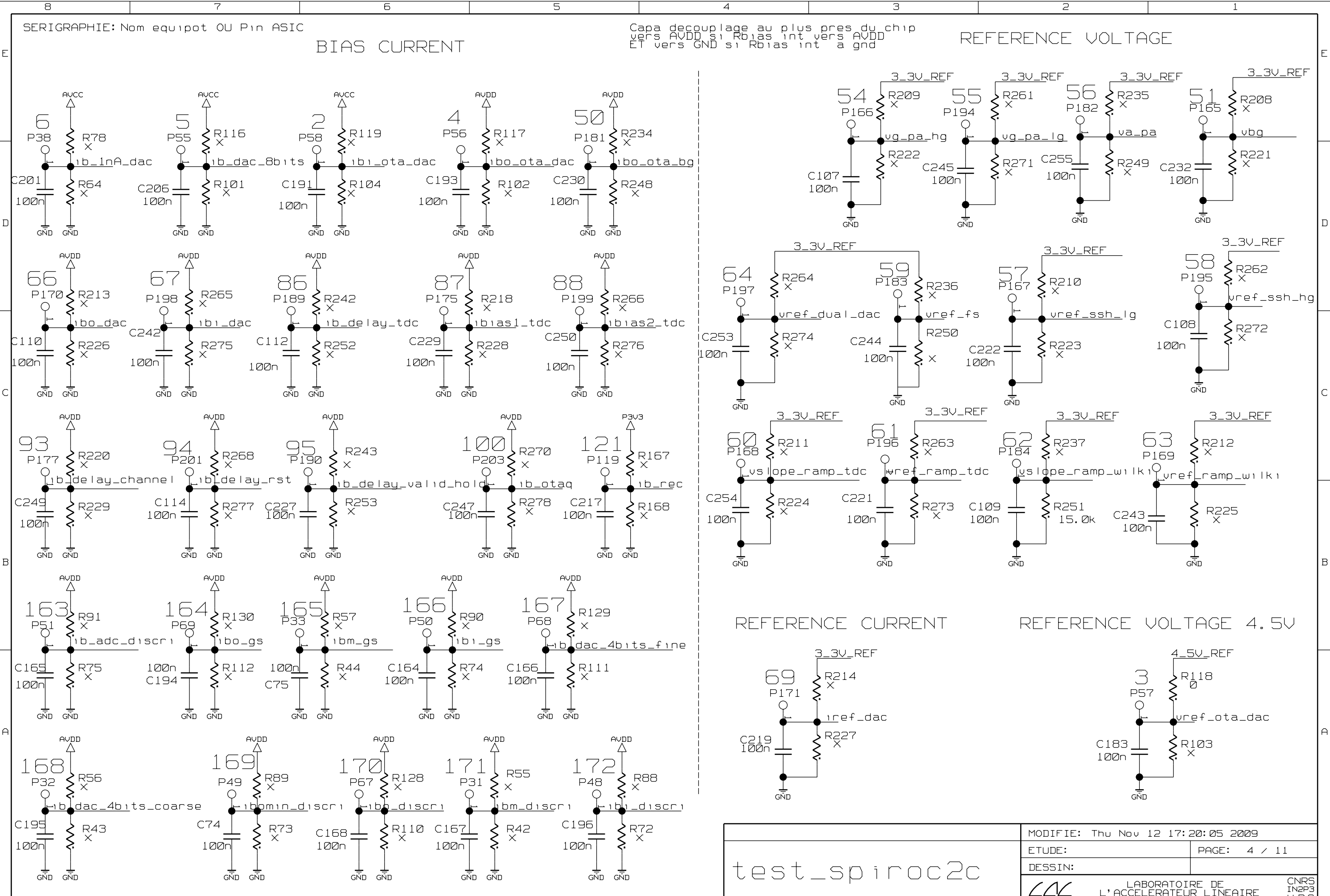
ETUDE: PAGE: 1 / 11

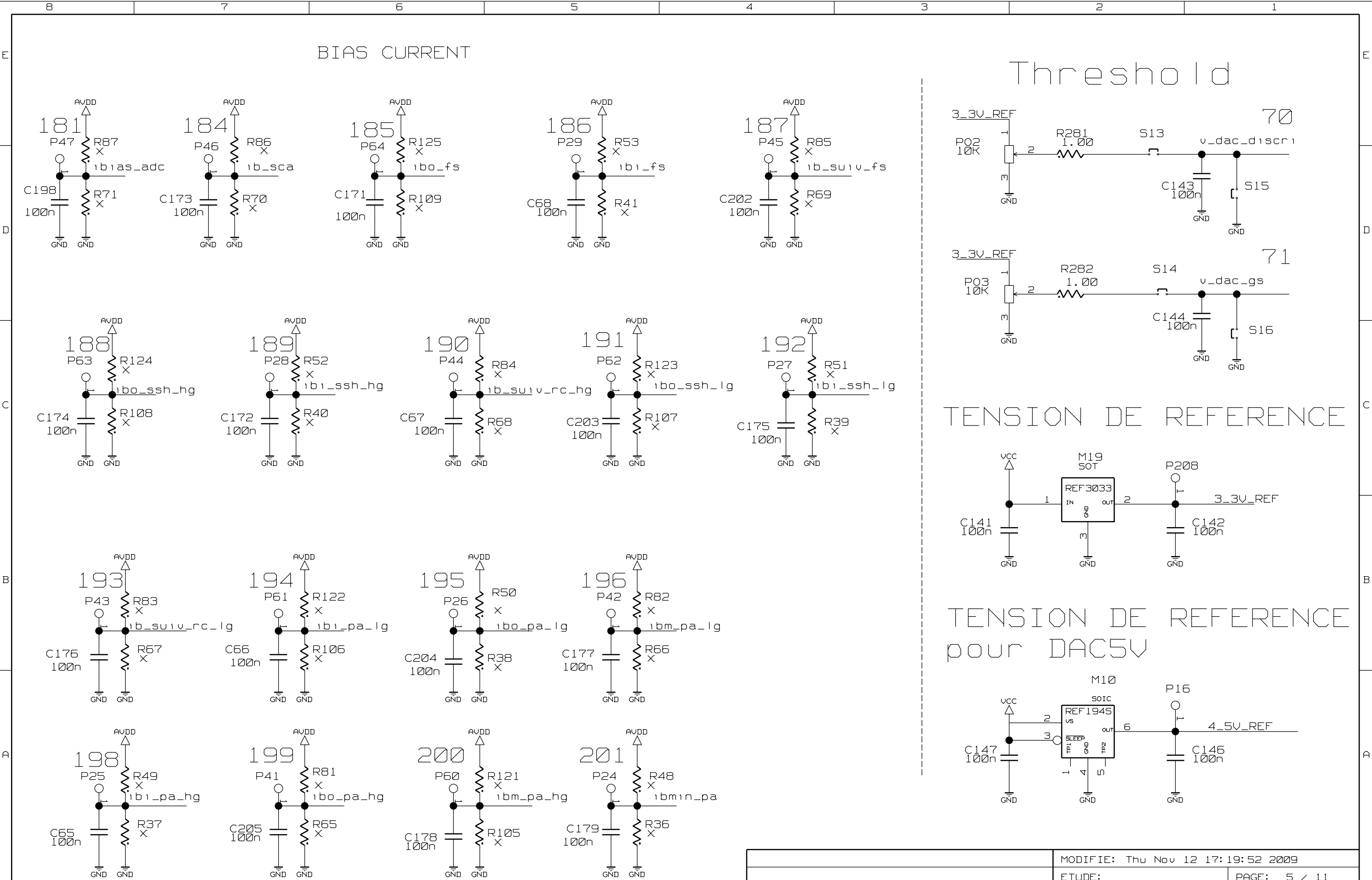
DESSIN:

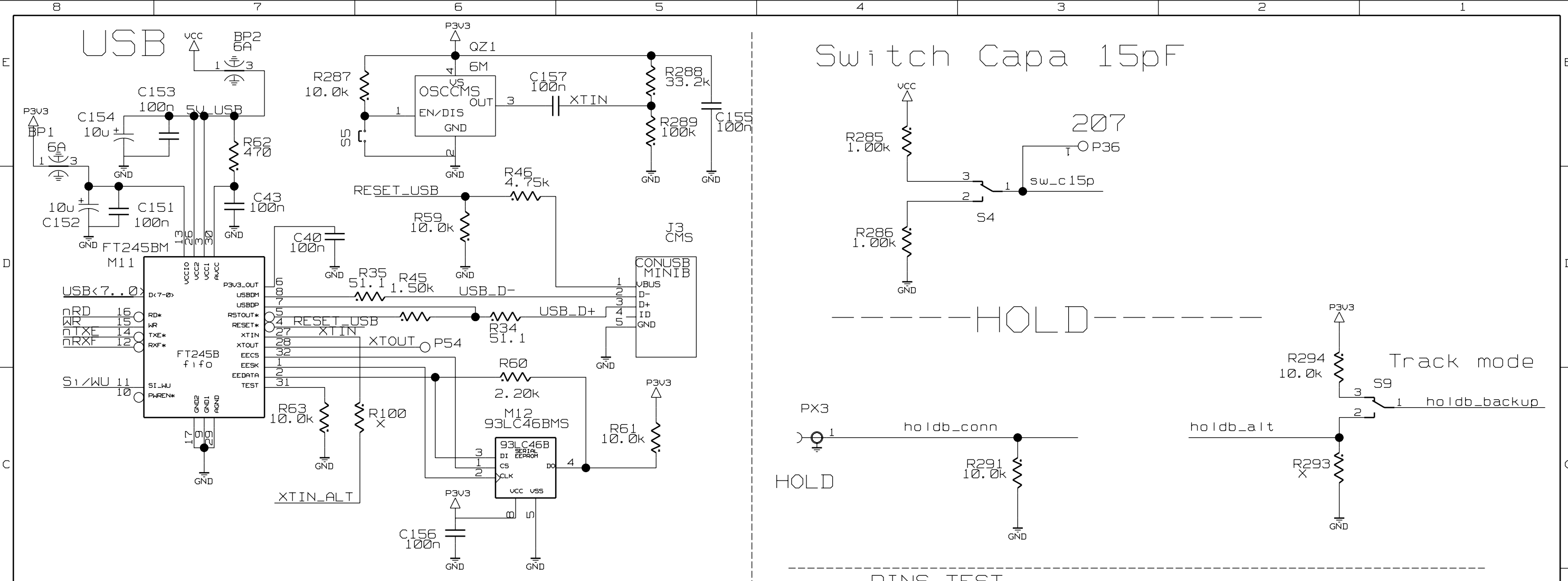
LLC LABORATOIRE DE L'ACCELERATEUR LINEAIRE CNRS IN2P3 U.P.S.

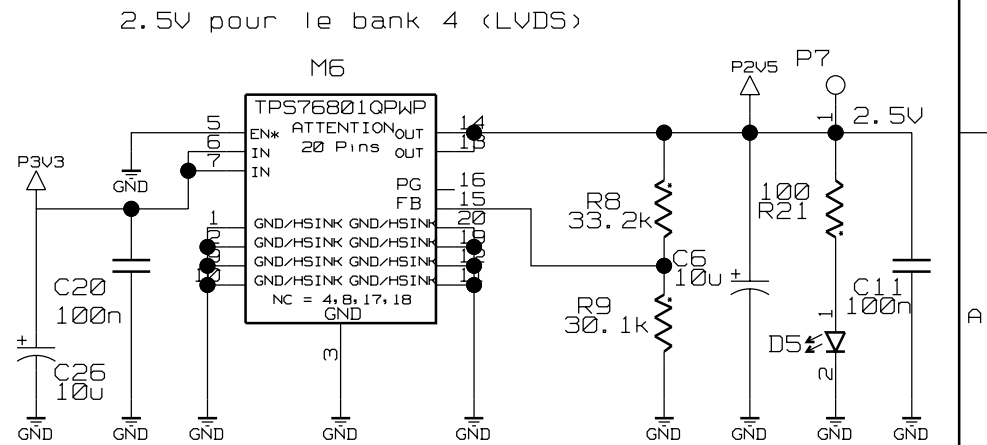
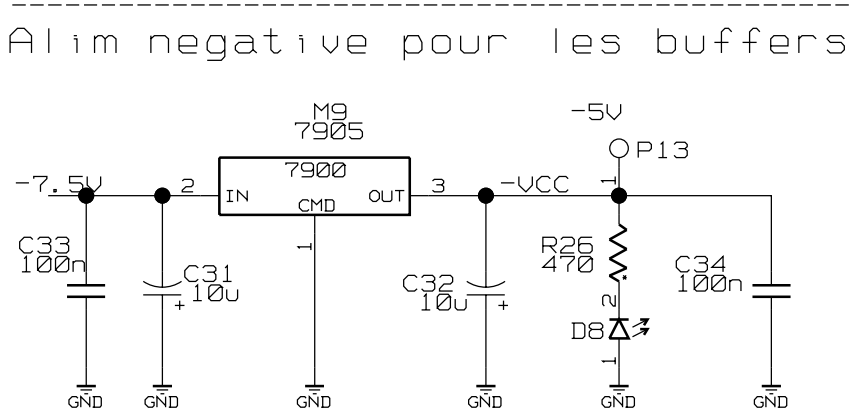
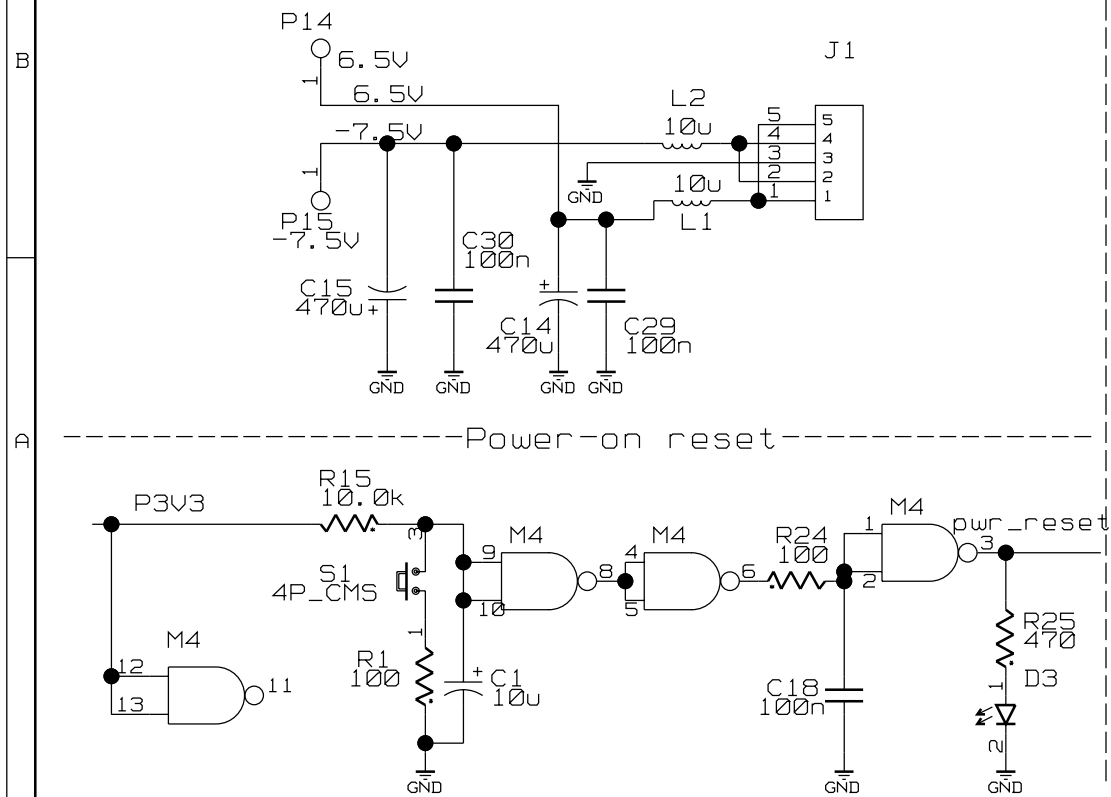
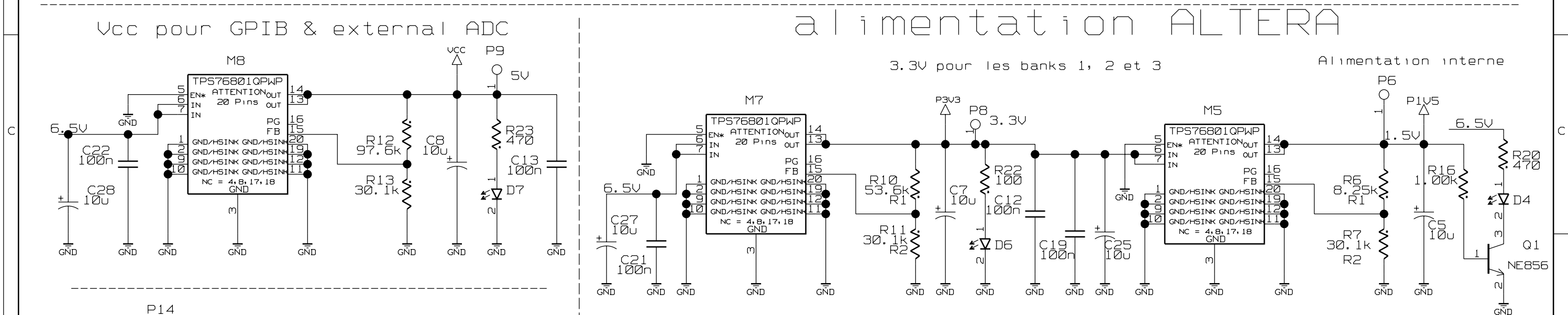
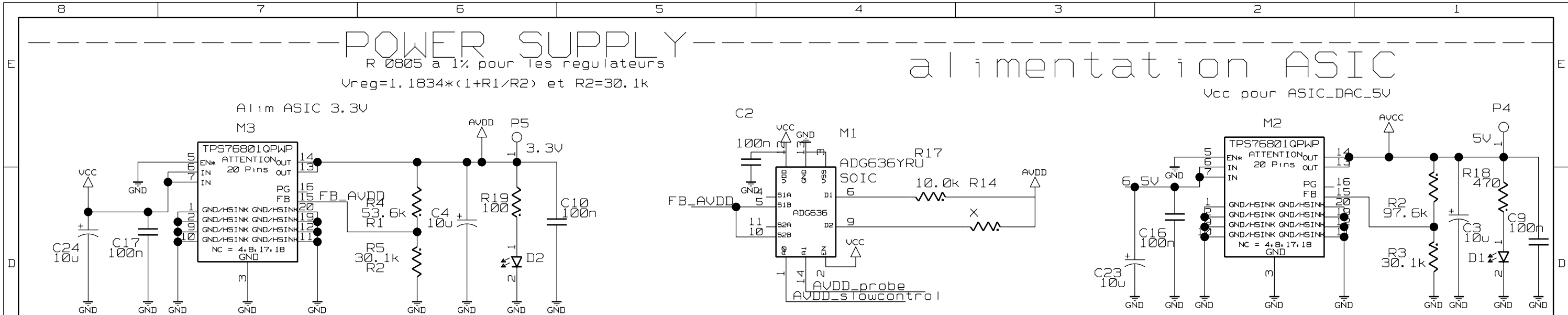




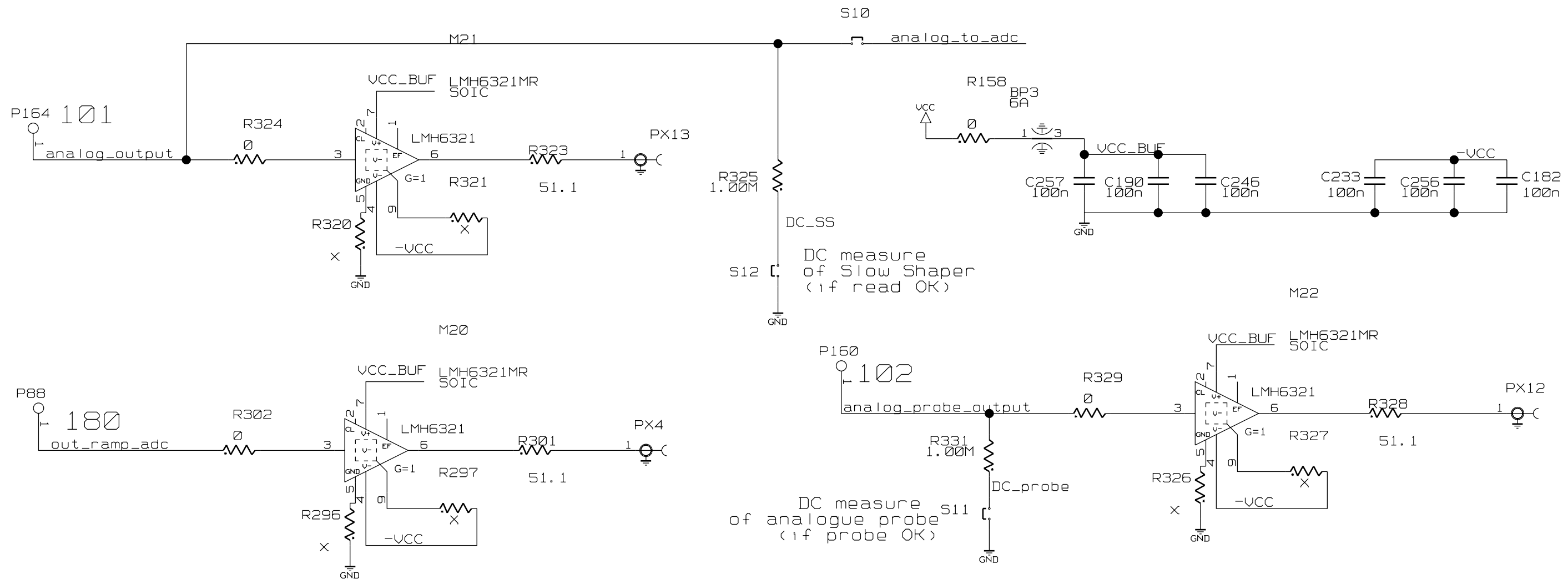




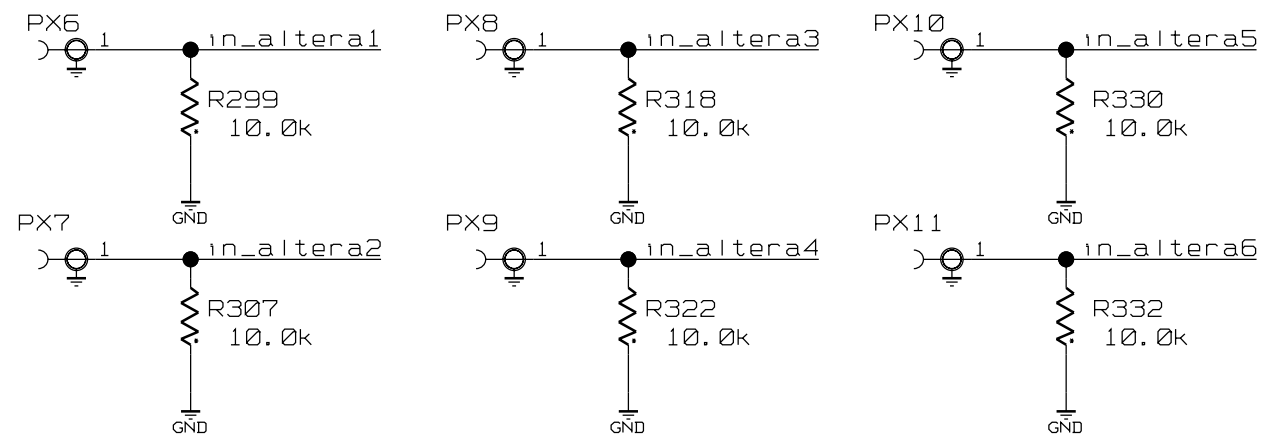




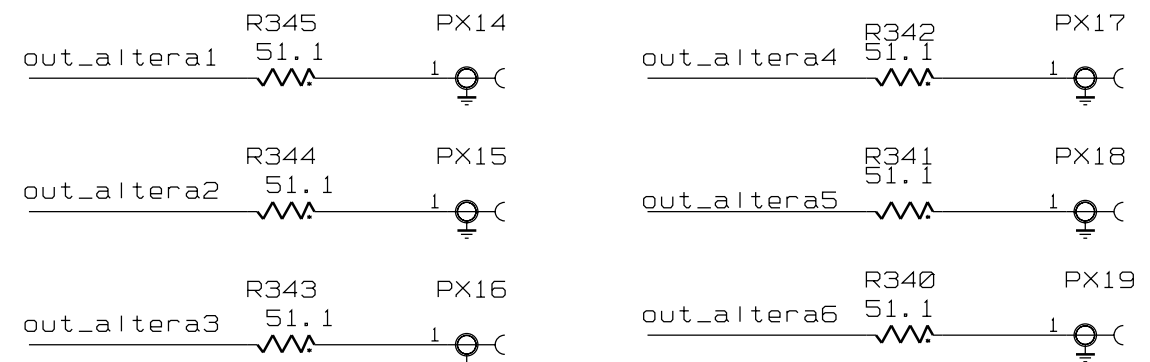
BUFFERED OUTPUTS



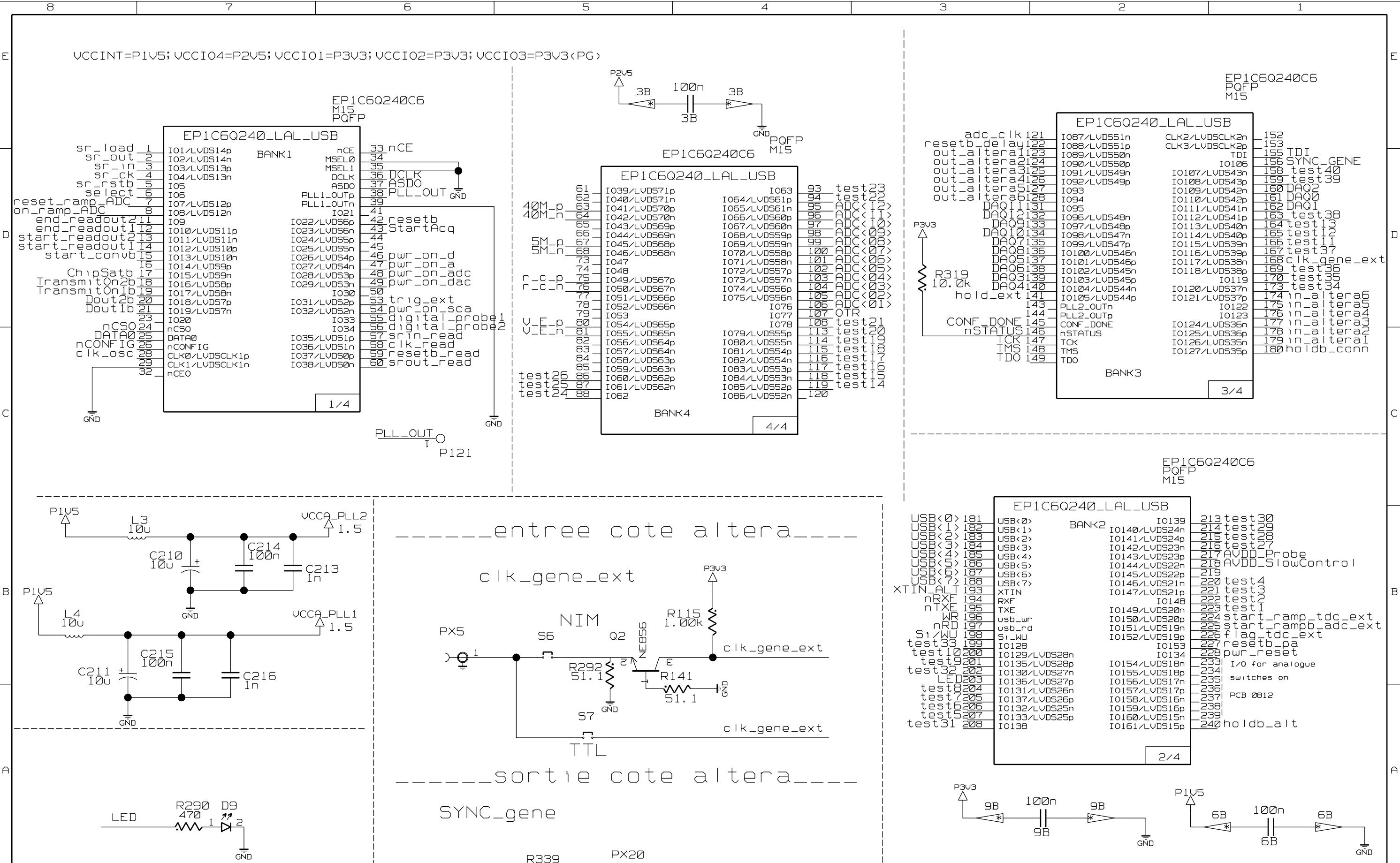
I/O to ALTERA



I/O from ALTERA



test_spiroc2c



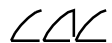
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MODIFIE: Thu Nov 12 17:20:01 2009

ETUDE:

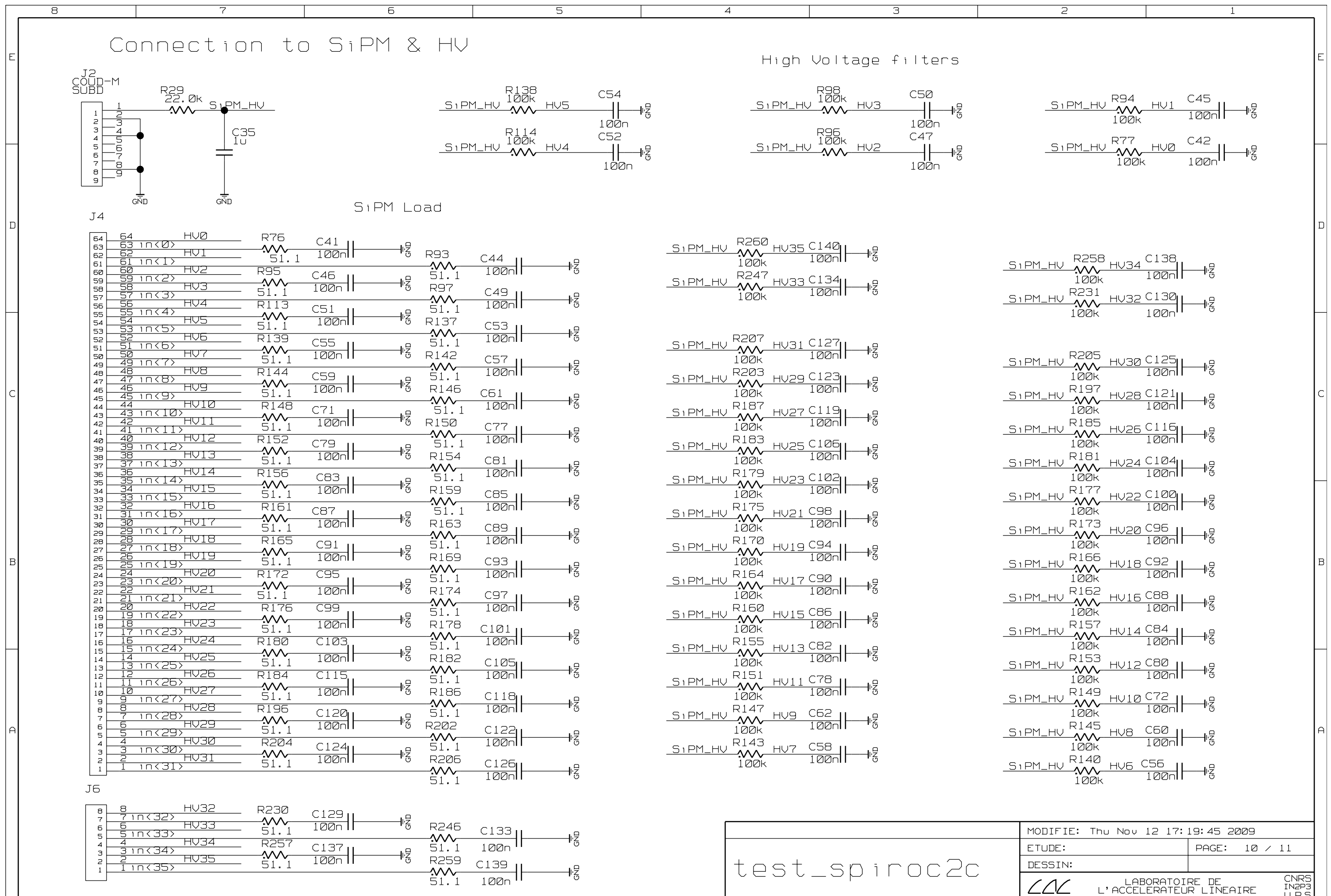
PAGE: 9 / 11

DESSIN:



LABORATOIRE DE
L'ACCELERATEUR LINEAIRE

CNRS
IN2P3
LPS



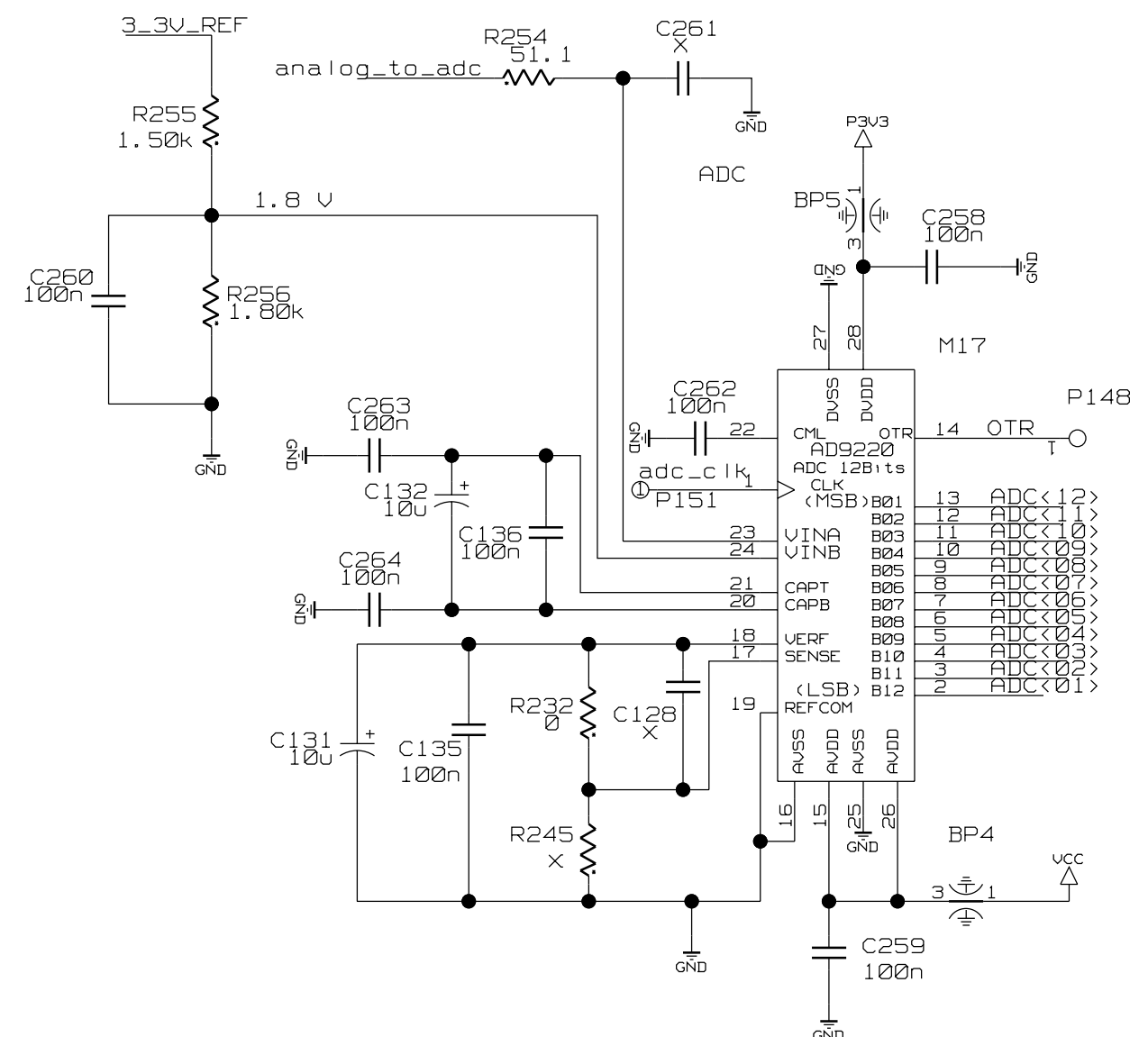
Digital ASIC

hold_ext	OP138	103	pwr_on_a	OP124	110	end_readout2	OP102	143
trig_ext	OP127	110	start_readout1	OP107	140	select	OP103	144
resetb_delay	OP137	105	pwr_on_d	OP125	120	Dout2b	OP115	135
resetb_read	OP134	106	start_convb	OP110	130	start_readout2	OP106	141
clk_read	OP135	107	resetb	OP123	130	sr_rstb	OP104	145
sr_in_read	OP131	108	TransmitOn2b1	OP111	137	sr_ck	OP99	140
srout_read	OP139	104	TransmitOn1b1	OP114	136	sr_in	OP100	147
sr_out	OP98	140	Dout1b	OP116	134	sr_load	OP96	140
end_readout1	OP108	140	ChipSatb	OP112	130	holdb_backup	OP90	150
pwr_on_dac	OP128	117	digital_probe1	OP132	114	resetb_pa	OP95	153
pwr_on_adc	OP129	118	digital_probe2	OP136	113	flag_tdc_ext	OP92	150
pwr_on_sca	OP133	115	StartAcq	OP126	120	start_rampb_adc_ext	OP93	157
						start_ramp_tdc_ext	OP89	158

DAQ Connector

DAQ12	14	14
DAQ11	13	13
DAQ10	12	12
DAQ9	11	11
DAQ8	10	10
DAQ7	9	9
DAQ6	8	8
DAQ5	7	7
DAQ4	6	6
DAQ3	5	5
DAQ2	4	4
DAQ1	3	3
DAQ0	2	2
	1	1

external ADC



ADC Ramp

