

**Hasan Mutlu**Eskişehir
Computer Engineering Student

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#### **EDUCATION**

# •Gebze Technical University

2019 -

GPA: 3.07

Computer Engineering Bachelor's

### EXPERIENCE

# •Birleşik Yazılım

July - August 2022 (20 Days)

Software Development Intern

Gebze, Kocaeli

– Worked on the company's website as Full Stack Developer. HTML-CSS-Javascript are used alongside C# and SQL on ASP.NET Core.

#### **PROJECTS**

# •Android Application Development Project: Randebul

School Project

- Within the scope of the Software Engineering course, an Android application was developed as a group project. The application allows customers to make appointments with professionals who are experts in their fields. In the project, both front-end and back-end parts were involved. Flutter was used while developing the app. Agile methods were used throughout the project.
- $-\ Git Hub\ Link\ of\ the\ Project:\ https://github.com/hasanmutlu26/randebul$

### •Mini MIPS Processor Project

School Project

- As part of the Computer Organization course, a single cycle processor was designed, which is a simple version
  of MIPS. It can run some simple commands and small programs. The design of the processor was done in the
  Quartus II program, largely using the structural Verilog HDL.
- GitHub Link of the Project: https://github.com/hasanmutlu26/Mini-MIPS-Processor

# •Hardware Trojan Detection Using Delay Based Methods on FPGA

Graduation Project

- This project aims to detect hardware trojans on the FPGA by measuring the delay values of a single sample logical circuit path and its infected versions. Verilog HDL language is used. In the first semester of the project, a significant difference was detected in the delay values between the clean path and its infected versions, and the method proved to be effective. In the second semester, it was aimed to detect trojans that managed to hide in cases where intra-chip and inter-chip variations were effective on delay values. "Delay Rate" method is usef to reveal this hidden trojans and it was concluded that the method was largely effective.
- GitHub Links of the Project:
- 1. Semester: https://github.com/hasanmutlu26/Hardware-Trojan-Detection-Using-Delay-Based-Method-on-FPGA
- $-2. \ Semester: \ https://github.com/hasanmutlu26/Hardware-Trojan-Detection-Using-Delay-Ratio-Method-on-FPGA-New Method-on-FPGA-New Method-on-FP$

#### SKILLS

C / C++: Advanced Level Python: Advanced Level Java: Advanced Level

Verilog HDL: Advanced Level Flutter / Dart: Intermediate Level Assembly: Intermediate Level SQL: Intermediate Level Javascript: Intermediate Level HTML - CSS: Beginner Level

C#: Beginner Level

# LANGUAGES

Turkish: Native

English: Proficient