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|  | **Hasan Mutlu**  Software Engineer  Eskişehir | Alıcı düz dolguyla +90 534 685 2803  E-posta düz dolguyla [hasanmutlu2633@gmail.com](mailto:%20hasanmutlu2633@gmail.com)  GitHub Connector | Low-Code GitHub Integration | Cyclr [GitHub: hasanmutlu26](https://github.com/hasanmutlu26)  LinkedIn logo - Free logo icons [Linkedin: Hasan Mutlu](https://www.linkedin.com/in/hasanmutlu26/)  Website icons for free download | Freepik [Website: hasanmutlu26.github.io](https://hasanmutlu26.github.io) |

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| **EDUCATION** |
| |  |  |  | | --- | --- | --- | |  | * **Gebze Technical University**   *Computer Engineering (English)* | 2019 – 2024  GPA: 3.08 | |

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| **EXPERIENCE** |
| |  |  |  | | --- | --- | --- | |  | * **TEI – Tusaş Engine Industries** * *Assistant Embedded Software Engineer* * *Part-Time Embedded Software Engineer* * *Candidate Embedded Software Engineer* | Eskişehir, Türkiye  October 2024 - Today June 2024 – September 2024  January 2024 – May 2024 | |
| |  |  |  | | --- | --- | --- | |  | * **Birleşik Yazılım** * *Software Development Intern* | Kocaeli, Türkiye  July 2022 – August 2022 | |

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| **PUBLICATIONS** |
| * Fatma Nur Esirci, Hasan Mutlu, Alp Arslan Bayrakci, "FPGA Based Verification for the Difficulty of Delay Based Hardware Trojan Detection" 14th International Conference on Electrical and Electronics Engineering (ELECO), 2023. [Link with solid fill](https://ieeexplore.ieee.org/abstract/document/10416020)[Newspaper with solid fill](http://www.eleco.org.tr/ELECO2023/eleco2023-papers/193.pdf) |

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| **SKILLS** |
| |  |  | | --- | --- | | Languages | Tools and Technologies | | * C * C++ * Java * Python * Verilog HDL * Assembly * Javascript * Flutter / Dart * SQL | * Git * Linux * Microcontrollers * STM32, MSP430, Arduino * Real Time Operating Systems * Micrium uC/OS-III, FreeRTOS | |

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| **LANGUAGES** |
| * Turkish: Native * English: Proficient |

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| **PROJECTS** |
| * **Hardware Trojan Detection Using Delay Based Method on FPGA -** [GitHub Connector | Low-Code GitHub Integration | Cyclr](https://github.com/hasanmutlu26/Hardware-Trojan-Detection-Using-Delay-Based-Method-on-FPGA)   *Graduation Project Tools: Verilog HDL, FPGA, Python*  This project aims to detect hardware trojans on the FPGA by measuring the delay values of a single sample logical circuit path and its infected versions. The effect of inter-chip and intra-chip variations are also investigated and it is observed that the trojan can hide in certain situations. It aims to reveal these hidden trojans by using a "Delay Ratio" method.  This project paved the way for the publication of "[FPGA Based Verification for the Difficulty of Delay Based Hardware Trojan Detection](#Publications)" conference paper.   * **File Synchronization Server in C -** [GitHub Connector | Low-Code GitHub Integration | Cyclr](https://github.com/hasanmutlu26/School-Assignments/tree/main/CSE%20344%20System%20Programming/2023/Final%20Project)   *School Project Tools: C*  A POSIX-compliant simplified version of Dropbox is implemented in C language as final project for System Programming course. The server side is capable of handling multiple clients using a pool of threads. The directories on both sides are synchronized upon any changes. The communication is established via sockets. Interprocess communication, thread synchronization and signal handling are well implemented.   * **STM32: uCOS-III Micrium RTOS and SystemView Integration -** [GitHub Connector | Low-Code GitHub Integration | Cyclr](https://github.com/hasanmutlu26/STM32-Micrium-ucOSIII-with-SystemView)   *Personal Project*  *Tools: C, STM32, RTOS*  This project is the integration of Micrium's uCOS-III Real-Time Operating System and SEGGER SystemView analyzer tool into STM32 microcontroller. The integration is tested and verified with a small multi-task LED blink application. The integration steps are explained and documented in the project's Readme on GitHub.   * **Mini MIPS Processor in Verilog -** [GitHub Connector | Low-Code GitHub Integration | Cyclr](https://github.com/hasanmutlu26/Mini-MIPS-Processor)   *School Project*  *Tools: Verilog HDL*  A single cycle processor was designed, based on MIPS processor architecture, as final project for Computer Organization course. It can run some basic 16bit instructions and small programs. The processor is implemented using structural Verilog, using logic gates only.   * **Semi-Autonomous Robot Dog Project -** [GitHub Connector | Low-Code GitHub Integration | Cyclr](https://github.com/anasabk/BitBuddy) **-** [Youtube - Free logo icons](https://youtu.be/I4R-5OSDzrc) **-** [Website icons for free download | Freepik](https://roklentoo.wixsite.com/sitem/)   *School Project*  *Tools: Python*  A 4-legged semi-autonomous robot dog is developed as a group project for Computer Engineering Project course, throughout a semester. It is capable of mapping its environment, receiving commands to move to specific locations, and recognizing objects.  I have taken part mostly in mapping module of the robot, working on 2D map construction, camera calibration and path planning.   * **Appointment Application on Android –** [GitHub Connector | Low-Code GitHub Integration | Cyclr](https://github.com/hasanmutlu26/randebul)   *School Project*  *Tools: Flutter, Dart, Firebase, Agile*  An Android application was developed as a group project for Software Engineering course, throughout a semester.The application allows customers to make appointments with people and companies providing services in wide variaty of different fields such as dentists, music instructors or carrying agents. I have taken part in both front-end and back-end development. |