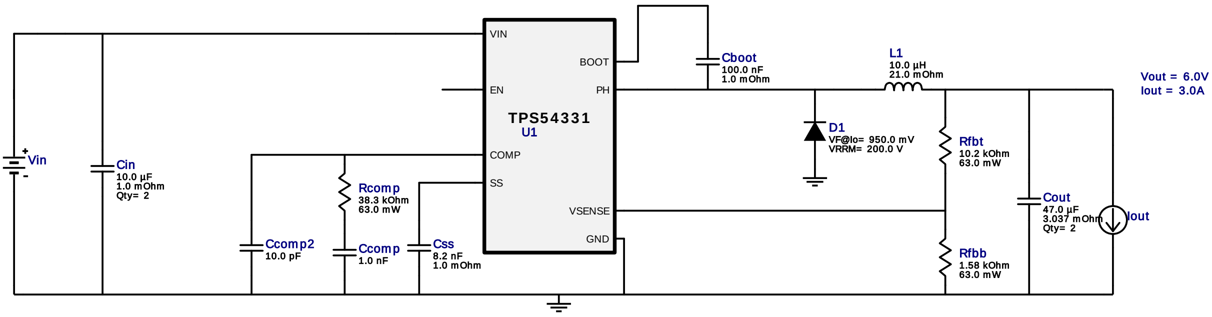
VinMin = 9.0V
VinMax = 24.0V
Vout = 6.0V
Iout = 3.0A

Device = TPS54331DDAR
Topology = Buck
Created = 2022-05-19 07:53:45.931
BOM Cost = $2.49
BOM Count = 14
Total Pd = 3.31W

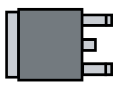
WEBENCH ® Design Report

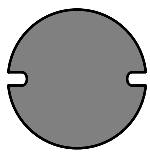
Design : 9 TPS54331DDAR
TPS54331DDAR 9V-24V to 6.00V @ 3A



Electrical BOM

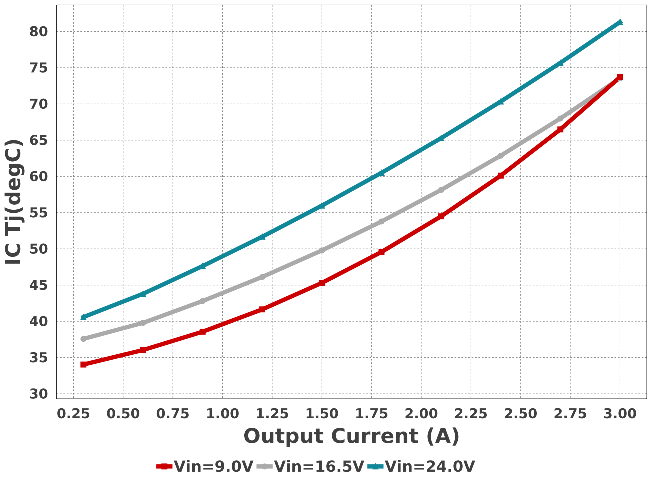
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	MuRata	GRM155R71A104KA01D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 10.0 V IRMS= 0.0 A	1	$0.01	0402 3 mm^2
Ccomp	MuRata	GRM1555C1H102JA01J Series= C0G/NP0	Cap= 1.0 nF VDC= 50.0 V IRMS= 0.0 A	1	$0.01	0402 3 mm^2
Ccomp2	Samsung Electro-Mechanics	CL21C100JBANNNC Series= C0G/NP0	Cap= 10.0 pF VDC= 50.0 V IRMS= 0.0 A	1	$0.01	0805 7 mm^2
Cin	TDK	C3225X7R1H106M250AC Series= X7R	Cap= 10.0 uF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 5.0 A	2	$0.28	1210 15 mm^2
Cout	MuRata	GRM32ER61C476KE15L Series= X5R	Cap= 47.0 uF ESR= 3.037 mOhm VDC= 16.0 V IRMS= 4.59346 A	2	$0.38	1210_280 15 mm^2
Css	MuRata	GRM033R71A822KA01D Series= X7R	Cap= 8.2 nF ESR= 1.0 mOhm VDC= 10.0 V IRMS= 0.0 A	1	$0.01	0201 2 mm^2
D1	SMC Diode Solutions	SBRD10200TR	VF@Io= 950.0 mV VRRM= 200.0 V	1	$0.12	DPAK 102 mm^2
L1	Bourns	SDR1307-100ML	L= 10.0 µH 21.0 mOhm	1	$0.42	SDR1307 226 mm^2
Rcomp	Vishay-Dale	CRCW040238K3FKED Series= CRCW..e3	Res= 38.3 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rfbb	Vishay-Dale	CRCW04021K58FKED Series= CRCW..e3	Res= 1.58 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2



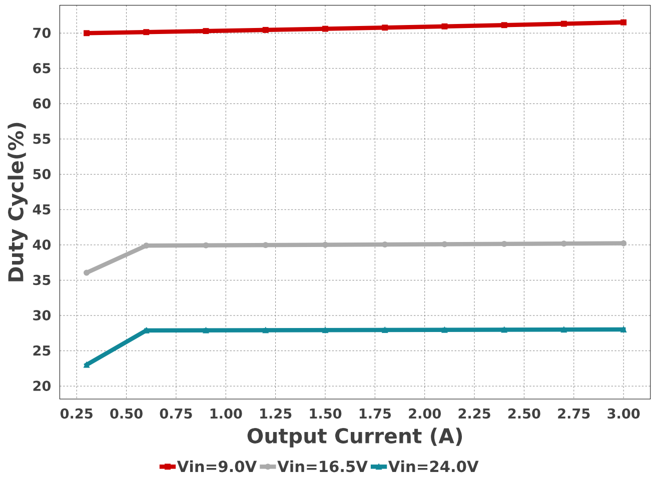


Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Rfbt	Vishay-Dale	CRCW040210K2FKED Series= CRCW..e3	Res= 10.2 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm²
U1	Texas Instruments	TPS54331DDAR	Switcher	1	$0.56	DDA0008H 55 mm²

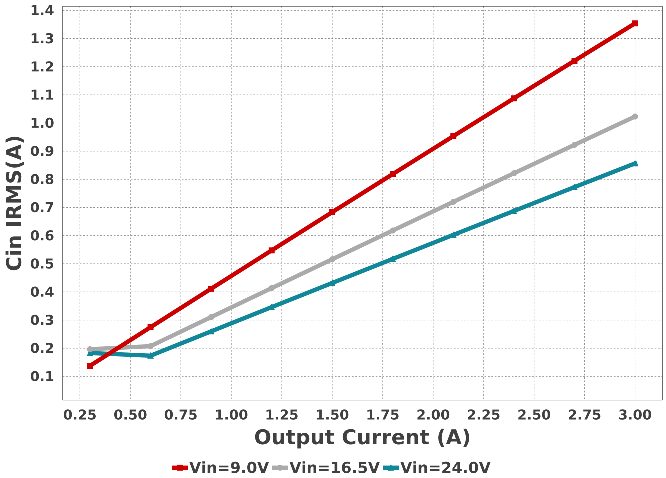
IC Tj



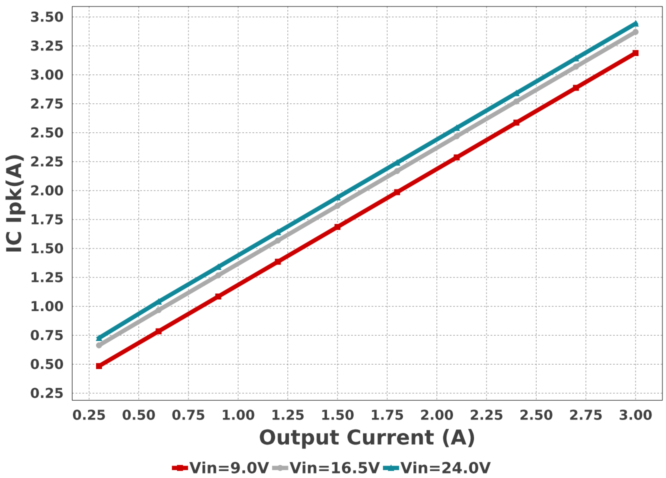
Duty Cycle



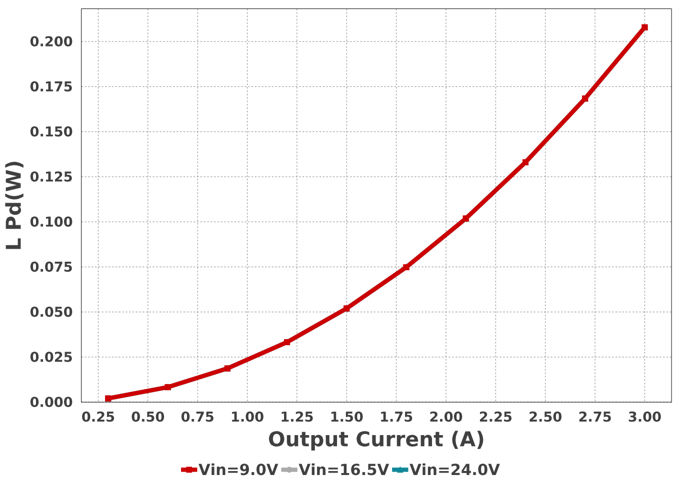
Cin IRMS



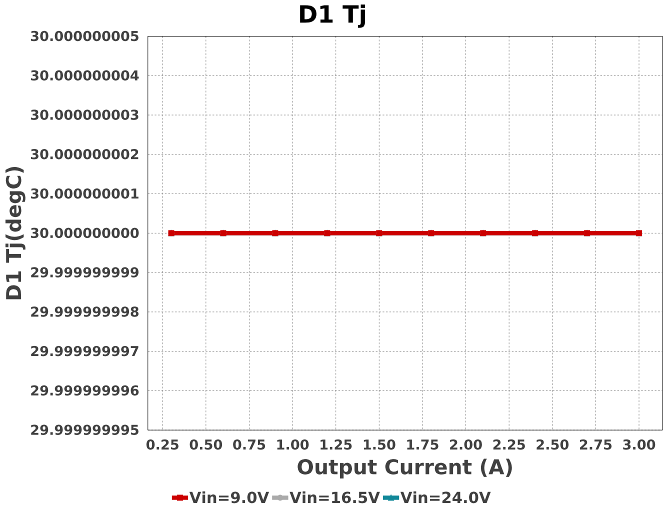
IC Ipk

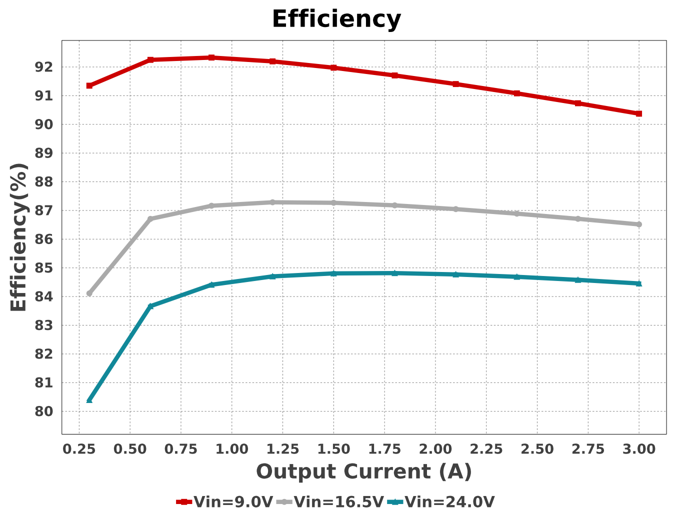


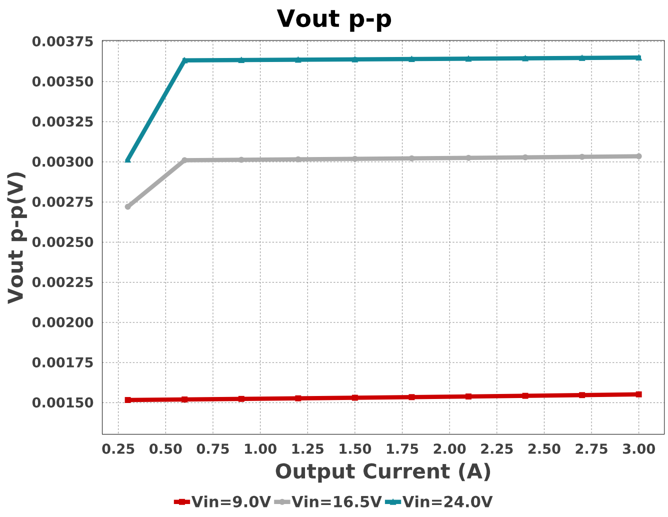
L Pd

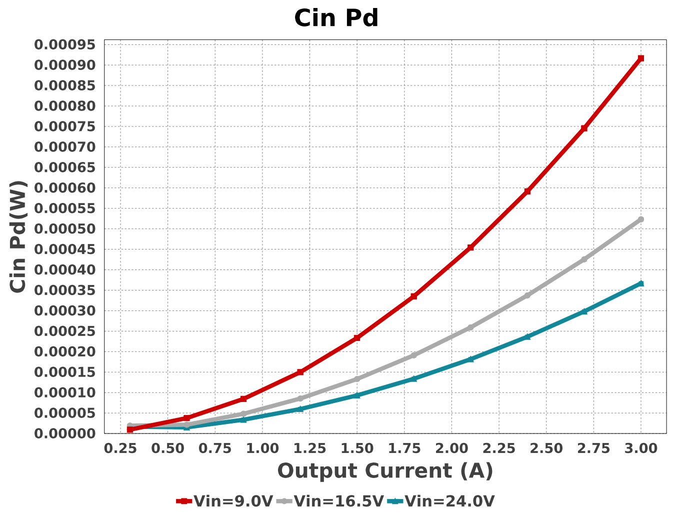


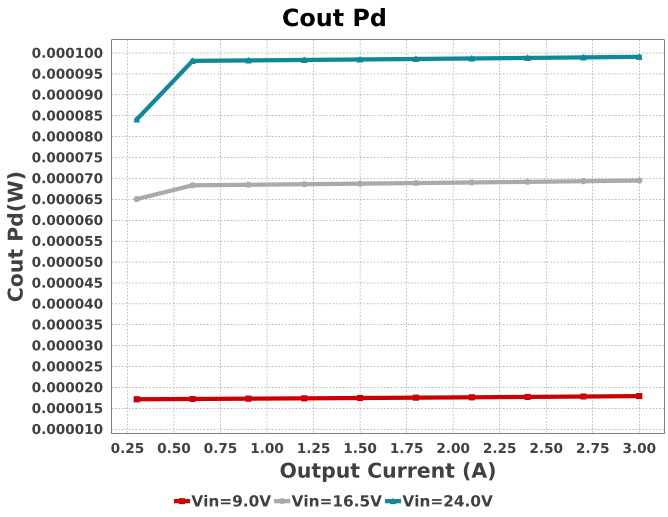
D1 Tj

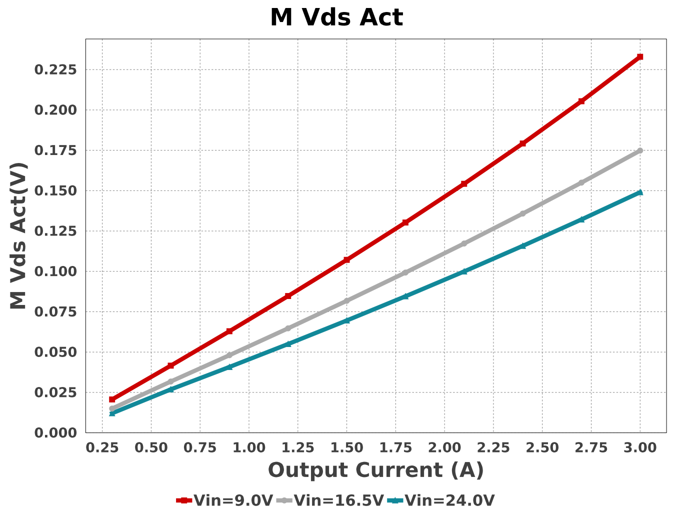


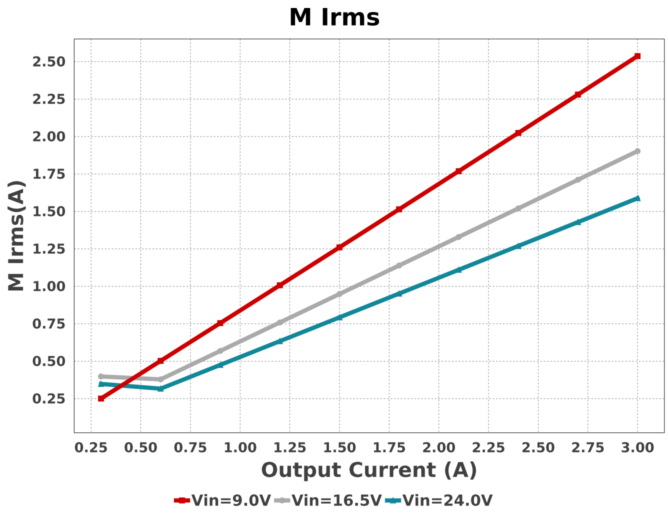

Efficiency
Efficiency(%)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V

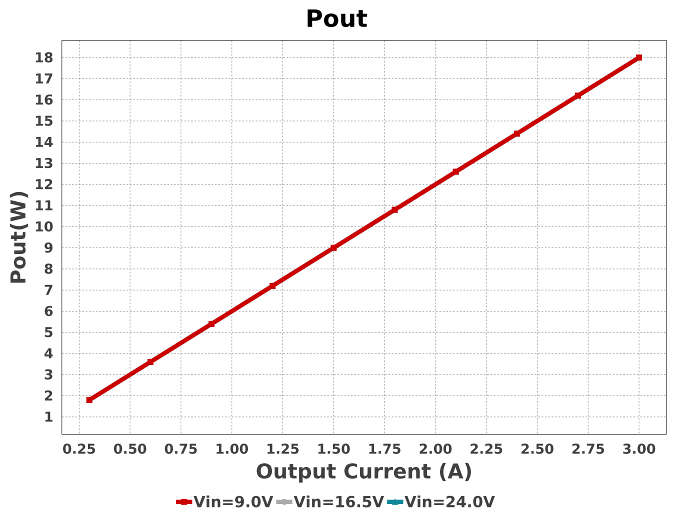

Vout p-p
Vout p-p(V)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V

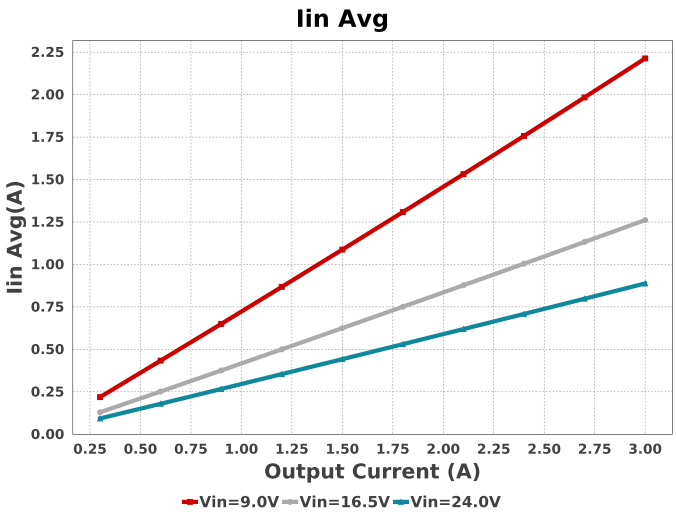

Cin Pd
Cin Pd(W)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V

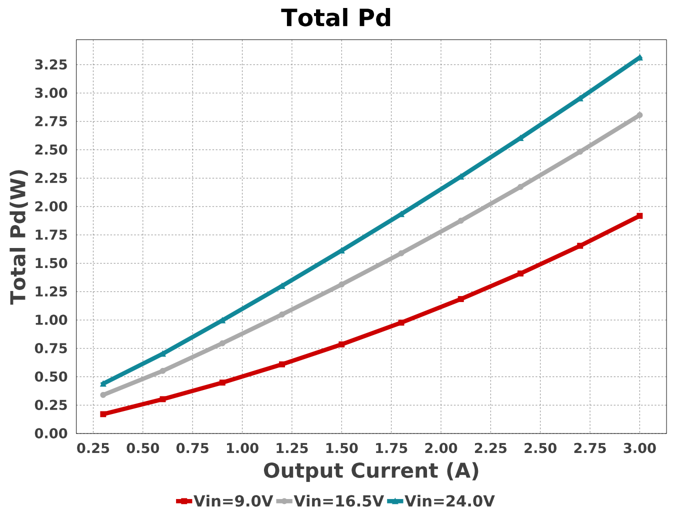

Cout Pd
Cout Pd(W)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V

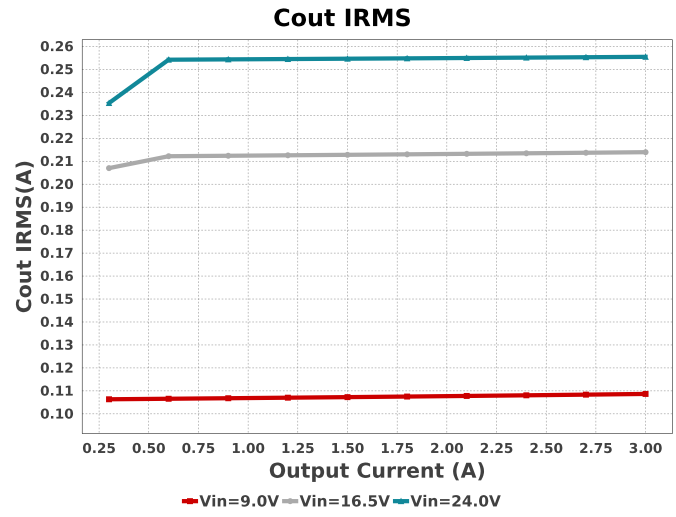

M Vds Act
M Vds Act(V)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V

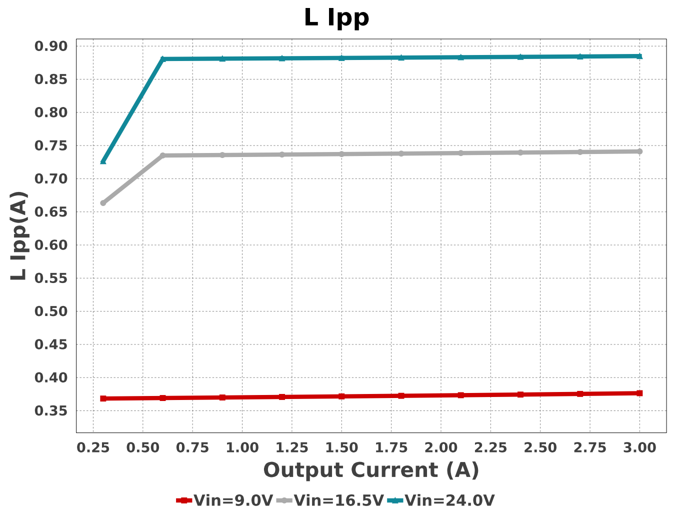

M Irms
M Irms(A)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V

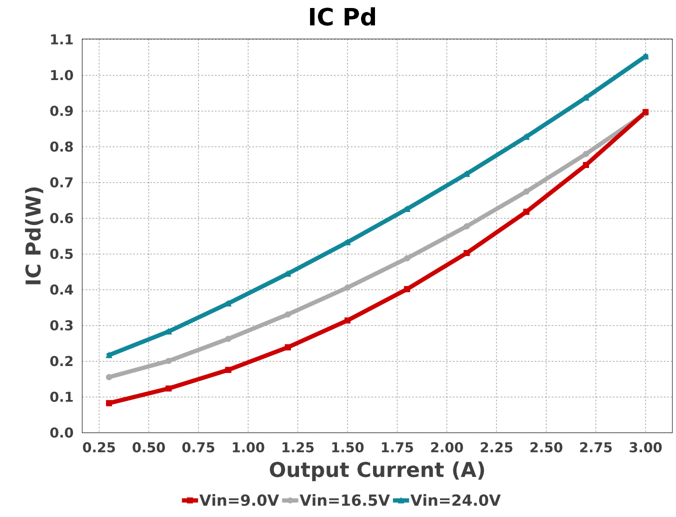

Pout
Pout(W)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V

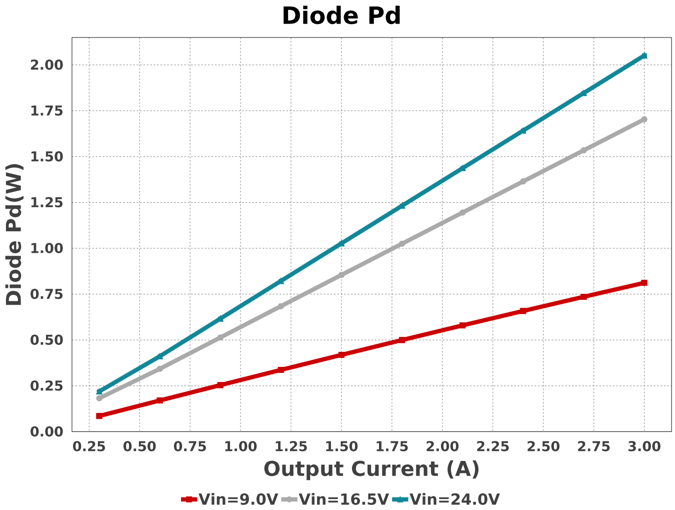

Iin Avg
Iin Avg(A)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V

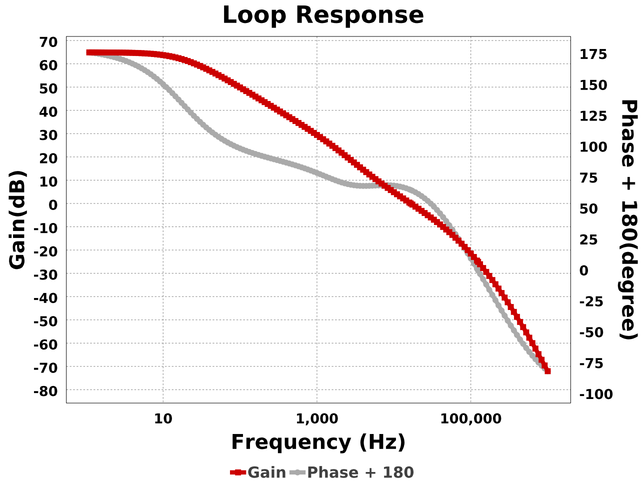

Total Pd
Total Pd(W)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V


Cout IRMS
Cout IRMS(A)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V


L Ipp
L Ipp(A)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V


IC Pd
IC Pd(W)
Output Current (A)
Vin=9.0V Vin=16.5V Vin=24.0V





Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	856.529 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	366.82 µW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	255.46 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	99.097 µW	Capacitor	Output capacitor power dissipation
5.	D1 Tj	30.0 degC	Diode	D1 junction temperature
6.	Diode Pd	2.051 W	Diode	Diode power dissipation
7.	IC Ipk	3.442 A	IC	Peak switch current in IC
8.	IC Pd	1.053 W	IC	IC power dissipation
9.	IC Tj	81.293 degC	IC	IC junction temperature
10.	ICThetaJA	48.7 degC/W	IC	IC junction-to-ambient thermal resistance
11.	Iin Avg	888.04 mA	IC	Average input current
12.	L Ipp	884.94 mA	Inductor	Peak-to-peak inductor ripple current
13.	L Pd	207.9 mW	Inductor	Inductor power dissipation
14.	M Irms	1.588 A	Mosfet	MOSFET RMS ripple current
15.	M Vds Act	149.012 mV	Mosfet	Voltage drop across the MosFET
16.	Cin Pd	366.82 µW	Power	Input capacitor power dissipation
17.	Cout Pd	99.097 µW	Power	Output capacitor power dissipation
18.	Diode Pd	2.051 W	Power	Diode power dissipation
19.	IC Pd	1.053 W	Power	IC power dissipation
20.	L Pd	207.9 mW	Power	Inductor power dissipation
21.	Total Pd	3.313 W	Power	Total Power Dissipation
22.	BOM Count	14	System Information	Total Design BOM count
23.	Cross Freq	16.56 kHz	System Information	Bode plot crossover frequency
24.	Duty Cycle	28.023 %	System Information	Duty cycle
25.	Efficiency	84.456 %	System Information	Steady state efficiency
26.	FootPrint	466.0 mm^2	System Information	Total Foot Print Area of BOM components
27.	Frequency	570.0 kHz	System Information	Switching frequency
28.	Gain Marg	-25.094 dB	System Information	Bode Plot Gain Margin
29.	Iout	3.0 A	System Information	Iout operating point
30.	Low Freq Gain	64.902 dB	System Information	Gain at 1Hz
31.	Mode	CCM	System Information	Conduction Mode
32.	Phase Marg	65.215 deg	System Information	Bode Plot Phase Margin
33.	Pout	18.0 W	System Information	Total output power
34.	Total BOM	$2.49	System Information	Total BOM Cost
35.	Vin	24.0 V	System Information	Vin operating point
36.	Vout	6.0 V	System Information	Operational Output Voltage

#	Name	Value	Category	Description
37.	Vout Actual	5.965 V	System Information	Vout Actual calculated based on selected voltage divider resistors
38.	Vout Tolerance	5.31 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
39.	Vout p-p	3.65 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	3.0	Maximum Output Current
VinMax	24.0	Maximum input voltage
VinMin	9.0	Minimum input voltage
Vout	6.0	Output Voltage
base_pn	TPS54331	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

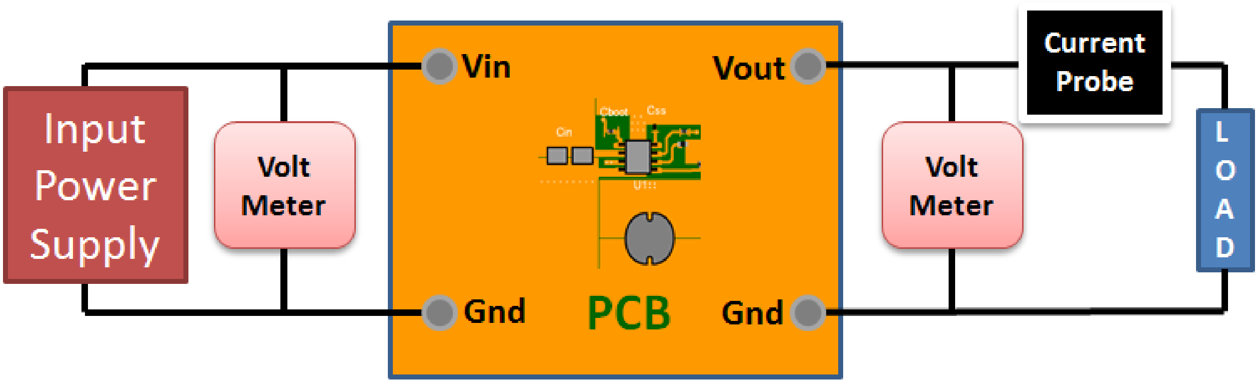
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 9.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : 59D8241C6A2D8FA4[v1]

2. **TPS54331** Product Folder : http://www.ti.com/product/TPS54331 : contains the data sheet and other resources.