## Preliminary remarks.

The diagrams look unfinished to the end. There is no schema. Project-C\_LED.PrjPCB. Many conductors and components are not connected anywhere. Specific errors are indicated in the diagrams (in PDF) in large red print.

## **Power consumption**

To estimate the power consumption of the "antennas", we will take the capacity of one "antenna" 10pF. The maximum signal swing is 30V, therefore, when switching the antenna, the charge is recharged 10 pF \* 30 V = 300 pC. At a signal frequency of 1 MHz, recharging the antenna leads to a current consumption of 300 pC \* 1 MHz \* 2 = 600  $\mu$ A. 320 channels will consume 192mA. Taking into account the DCDC converter from a 12V source, this load will consume 192mA \* 30V / 12V = 480mA. Note that this estimate is based on the antenna capacitance of 10 pF. The real consumption can be higher according to the increase in the antenna capacity.

Estimating the consumption of output buffers. Average consumption of the buffer in static (2.5 mA + 0.6 mA) /2=1.5 mA. From a 12V source, 320 channels will consume 320 \* 1.5 mA / 2 = 600 mA

FPGA consumption, from 12V source: 200mA \* 3.3V / 12V = 440mA.

The total current from the 12V source will be approximately 1.5A, which is quite acceptable for RJ45 (see PoE).

The TPS61175PWPR has sufficient current.

MP2333H has sufficient current.

## Miscellaneous notes

The Project-C\_CONTOLLER.PrjPCB device does not have RJ45 connectors to connect Project-C\_VOLTAGE.PrjPCB.

There is no 12V power connector. This connector should be rated for a current of about 15 A.

I recommend connecting a resistor in parallel with the MCP4562. The value of this resistor must be such that when the MCP4562 is not turned on (or burned out), the VCC\_OUT voltage does not exceed the maximum allowable one (30 V).