

According to the guide given by the tutorial at http://asiri.rathnayake.org/articles/hacking-josh-operating-system-tutorial/, I was able to boot the Josh operating system after overriding USB pen by the floppy image which was formatted with FAT 12. I have added some snapshots of my attempts as follows.

Setting up Netwide Assembler and DOS file utilities

```
### File Edit View Terminal Help

sudo apt-get install nasm
hashini@ubuntu:-$ sudo apt-get install nasm
Reading package lists... Done

Building dependency tree
Reading state information... Done
The following NEW packages will be installed:
    nasm
    0 upgraded, 1 newly installed, 0 to remove and 479 not upgraded.
Need to get 1,039kB of archives.

After this operation, 2,937kB of additional disk space will be used.
Get:1 http://us.archive.ubuntu.com/ubuntu/ lucid/main nasm 2.07-1 [1,039kB]
Fetched 1,039kB in 18s (57.5kB/s)

Selecting previously deselected package nasm.

(Reading database ... 122835 files and directories currently installed.)
Unpacking nasm (from .../archives/nasm 2.07-1_i386.deb) ...

Processing triggers for man-db ...
Processing triggers for fodc-base ...
Processing triggers for install-info ...

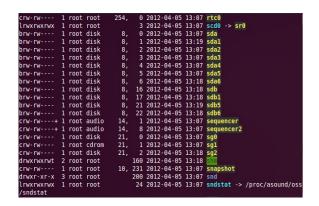
Setting up nasm (2.07-1) ...
hashini@ubuntu:-$ nasm -version
NASM version 2.07 compiled on Nov 5 2009
hashini@ubuntu:-$ 1

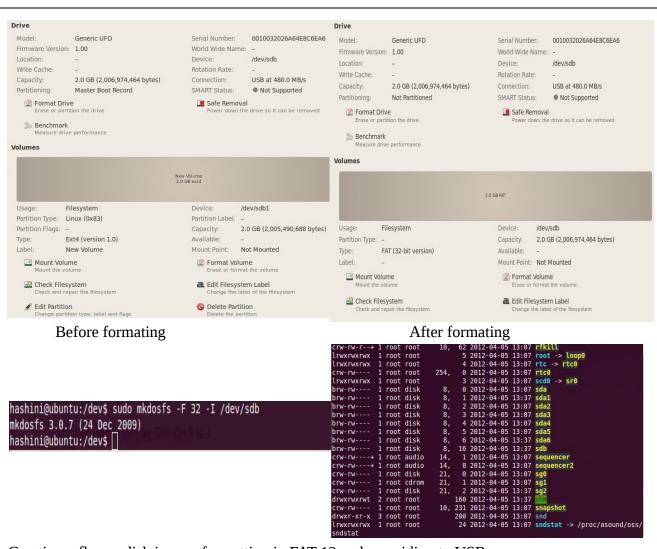
#### Ashini@ubuntu:-$ masm version
NASM version 2.07 compiled on Nov 5 2009
hashini@ubuntu:-$ 1

#### Ashini@ubuntu:-$ masm version
NASM version 2.07 compiled on Nov 5 2009
hashini@ubuntu:-$ nasm version
NASM version 2.07 compiled on Nov 5 2009
```

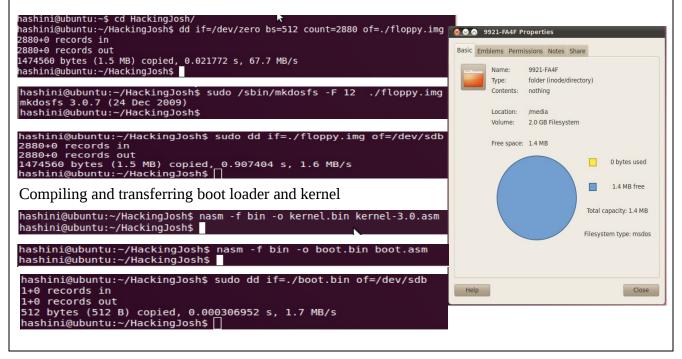
Identifying the USB device under /dev and formatting into a single disk. (in my case sdb along with the primary partition sdb1)

Before plug in After plug in





Creating a floppy disk image, formatting in FAT 12 and overriding to USB.



For the ease of rebooting the computer frequently, I installed Oracle VM Virtual box and proceeded developing with the use of it.

Added Features

'hw' command which shows hardware details on the screen

CPU vendor

CPU type

Mouse details

Keyboard details

RAM size

Installed hard drives

Detected Diskettes

Detected Serial ports

Detected Parallel Ports

'help' command which shows the available command list on the screen

ver -to display version

hw -to display hardware information

exit -to reboot

How Achieved

First of all I had to understand the assembly codes further more and find out from which registries I can get the hardware information. For these purposes I referred the references which I have attached at the end of this document.

Hardware Information

To display hardware information I added the function "_hw" which is called through the shell using "_cmd_hw". If the user entered command matches with cmdhw string, this "_hw" function is proceeded, which calls several other sub functions to show variety of hardware details.

The source code related to this part is as follows.

```
:::added to shell
cmd hw:
      mov si, strCmd0
      mov di, cmdHW
      mov cx, 1
      repe cmpsb
      jne cmd exit ;next command
      call hw
      jmp cmd done
                           ;;;calls to desplay hardware info
hw:
      call display endl
      mov sī, strHardware
      mov al, 0x01
      int 0x21
      call display endl
      call _cpu_details
      call mouse details
      call keyboard details
      call memory details
      call hardDrive details
      call diskette details
      call serial details
      call parallel details
      call bios details
      call display endl
      ret
[SEGMENT .data]
                          db
                                 "Hardware Information...",0x00
      strHardware
      cmdHW
                           db
                                  "hw", 0x00 ; internal commands
[SEGMENT .bss]
      strCmd0
                  resb 256
                                 ; buffers for the command components
```

CPU details

```
;************CPU details*************
                                                  call display endl
                                                                             ;cpu type
_cpu_details:
                                                  mov sī, strCpuType
mov al, 0x01
      call display endl
                        ;cpu vender
                                                  int 0x21
      mov si, strVendor
      mov al, 0x01
                                                  mov eax, 0x80000002
      int 0x21
                                                  cpuid
                                                  mov [strBrand],eax
                                                  mov [strBrand+4],ebx
      mov eax, 0x00
                                                  mov [strBrand+8].ecx
                                                  mov [strBrand+12].edx
      cpuid
      mov [strCPUID], ebx
                                                  mov eax, 0x80000003
      mov [strCPUID+4], edx
                                                  cpuid
                                                  mov [strBrand+16],eax
      mov [strCPUID+8], ecx
                                                  mov [strBrand+20],ebx
                                                  mov [strBrand+24],ecx
      mov si, strCPUID
                                                  mov [strBrand+28],edx
      mov al, 0x01
                                                  mov eax, 0x80000004
      int 0x21
                                                  cpuid
                                                  mov [strBrand+321.eax
                                                  mov [strBrand+36],ebx
                                                  mov [strBrand+40],ecx
                                                  mov [strBrand+44].edx
                                                  mov si, strBrand
mov al, 0x01
                                                  int 0x21
```

"cpuid" opcode is used to access the information on CPU; vendor and processor brand. Here the arguments for the opcode are given through the register "eax" and used "eax", "ebx", "ecx" and "edx" registers to get the results. The values in those registers are stored as a single string to get the final result.

(The **CPUID** opcode is a processor supplementary instruction (its name derived from CPU IDentification) for the x86 architecture.)

EAX=0: Get vendor ID

EAX=80000002h,80000003h,80000004h: Processor Brand String

(http://en.wikipedia.org/wiki/CPUID)

Mouse Details

```
;*************Mouse Details*********
mouse details:
      call display endl
       xor ax, ax
       int 0x11
                            ;bit 2
       and ax, 0x04
       shr ax, 2
       cmp ax, 0x01
       je endmouse
       mov si, strnomouse ;if no mouse
       mov al, 0x01
       int 0x21
       jmp end1
                              ;if detected
       endmouse:
       mov si, strismouse
       mov al, 0x01
       int 0x21
       end1:
       ret
```

With the use of BIOS Service interrupts we can get hardware information which are stored in BIOS data area where the results are get stored in "ax" register after the interrupt call.

Here I have used the interrupt 0x11 (BIOS-Get equipment list).

Return:

(E)AX = BIOS equipment list word

Since older BIOSes do not know of the existence of EAX, the high word of EAX should be cleared before this call, if any of the high bits will be tested (xor ax,ax)

```
Bit fields for BIOS equipment list:

Bit(s) Description

2 pointing device installed (PS)
```

Calling interrupt 0x11 will store BIOS equipment list flags in ax register. Using "and" command masks the needed bit (2^{nd} bit). Then ax is right shifted by 2 bits to get the expected value. Then by comparing the value in ax, determine if a PS2 mouse in installed or not. Value 0x00- for no PS2 mouse detection and value 0x01- for PS2 mouse detection.

Keyboard details

```
Here the interrupt 0x16 is used.
;**********Keyboard details********
keyboard details:
      call _display_endl
                                           Int 16/AH=F2h
      xor ah,ah
      mov ah.0xf2
      int 0x16
      cmp al,0x00
                                           (Compag 386 and newer - DETERMINE ATTACHED
      je none
                   ;if 9-bit PC keyboard
      mov si, strPC
                                           KEYBOARD TYPE)
      mov al, 0x01
      int 0x21
      jmp _end2
                                           Return:
       none:
      cmp al,0x01
                                           AL = type
      mov si, strnokey ;if no keyboard detected 00h if 11-bit AT keyboard is in use
      mov al, 0x01
                                           01h if 9-bit PC keyboard is in use
      int 0x21
                                           AH = 00h (04/08/93 \text{ system ROM})
      jmp _end2
       AT:
      mov si, strAT
                    ;if 11-bit AT keyboard
      mov al, 0x01
                                           By calling the above mentioned interrupt, we can gain
      int 0x21
      jmp _end2
                                           the keyboard type of PCs which are newer than compag
                                           386 by looking in to the stored value at "al".
       end2:
      ret
```

Memory details

```
;************memory details****************
                                                                      mem cal:
memory details:
                                                                     \bar{s}hr \bar{d}x, 4
                                                                                               ;divide dx by 2^4
       call display endl
                                                                                               ;divide cx by 2^10
                                                                     shr cx, 10
       xor ax,ax
                                                                     add cx,dx
                                                                                               ;get the total
       xor bx,bx
                                                                     mov dx, cx
       xor cx,cx
                                                                     call hex2dec
                                                                                               ;convert hex to decimal
       xor dx,dx
                                                                     mov sī, strMB
       mov ax. 0xe801
                                                                     mov al, 0x01
       int 0x15
                                                                     int 0x21
       jc _error
                             ; if CF is set on an error
                                                                     jmp _memdone
       cmp ah, 0x86
                             ; check for unsupported function
       je error
                                                                      error:
                                                                                               ;error message
       cmp ah, 0x80
                             ; check for invalid command
                                                                     mov si, strMemErr
       je error
                                                                     mov al, 0x01
       mov si, strmem
                                                                     int 0x21
       mov al. 0x01
       int 0x21
       cmp cx, 0x0000
                                                                      memdone:
                             :if cx=0
                                                                     ret
       je _cx_zero
       jmp mem cal
                             ;solve cx conflict
        cx_zero:
       mov cx,ax
       mov dx,bx
```

Here interrupt 0x15 is used with EAX = 0xE801.

Int 15/AX=E801h (Phoenix BIOS v4.0 - GET MEMORY SIZE FOR >64M CONFIGURATIONS)

Return:

```
CF clear if successful

AX = extended memory between 1M and 16M, in K (max 3C00h = 15MB)

BX = extended memory above 16M, in 64K blocks

CX = configured memory 1M to 16M, in K

DX = configured memory above 16M, in 64K blocks

CF set on error
```

For error detection CF is checked and further more ah is compared with 0x86- to check whether the function is supported and ah is compared with 0x80- to check whether command is invalid. On some systems, the BIOS returns CX=DX=0000h; in this case, use AX and BX instead of CX and DX. This is also corrected. "_hex2dec" converts hexadecimal value to decimal. To convert into MB, "cx" is divide by 2^10 and "dx" is divided by 2^4 (dx is in 64K blocks).

Hard drive details

Here I have accessed BIOS Data area with the use of offsets, unlike using interrupts as described under Mouse details. The BIOS data area is created at memory location 0040:0000h with a typical size of 255 bytes, when the computer powered on.

To get hard drive details we have read the offset 40:75 with the use of ax and es registers. es register value has been restored at the end with the help of stack.

Diskette details

```
;************Diskette details*************
diskette details:
       call display endl
       mov si, strDiskette
mov al, 0x01
       int 0x21
       xor ax, ax
       int 0x11
       and ax,0x01
       cmp ax,0x01
       je _is_floppy
       mov ah, 0x0e
                        ;if no floppy print 0
       mov al, '0'
       int 0x10
        is floppy:
                       ;if floppy is installed
       and ax, 0xc0
                       ;bit 6 and 7
       shr ax, 6
       add ax, 49
                        ;convert to ascii (+1)
       mov ah, 0x0e
       int 0x10
       ret
```

Just like we gain details regarding mouse, here also used the interrupt 0x11. In this case we can use 0,6 and 7 bits.

Bitfields for BIOS equipment list:

Bit(s) Description
0 floppy disk(s) installed (number specified by bits 7-6)
7-6 number of floppies installed less 1 (if bit 0 set)

Bit 0 is checked to see whether any floppy disk is installed. If it is set the number

of floppies are read by extracting bits 6

and 7 (To convert the no of diskettes added 49 because it is less than 1, otherwise prints θ .

Serial details

```
:**********Serial details***********
serial details:
                                       Again used interrupt 0x11 to obtain serial details.
      call display endl
      mov si, strSerial
                                       Bit fields for BIOS equipment list:
      mov al, 0x01
                                                    Description
                                       Bit(s)
      int 0x21
                                       11 - 9
                                                    number of serial ports installed
      xor ax. ax
      int 0x11
      and ax, 0xe00
                    ;bits 9-11
                                       To convert the value into ASCII, added 48.
      shr ax, 9
      add ax, 48
                    ;converts to ascii
      mov ah, 0x0e
      int 0x10
      ret
```

Parallel details

```
:*********Parallel details*********
_parallel_details:
      call _display_endl
                                       Same as above where 14 and 15 bits are extracted.
      mov si, strParallel
      mov al, 0x01
                                       Bit fields for BIOS equipment list:
       int 0x21
                                       Bit(s)
                                                  Description
      xor ax, ax
      int 0x11
                                       15-14
                                                   number of parallel ports installed
       and ax, 0xc000 ;bits 14 and 15
      shr ax, 14
       add ax, 48
                    ;converts to ascii
      mov ah, 0x0e
      int 0x10
```

Bios details

```
:*********Bios details**********
bios details:
       call _display_endl
       mov si, strBios
       mov al, 0x01
       int 0x21
       push es
       mov ax, 0xf000 ;BIOS release date
       mov es, ax ;
mov si, 0xfff5
                             is in F000:FFF5
       mov bl,8
                       ;loop to print
       mov al, [es:si]
       mov ah, 0x0e
       int 0x10
       inc si
       dec bl
       cmp bl, 0
       jne _loop
       pop es
       ret
```

By reading the memory locations F000 to FFF5 we can obtain Bios release date in format DD/MM/YY consists of 8 characters. The loop run for 8 times which prints on character at each time.

Available Commands

```
;*********Available Commands*******
help:
       call display endl
       mov si, strCommands
       mov al, 0x01
       int 0x21
       call display endl
       mov si, strVer
       mov al, 0x01
       int 0x21
       call _display_endl
       mov si, strHW
       mov al, 0x01
       int 0x21
       call _display_endl
       mov sī, strExīt
       mov al, 0x01
       int 0x21
       call display endl
       ret
```

This function prints all the available commands in the modified Josh Operating System.

```
Machine View Devices Help

Loading Boot Image
......

Welcome to JUSH Ver 0.01 with added features by Hashini Senaratne

Type 'help' to get the list of shell commands
JUSH>>help
Available Commands are...
ver -to display version
hw -to display hardware information
exit -to reboot

JUSH>>

Selection Commands

JUSH>>

JUSH>>
```

```
JOSH>>ver
JOSH version 0.03
JOSH>>hw
Hardware Information...
CPU vendor
                        : GenuineIntel
CPU type
                        Mouse details (PS)
                        : Mouse is installed
                        : 9-bit PC keyboard is in use
Keyboard details
                        : 511MB
RAM size
Installed hard drives
                        : 1
Detecked Diskettes
                        : 1
Detecked Serial Ports
                        : 0
Detecked Parallel Ports
                       : 0
Bios released
                        : 06/23/99
JOSH>>exit
```

References
http://asiri.rathnayake.org/articles/hacking-josh-operating-system-tutorial/http://problemsolvedweb.blogspot.co.uk/2012/04/hacking-josh-with-virtual-hardware.html http://www.bioscentral.com/misc/bda.html http://stanislavs.org/helppc/bios_data_area.html https://github.com/sunimalr/Hacking-Josh-OS http://insightforfuture.blogspot.com/2012/01/hacking-josh-assembly-operating-system.html http://www.ctyme.com/intr/int.html http://leto.net/writing/nasm.php http://www.posix.nl/linuxassembly/nasmdochtml/nasmdoca.html http://www.cs.virginia.edu/~evans/cs216/guides/x86.html