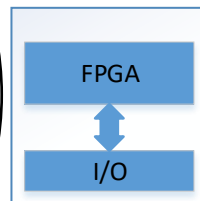
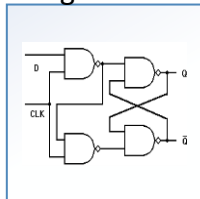


System Level



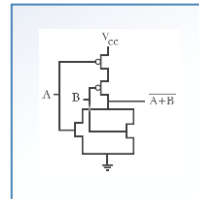
Logic Level



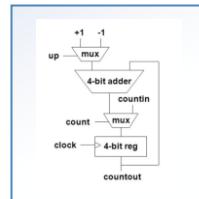
Algorithmic Level

```
A: A * B + F
  IF (C= TRUE)
    THEN
      A:= A + 2 * F
    ELSE A:= A - 1
  ENDIF
```

Transistor



RTL



Layout

