CS401 Solved MCQs from Online Quizzes- Computer Architecture and Assembly Language

Question # 1 of 10

mov [si+300], ax is an example of Indexed Register Indirect + Offset
True
False

Question # 2 of 10 (Start time: 06:51:43 PM)

we can not Subrtace index register from the base register(bx-si)in assemlby language
true
false

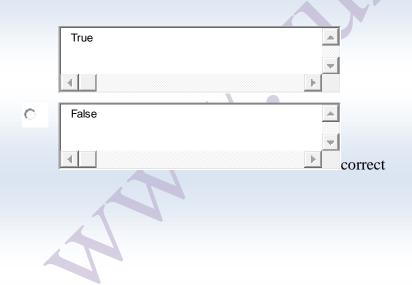**In direct addressing the memory address given in the instruction is**

   ○ | Fixed | correct

Variable

Register

Empty

The first 16-bit processor produced by "Intel" was 8085

True

○ False

correct

BP by default associated with

CS

IP

SS

○ SP   correct

Register to memory operation is not allowed

True

○ False   correct

Address is alway go from

Processor to memory   correct

Memory to processor

Memory to memory

None of the given

Data bus is

Uni-directional

Bi-directional   correct

Non-directional

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None of the given

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Quiz - Mozilla Firefox 4.0 Beta 8

http://quiz.vu.edu.pk/QuizQuestion.aspx?ver=c5fdc013-a38a-41f3-82f8-e01a60e90622   Feedback ▾

Time Left **40** sec(s)

Quiz Start Time: 02:04 PM

iAPX88 Architecture consists of

Select correct option:

○ 14 Registers

○ 16 Registers

○ 32 Registers

▶   Click here to Save Answer & Move to Next Question

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MC100404611 : Sajid Mehmood

Time Left **65** sec(s)

Quiz Start Time: 02:04 PM

Question # 5 of 10 (Start time: 02:13:10 PM)   Total Marks: 1

All the addressing mechanisms in iAPX88 return a number called _____ address.

Select correct option:

- Effective address

- ◉ Physical address

- Direct address

- None of the given

Click here to Save Answer & Move to Next Question

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MC100404611 : Sajid Mehmood

Time Left 78 sec(s)

Quiz Start Time: 02:04 PM

Question # 7 of 10 [Start time: 02:14:08 PM]   Total Marks: 1

Assembly languague is not a low level language.

Select correct option:

○ True

◉ False

Click here to Save Answer & Move to Next Question

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MC100404611 : Sajid Mehmood

Time Left 46 sec(s)

Quiz Start Time: 02:04 PM

Motorola follow

Select correct option:

○ Big endian

○ Litten endain

○ Both of them

○ None of the given

Click here to Save Answer & Move to Next Question

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MC100404611 : Sajid Mehmood

Time Left 66 sec(s)

Quiz Start Time: 02:04 PM

Question # 10 of 10 (Start time: 02:16:23 PM)   Total Marks: 1

Registers are also called scratch pad ram

Select correct option:

◉ True

○ False

Click here to Save Answer & Move to Next Question

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Quiz - Windows Internet Explorer

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MC110203429 : Saima Abbasi

Time Left 89 sec(s)

Quiz Start Time: 09:57 AM

Question # 1 of 10 (Start time: 09:57:25 AM)   Total Marks: 1

Group of bits processor uses to inform memory which element to read/write is collectively known as

Select correct option:

○ Control bus

○ Data bus

○ Address bus

○ RAM

Click here to Save Answer & Move to Next Question

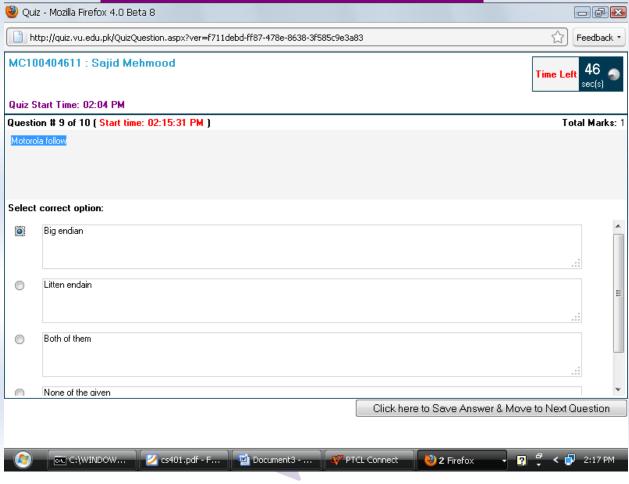Done   Internet | Protected Mode: Off   100%

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Quiz - Windows Internet Explorer

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MC110203429 : Saima Abbasi

Time Left **81** sec(s)

Quiz Start Time: 09:57 AM

Question # 3 of 10 (Start time: 09:59:53 AM)   **Total Marks: 1**

CX register mostly use a

Select correct option:

○ Counter register

○ Flag register

○ Base register

○ Desination register

Click here to Save Answer & Move to Next Question

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Page 2   Sec 1   2/2   At 1"   Ln 1   Col 2   REC TRK EXT OVR

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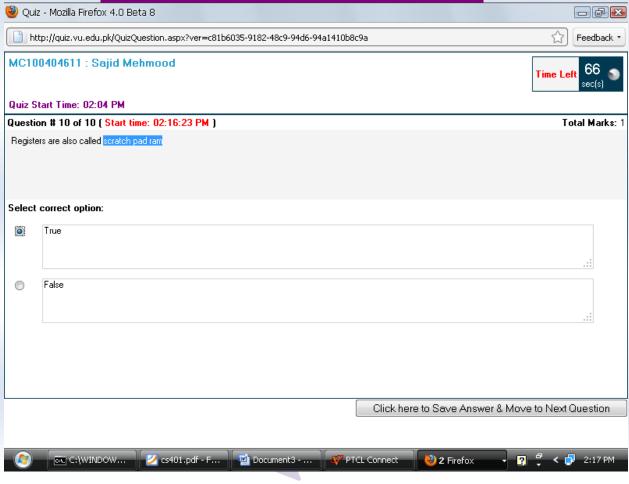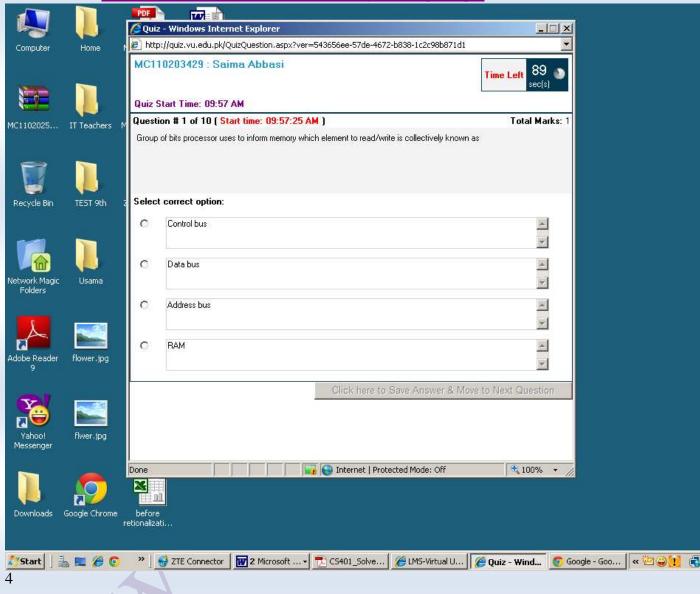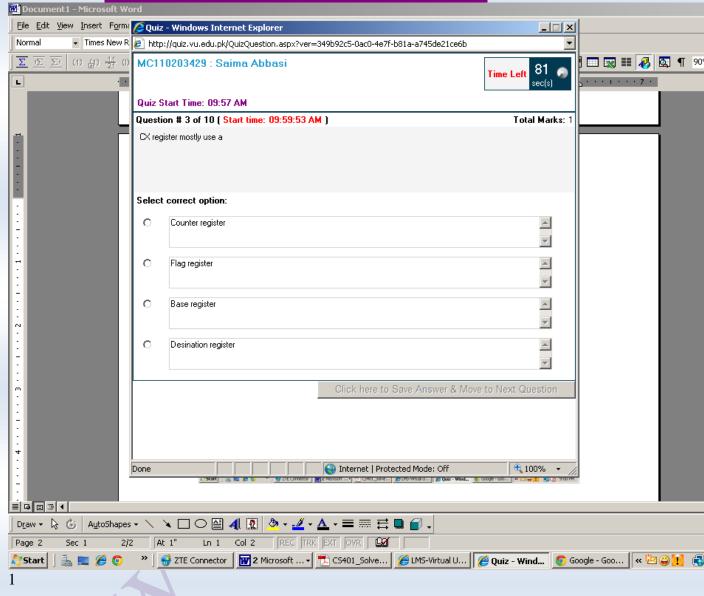
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MC110203429 : Saima Abbasi

Time Left **64** sec(s)

Quiz Start Time: 09:57 AM

Question # 8 of 10 (Start time: 10:05:46 AM)   Total Marks: 1

Memory cell can not be wider than the data bus.

Select correct option:

○ True

○ False

Click here to Save Answer & Move to Next Question

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Basic Registers in Computer processor

www.writework.com › ... › Programming Languages (65)

All data must be represented in a register before it can be processed. For example ... In general the **memory cell cannot be wider than** the width of the **data bus**. ...

Start   ZTE Connector   2 Microsoft ...   CS401_Solve...   LMS-Virtual U...   Quiz - Wind...   memory cell c...   9:07 PM

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