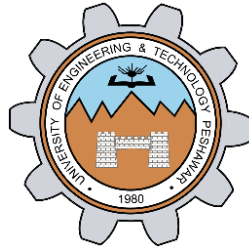


# **Lab Report - 08**

## **Characteristics of Transistor**



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"On my honor, as student of University of Engineering and Technology, I have neither given nor received unauthorized assistance on this academic work."

Student Signature: \_\_\_\_\_

Submitted to:

**Engr. Usman Malik**

Month Day, Year (15 05, 2024)

**Department of Computer Systems Engineering**  
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## Objectives:

- To determine transistor type (npn, pnp), terminals and material using Digital multimeter.
- To determine the values of the alpha and beta ratios of transistors.

## Equipment:

1. Digital Multimeter (DMM)
2. DC Power Supply

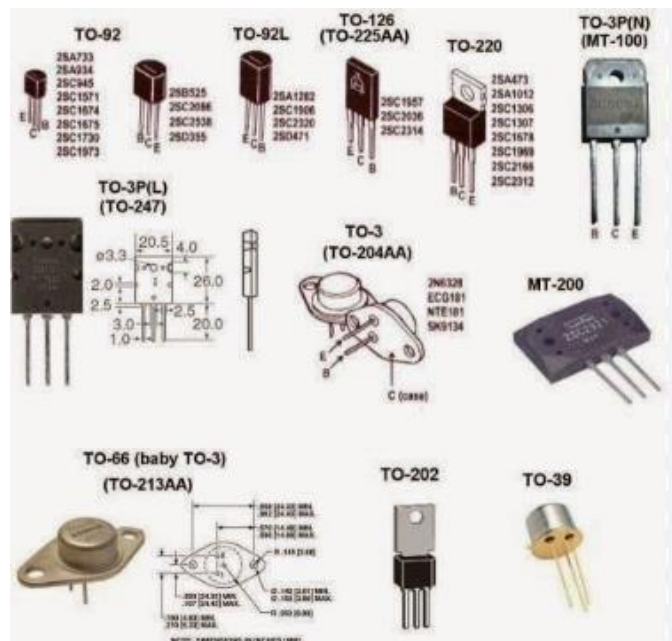
## Components

1. Resistors: 1 k $\Omega$ , 330 k $\Omega$ ,
2. 10 k $\Omega$  potentiometer, 1M $\Omega$  potentiometer
3. Transistors: 2N390

## Theory:

### 1. Transistor:

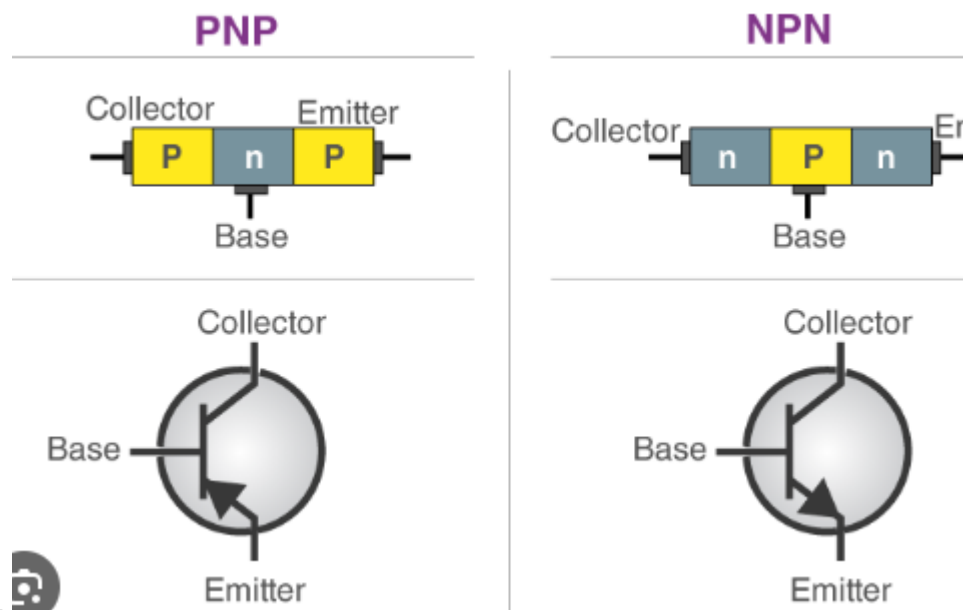
A transistor is a semiconductor device used to amplify or switch electronic signals and electrical power. It is composed of semiconductor material usually with at least three terminals for connection to an external circuit.



## Bipolar Junction Transistor (BJT)

Bipolar Junction Transistor (BJT) is a Semiconductor device constructed with three doped Semiconductor Regions (Base, Collector and Emitter) separated by two p-n Junctions, Figure 1.

p-n Junction between the Base and the Emitter has a Barrier Voltage ( $V_0$ ) of about 0.6 V, which is an important parameter of a BJT. The relationships between the voltages and the currents associated with a BJT under various operating conditions determine its performance. These relationships are collectively known as characteristics of the transistor.

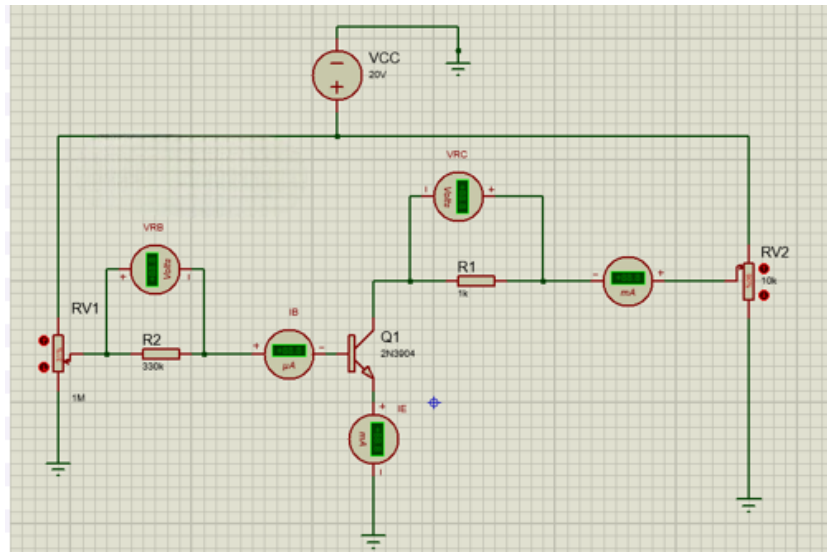


### **Biasing Circuits of BJT**

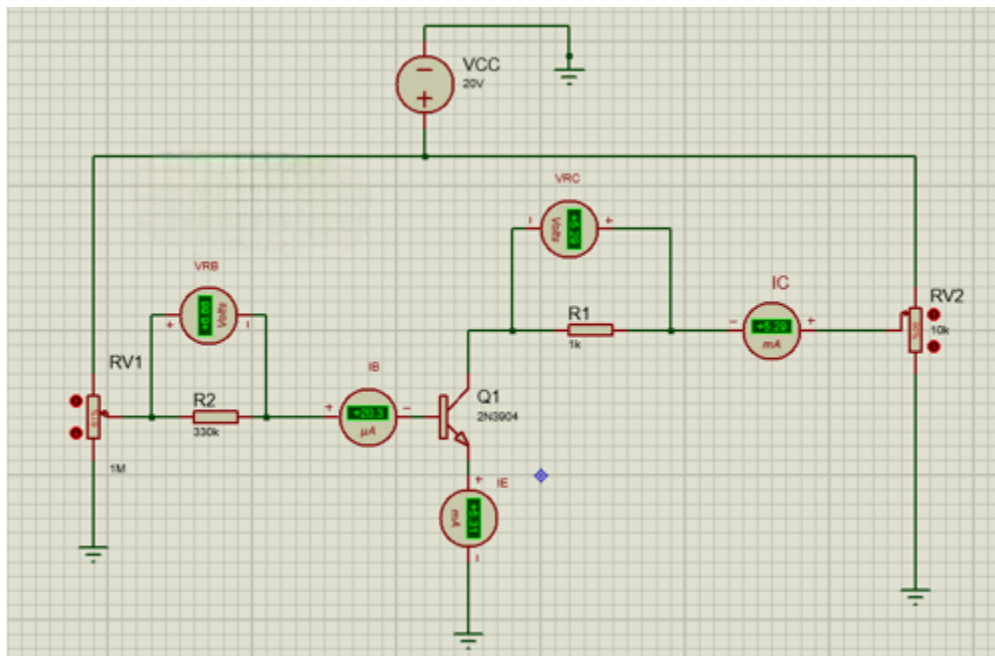
To make the Q point stable different biasing circuits are tried. The Q point is also called as operating bias point, is the point on the DC load line (a load line is the graph of output current vs. output voltage in any of the transistor configurations) which represents the DC current through the transistor and voltage across it when no ac signal is applied. The Q point represents the DC biasing condition. When the BJT is biased such that the Q point is halfway between cutoff and saturation than the BJT operates as a CLASS-A amplifier. The three circuits or biasing arrangements which are practically used are explained below.

**Circuit:**

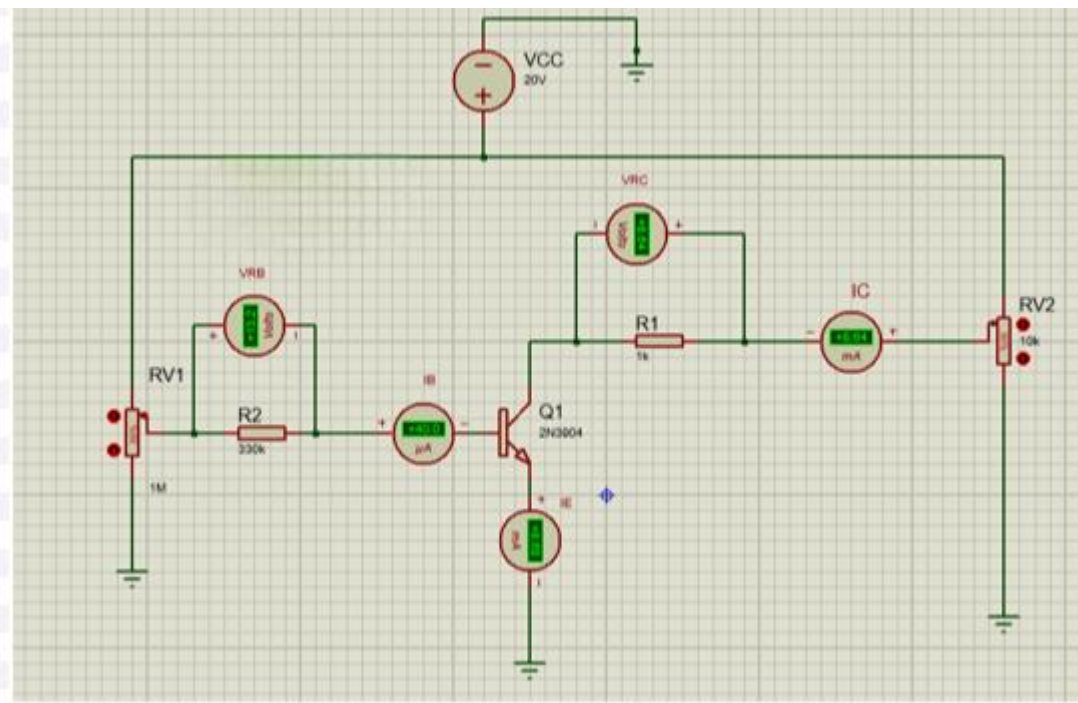
**$E_{RB} = 3.3\text{ V}$ ,  $I_B = 10\text{ micro A}$**



**$E_{RB} = 3.3\text{ V}$ ,  $I_B = 10\text{ micro}$**

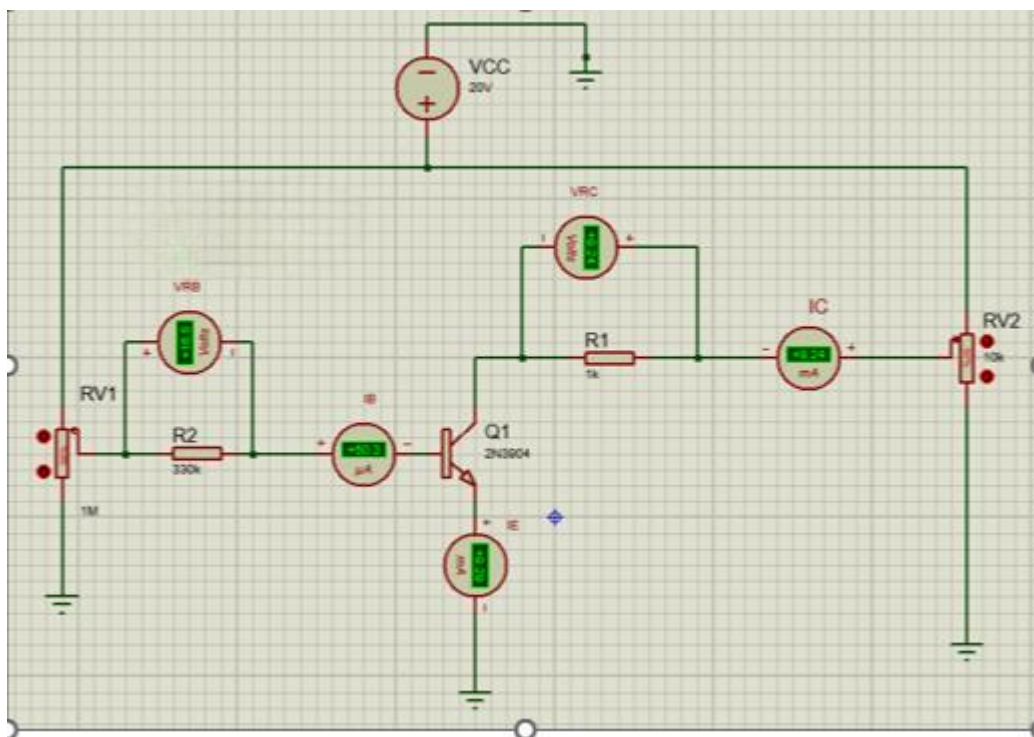


**$E_{RB} = 3.3\text{ V}$ ,  $I_B = 10\text{ micro A}$**



$E_{RB} = 3.3 \text{ V}, I_B = 10 \text{ micro A}$

$E_{RB} = 3.3 \text{ V}, I_B = 10 \text{ micro A}$



## Procedure:

1. Set the VCE to 2V by varying 10k $\Omega$  potentiometer as required by the first line of
2. Record the VRC and VBE in Table .
3. Vary the 10 k $\Omega$  potentiometer to increase VCE from 2V to the values appearing in Table . (Note: IB should be maintained at 10  $\mu$ A for the range of VCE levels.
4. Record VRC and VBE values for each of the measured VCE values. Use the mV range for VBE
5. Repeat step 2 through 5 for all values of IB indicated in Table
6. Compute the values of IC (from  $I_C = V_{RC}/R_C$ ) and IE (from  $I_E = I_B + I_C$ ). Use measured resistor value for RC.
7. Compute the values of  $\alpha$  (from  $\alpha = I_C/I_E$ ) and  $\beta$  (from  $\beta = I_C/I_B$ ). Results: V<sub>RB</sub>(V) I<sub>B</sub>( $\mu$ A) V<sub>CE</sub>(V) V<sub>RC</sub>(V) I<sub>C</sub>(mA) V<sub>BE</sub>(V) I<sub>E</sub>(mA)

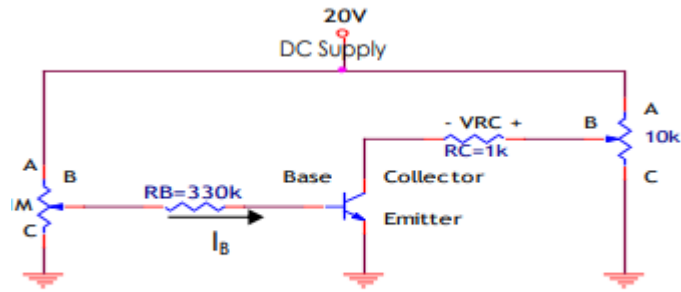


Fig. 1

## Readings:

V <sub>RB</sub>	I <sub>B</sub>	V <sub>RC</sub>	I <sub>C</sub>	V <sub>BE</sub>	I <sub>E</sub>	$\alpha = I_C/I_E$	$\beta = I_C/I_B$
3.3V	10 $\mu$ A	18V	2.98mA	0.7V	2.99mA	0.99	298
6.5V	20 $\mu$ A	5.29V	5.29mA	0.7V	5.31mA	0.99	264.5
9.9V	30 $\mu$ A	7.17V	7.17mA	0.7V	7.20mA	0.995	239
13.2V	40 $\mu$ A	8.64V	8.64mA	0.7V	8.68mA	0.995	216
16.5	50 $\mu$ A	9.24V	9.24mA	0.7V	9.29mA	0.994	184