

CHARITHA SAMIREDDY

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EDUCATION

Rajiv Gandhi University of Knowledge Technologies ,Nuzvid
Bachlelor of technology

August 2022 - Present
CGPA Till Now:8.02

Department electronics and communication Engineering

Rajiv Gandhi University of Knowledge Technologies ,Nuzvid
PreUniversity course.

Jan 2020 - July 2022
Overall CGPA: 9.95

Deepti english medium ,Vizaynagaram
SSC 10th

June 2019 - April 2020
Overall CGPA: 10

CARRIER OBJECTIVE

As a hardworking and dedicated student eager to apply my foundational knowledge, learn new technical skills and contribute to a dynamic team while gaining valuable real-world experience. Committed to leveraging this opportunity to further develop technical expertise.

PROJECTS

I2C protocol

Designed and implemented communication between microcontroller and multiple sensors using I2C protocol.Optimized data transfer for real-time processing, improving system efficiency

Design and implementation of an gcd calculator using verilog

In this project, I designed and implemented a hardware-efficient HCF (Highest Common Factor) calculator using Verilog. The system takes two integers as inputs and computes their HCF using the Euclidean algorithm. The design is modular, consisting of input, computation, and output blocks. Simulation and verification were carried out using a testbench, ensuring the accuracy and reliability of the design. This project highlights the practical application of digital design techniques and demonstrates proficiency in Verilog HDL for arithmetic computation.

Human following robot

In this project I built a robot that tracks and follows humans using ultrasonic sensors and Arduino. It is programmed real-time distance tracking and obstacle avoidance. It has Integrated motor drivers for smooth navigation in dynamic environments.

Home automation using arduino

In this project,I developed a home automation system to control appliances remotely using Arduino.I use sensors, like relays, and wireless communication modules (e.g., Bluetooth/Wi-Fi).It has Programmed features for real-time monitoring and control of devices.

TECHNICAL STRENGTHS

Programing languages	C , Arduino UNO
System design	Multisim , Xilinx Vivado
Hardware Languages	Verilog , System Verilog , Assembly language(8086)

WORK EXPERIENCE

PROJECT STAFF(INTERN),IIT GUWAHATI

Dec 2024-Jan 29 2025

Completed an on-site winter internship under Prof. Chandan Karfa at IIT Guwahati. My work focuses on utilizing System Verilog for hardware design and verification. I am enthusiastic about contributing to research projects in digital circuit design.

ACADEMIC ACHIEVEMENTS

Ranked in the top 1 with an 91% in the "**Digital Design with Verilog**" course.

Completed **COMPUTER ARCHITECTURE** Course provided by NPTEL

Participated in the project expo at IOT grandexpo conducted by RGUKT, Nuzvid, in the year 2023

Selected as Project STAFF(Winter intern)at IIT Guwahati,contributing to Advanced Reasearch in System Verilog.

PERSONAL TRAITS

Committed, responsible, and dedicated to delivering quality work.

Strong verbal and written communication abilities.

Eager to learn and explore new concepts

Ability to adjust to new challenges and environments quickly.

DECLARATION:

I, Samireddy Charitha ,hereby declare that the details furnished above are true and accurately reflects my qualifications and experience.

S.Charitha
Signature