Date:23.04.18

Student Names:Hatice Melike Ecevit- Merve Ilik
Student IDs:2016400138-2015400006
Group ID:25
Session ID:1

CMPE 240 Experiment 5 Preliminary Work

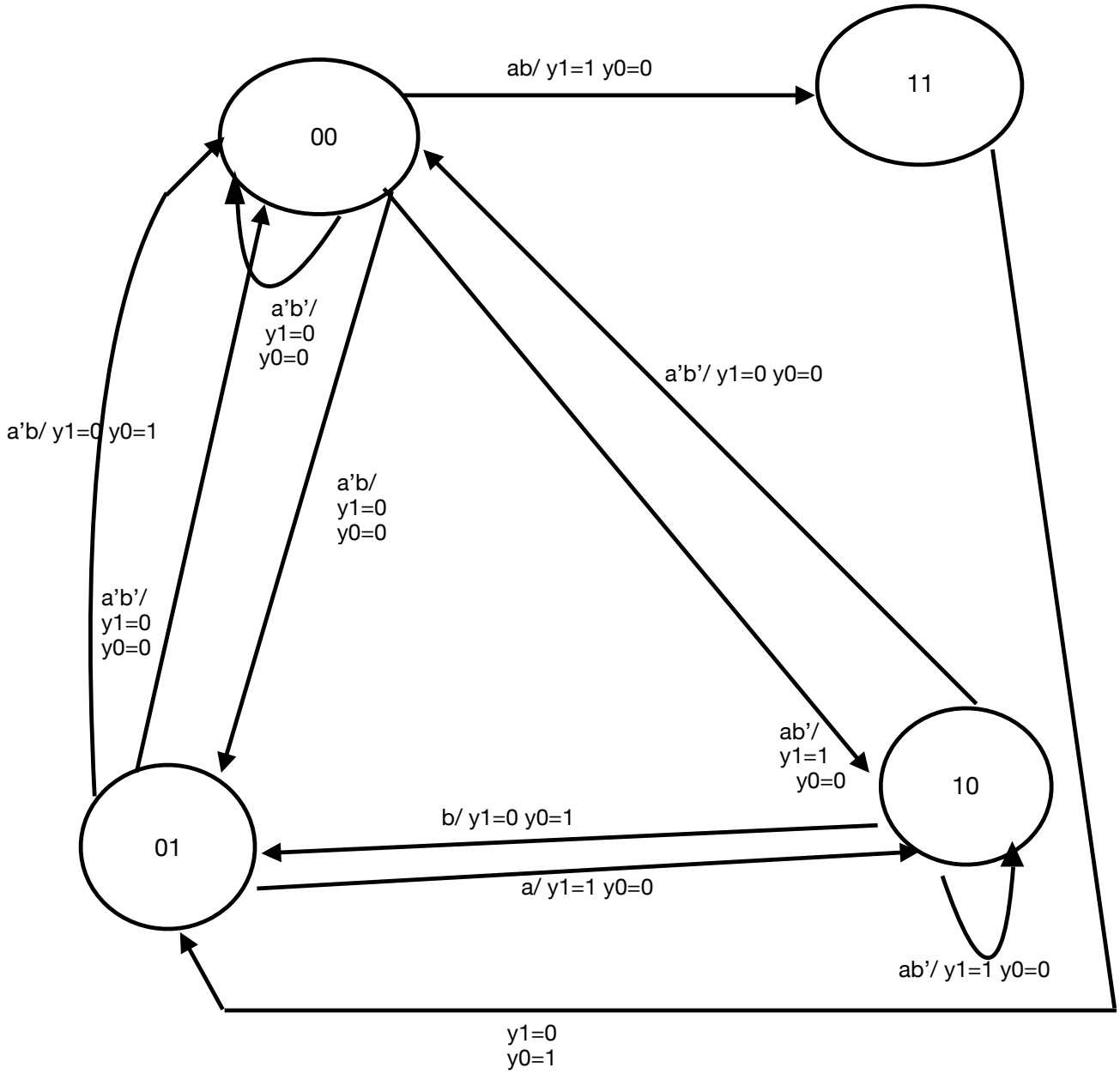
1. State Register Inputs: n1, n0
2. State Register Outputs: s1, s0
3. Combinational Block Inputs: a, b, s1, s0
4. Combinational Block Outputs: y1, y0, n1, n0
5. Obtain the truth table.

s1	s0	a	b	n1	n0	y1	y0
0	0	0	0	0	0	0	0
0	0	0	1	0	1	0	1
0	0	1	0	1	0	1	0
0	0	1	1	1	1	1	0
0	1	0	0	0	0	0	0
0	1	0	1	0	0	0	1
0	1	1	0	1	0	1	0
0	1	1	1	1	0	1	0
1	0	0	0	0	0	0	0
1	0	0	1	0	1	0	1
1	0	1	0	1	0	1	0
1	0	1	1	0	1	0	1
1	1	0	0	0	1	0	1
1	1	0	1	0	1	0	1
1	1	1	0	0	1	0	1
1	1	1	1	0	1	0	1

Date:23.04.18

Student Names:Hatice Melike Ecevit- Merve Ilik
Student IDs:2016400138-2015400006
Group ID:25
Session ID:1

6. Is this a Moore or Mealy Machine? (No explnation, only short answer) Mealy.
7. Draw the finite state machine.



Date:23.04.18

Student Names:Hatice Melike Ecevit- Merve Ilik
Student IDs:2016400138-2015400006
Group ID:25
Session ID:1