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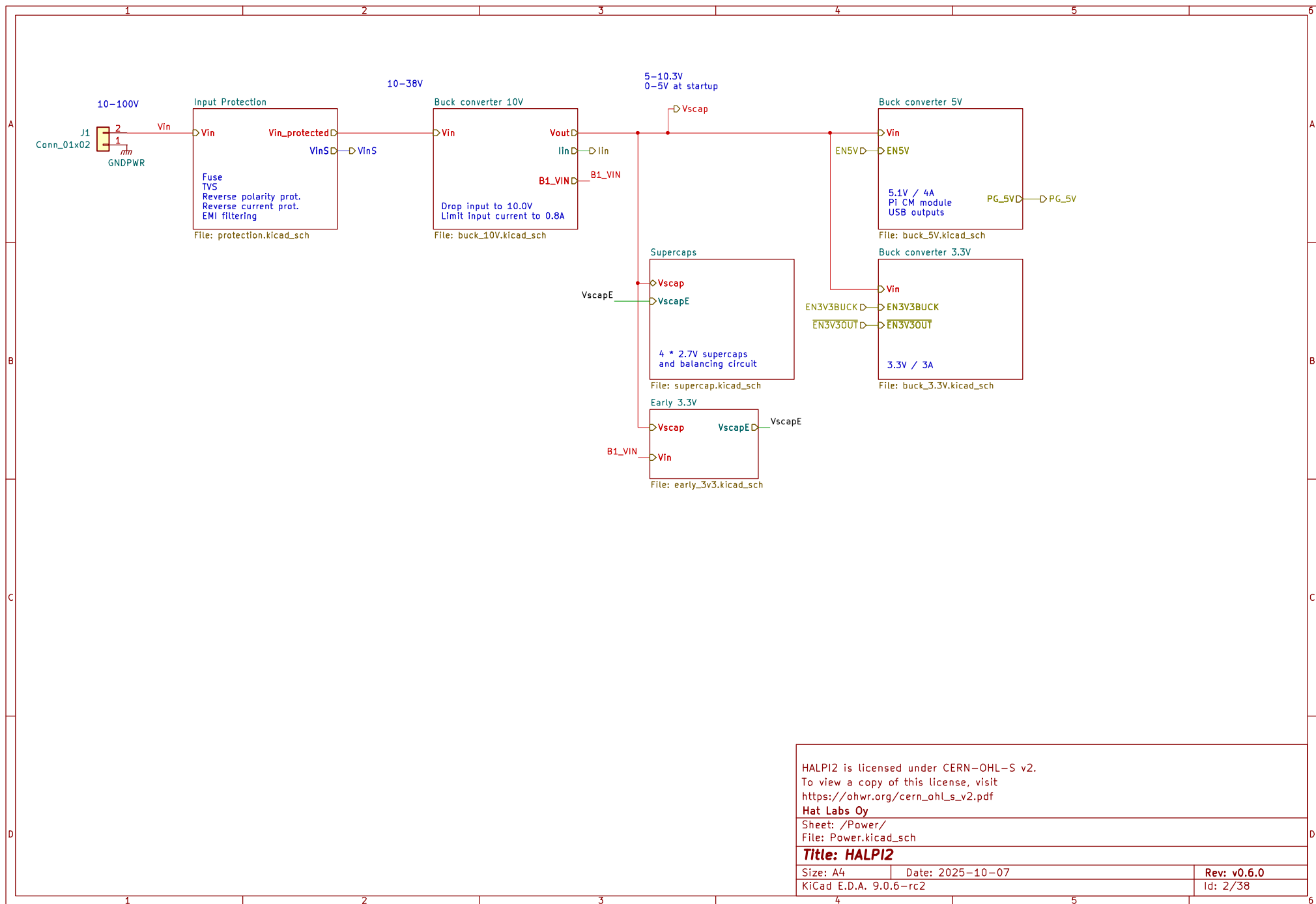
Hat Labs Oy

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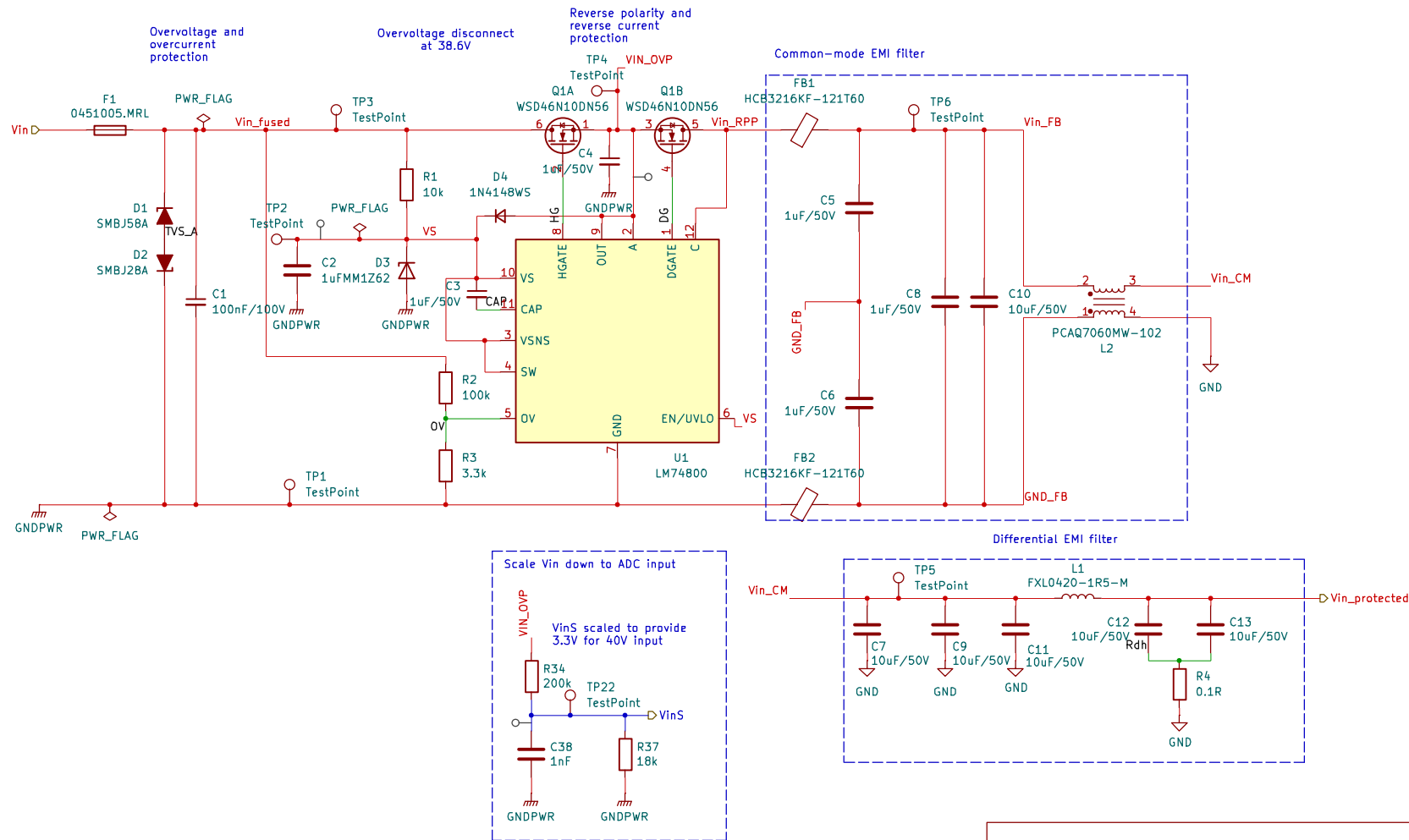
Title: HALPI2

Size: A4 Date: 2025-10-07
KiCad E.D.A. 9.0.6-rc2

Rev: v0.6.0
Id: 1/38



EMI pre-filter design taken from PMP15013 & 62AN101E.



Overvoltage limit 38.6V

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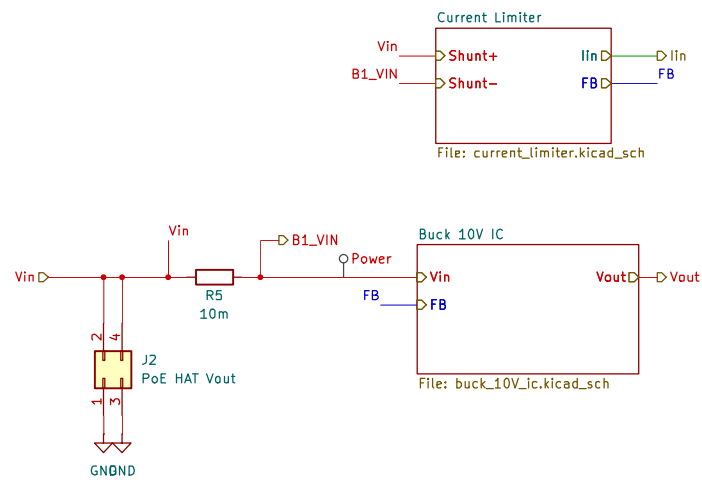
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Size: A4 Date: 2025-10-07
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Id: 3/38



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Size: A4

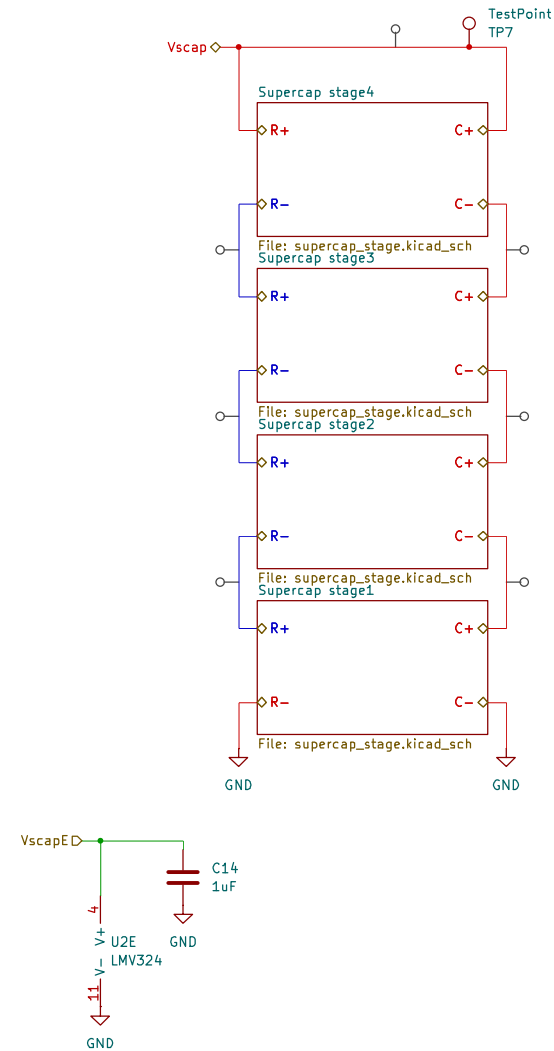
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Supercapacitor balancing circuit modeled after
<https://www.eevblog.com/forum/renewable-energy/supercapacitor-balancer/>



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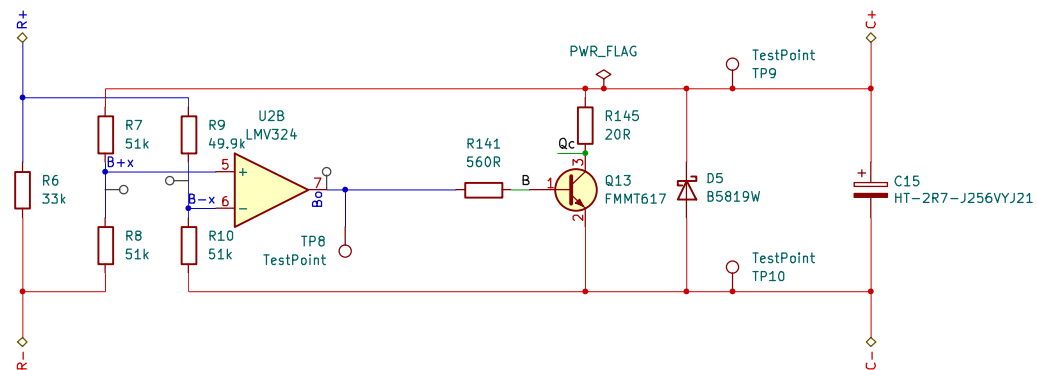
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Size: A4 Date: 2025-10-07
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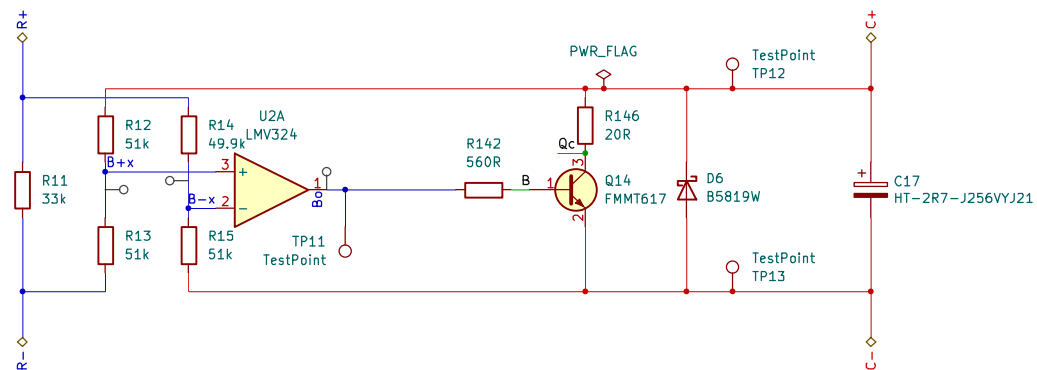
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Title: HALPI2

Size: A4 Date: 2025-10-07
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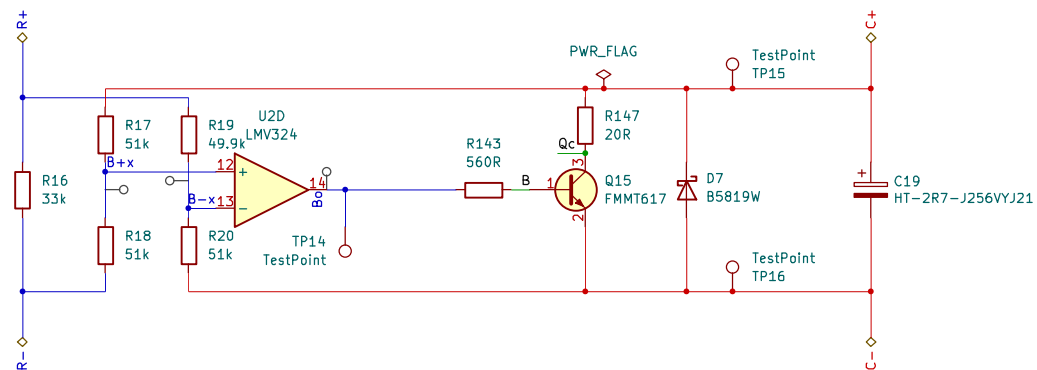
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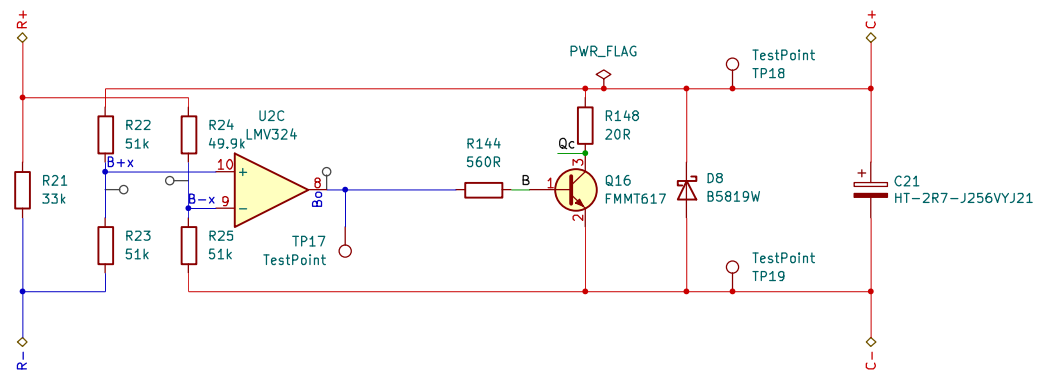
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Size: A4 Date: 2025-10-07
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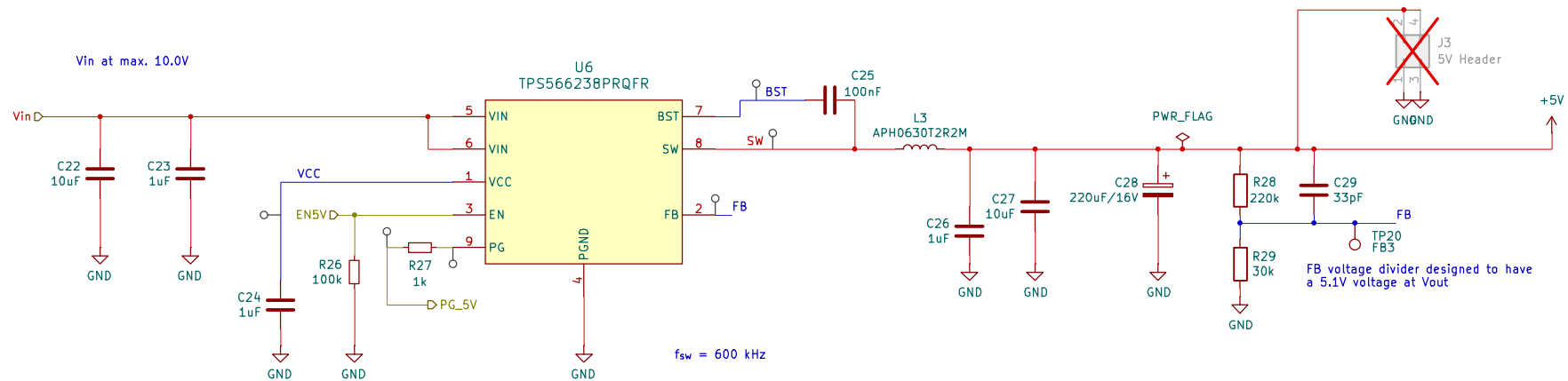
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Size: A4 Date: 2025-10-07
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Buck converter dropping the supercap voltage
down to 5V for the CM4/5 module input and
USBs



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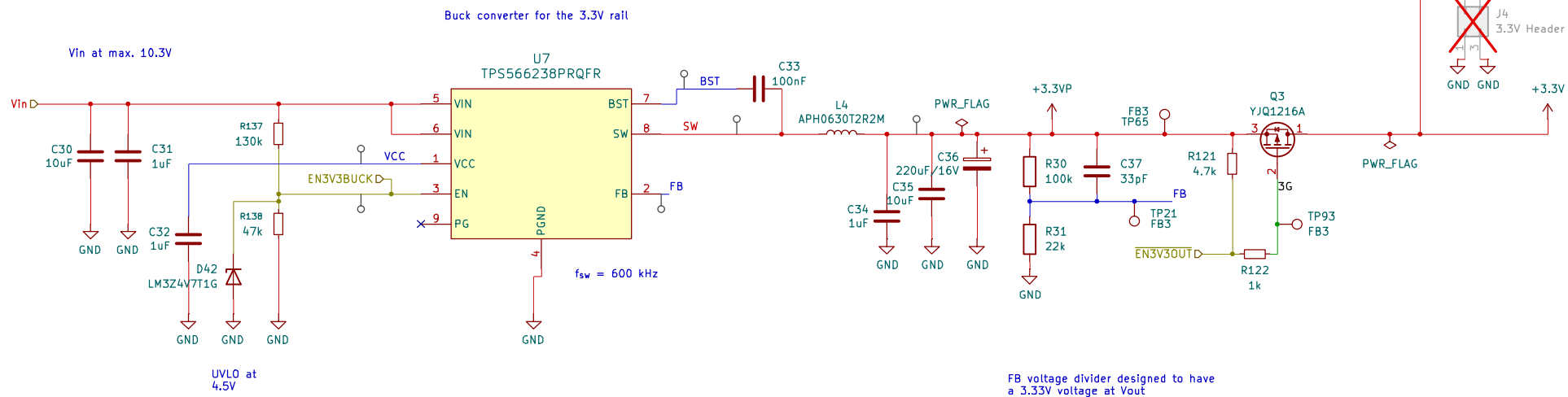
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Size: A4 Date: 2025-10-07
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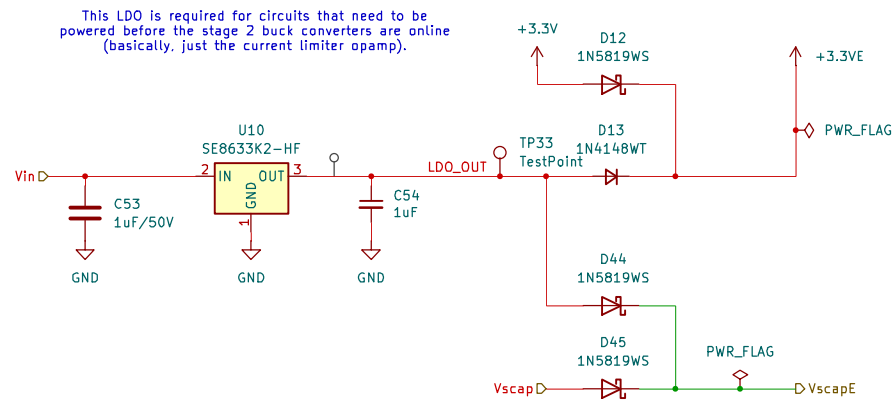
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Title: HALPI2

Size: A4 Date: 2025-10-07
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VscapE is used to ensure the supercap balancing circuit op-amps are powered immediately.

During undervoltage, TTL op-amps may be at a high state, causing all balancing NPNs to conduct before voltage is increased.

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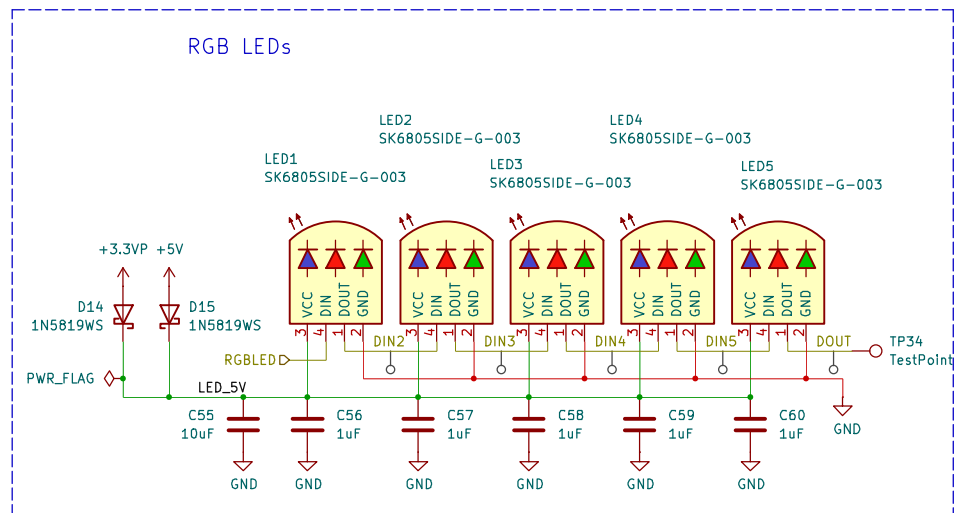
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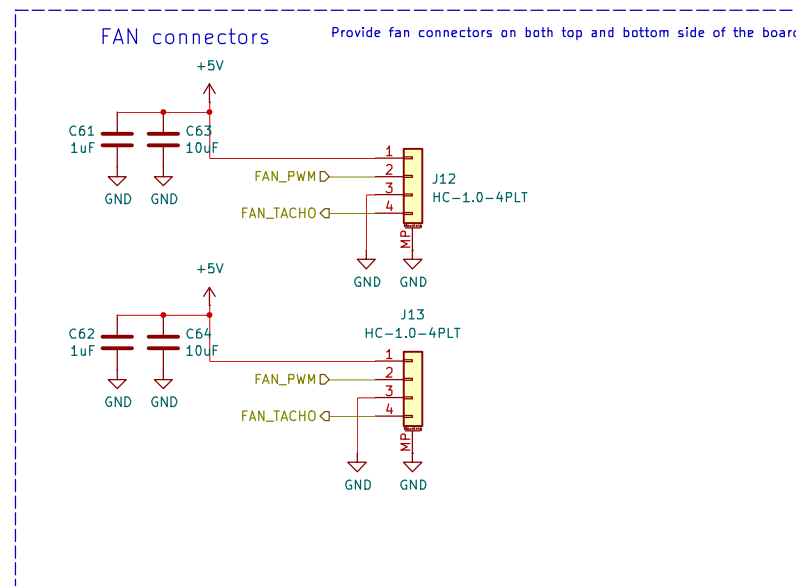
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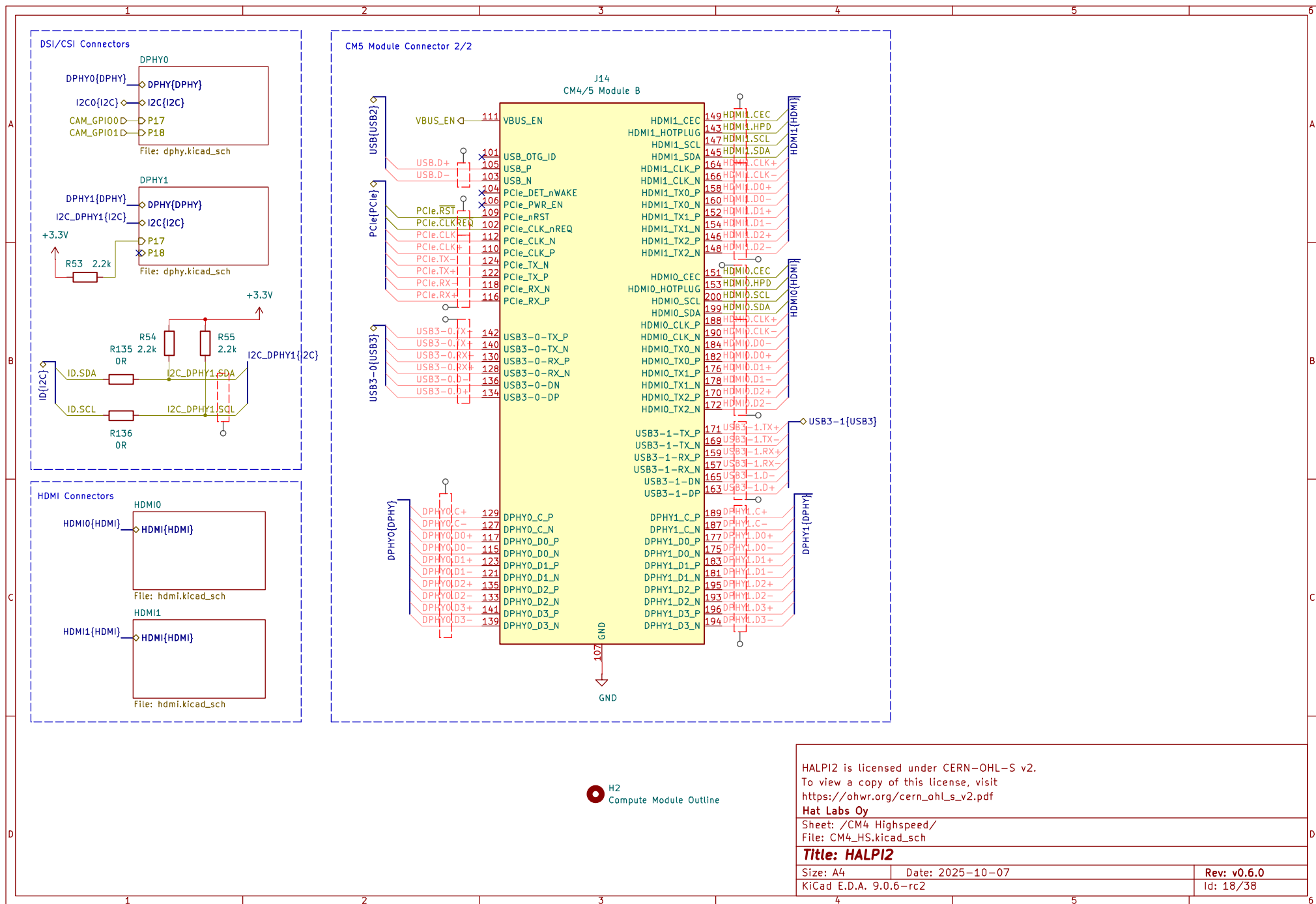
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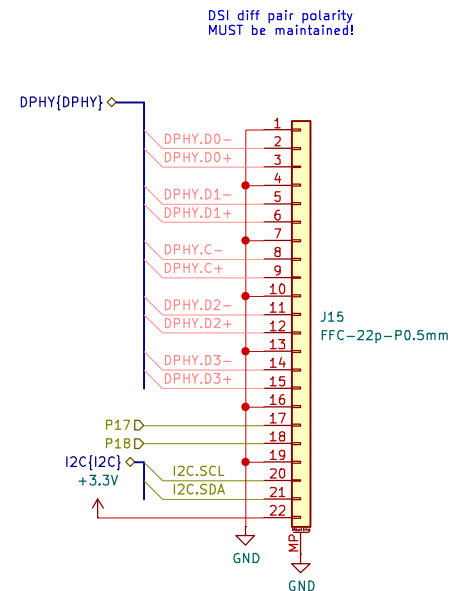
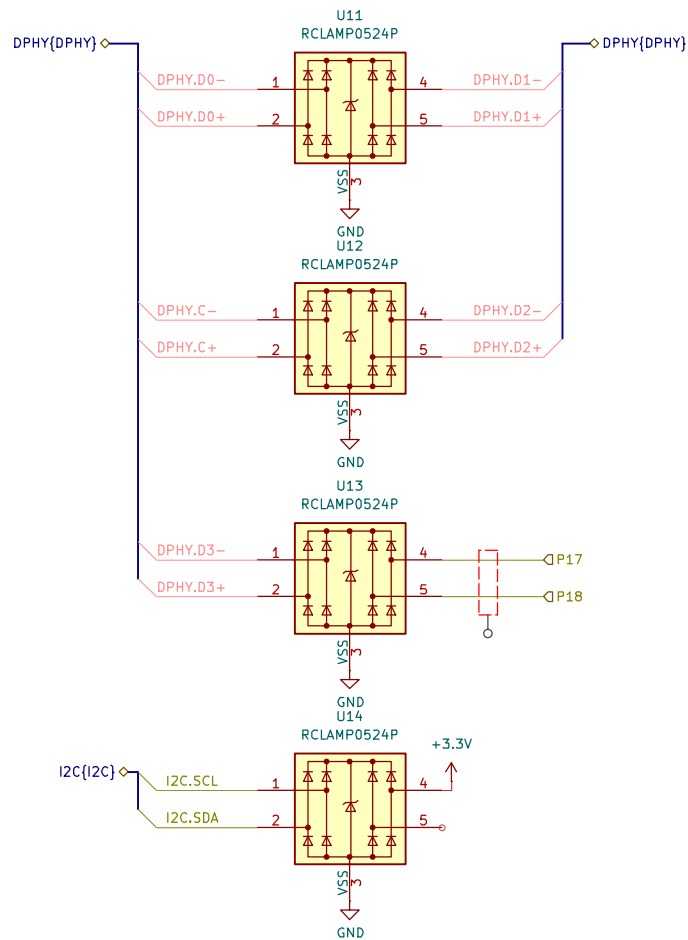
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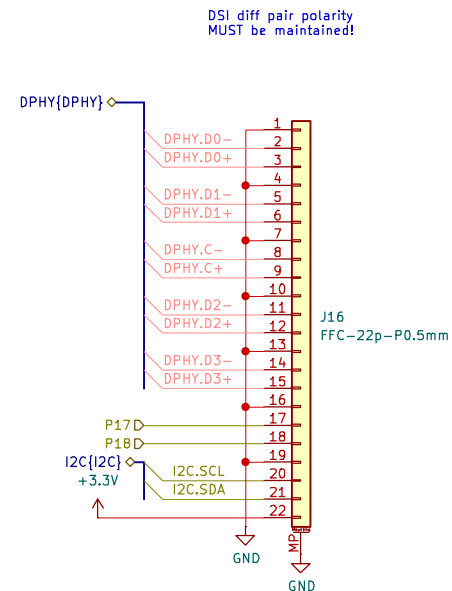
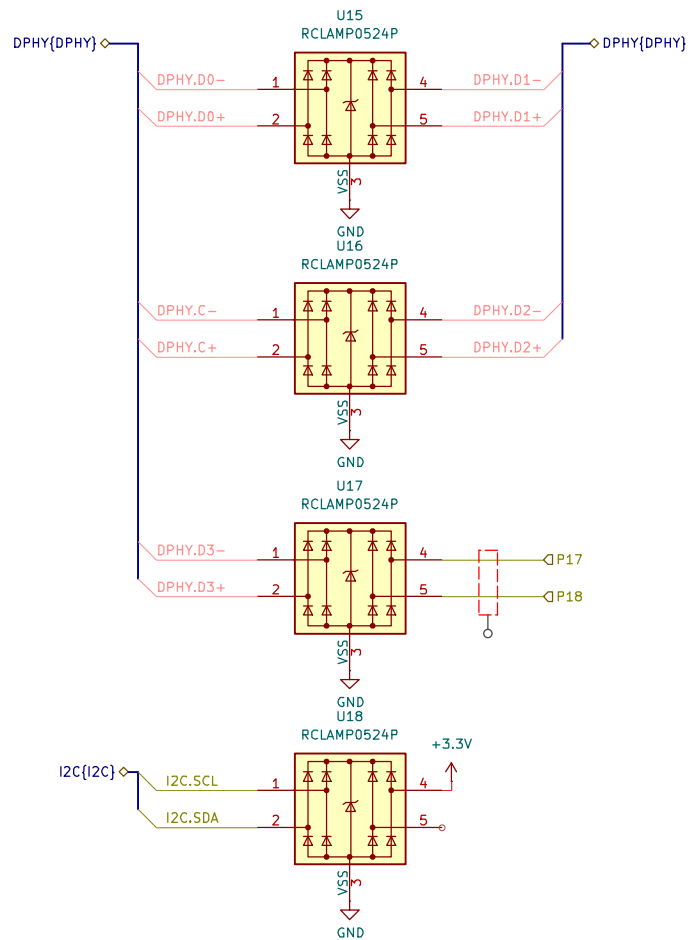
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Rev: v0.6.0

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Sheet: /CM4 Highspeed/DPHY1/

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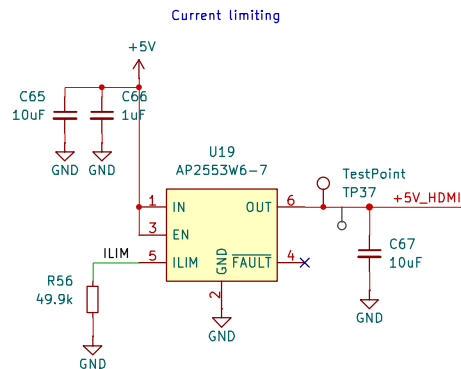
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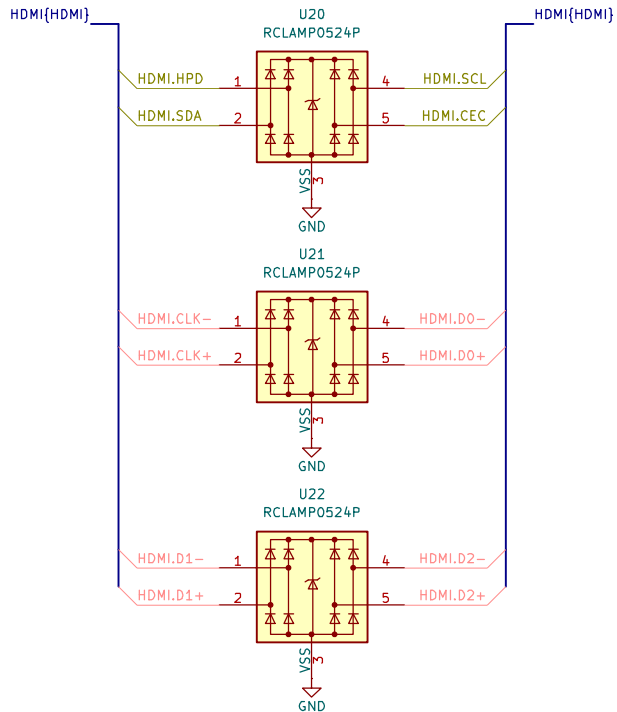
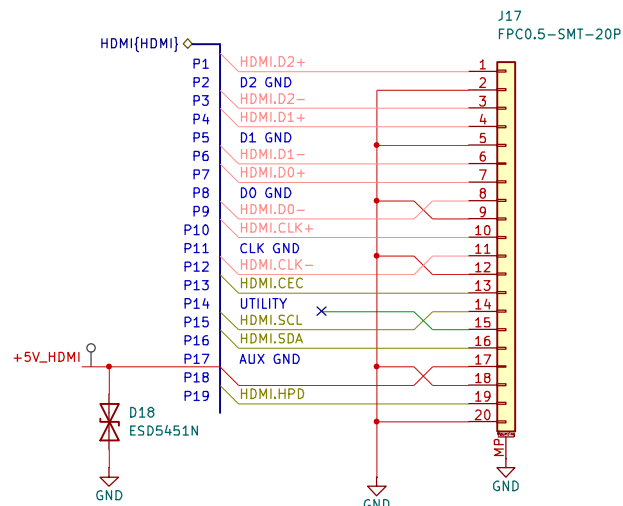
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KiCad E.D.A. 9.0.6-rc2

Id: 20/38



HDMI diff pair polarity
MUST be maintained.



Circuit Diagram / 电路图

C1	C2	C1	C2
1	1	11	12
2	2	12	11
3	3	13	13
4	4	14	15
5	5	15	14
6	6	16	16
7	7	17	18
8	8	18	17
9	8	19	19
10	10	Shell	20

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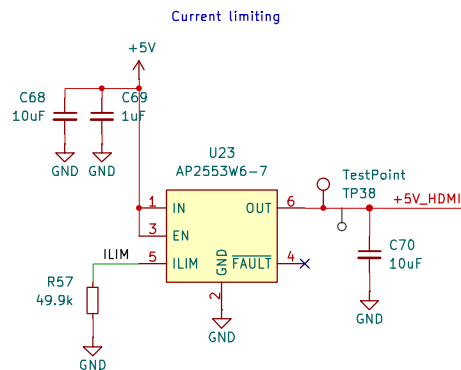
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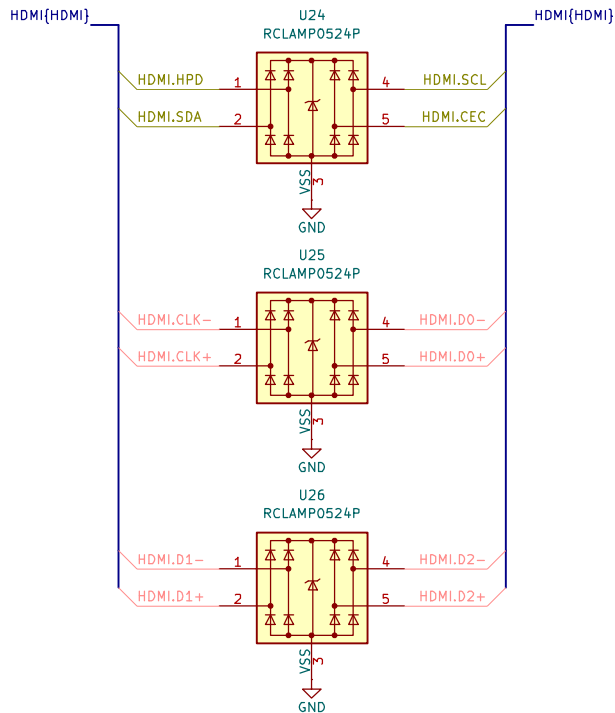
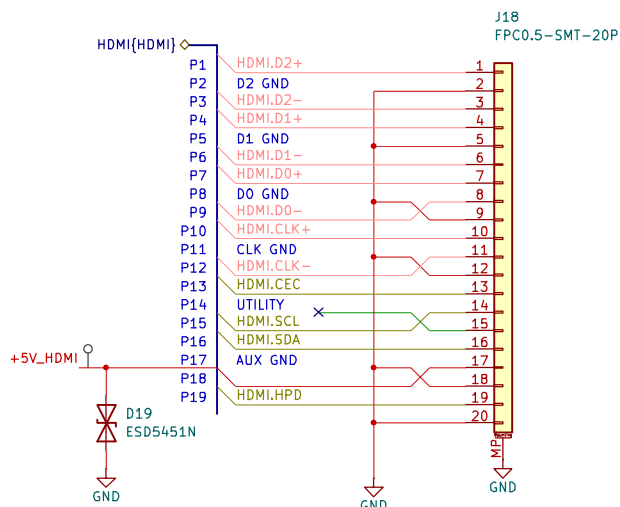
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Rev: v0.6.0
Id: 21/38



HDMI diff pair polarity
MUST be maintained.



Circuit Diagram / 电路图

C1	C2	C1	C2
1	1	11	12
2	2	12	11
3	3	13	13
4	4	14	15
5	5	15	14
6	6	16	16
7	7	17	18
8	8	18	17
9	9	19	19
10	10	Shell	20

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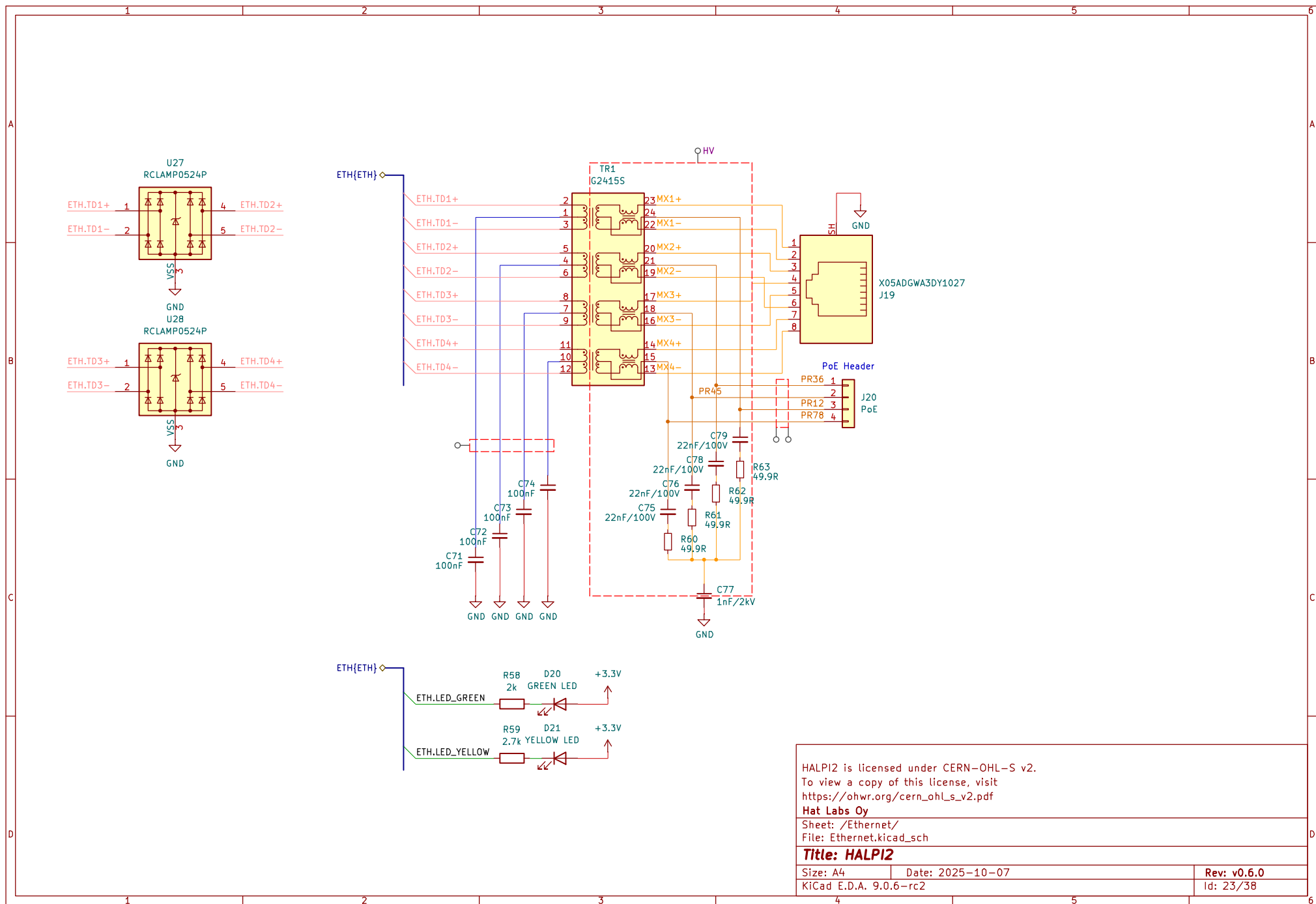
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Sheet: /CM4 Highspeed/HDMI1/
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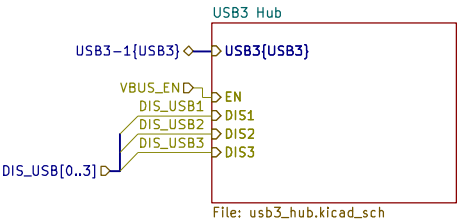
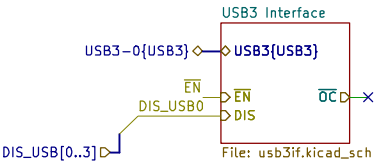
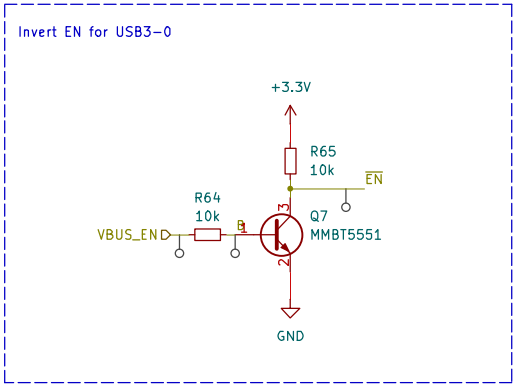
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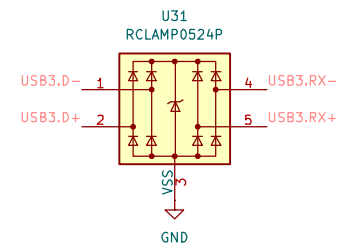
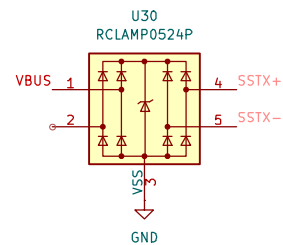
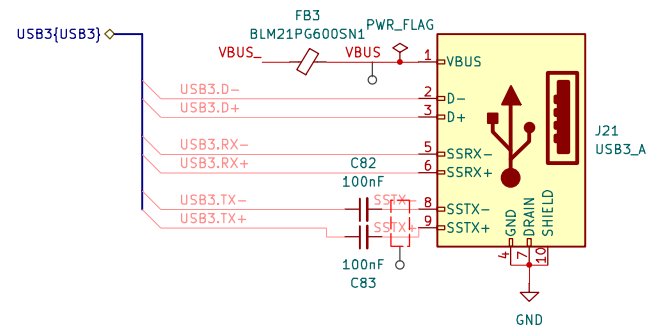
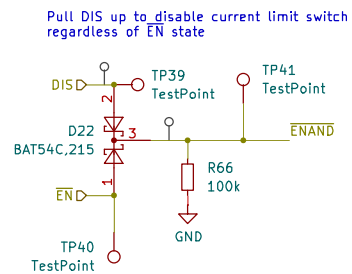
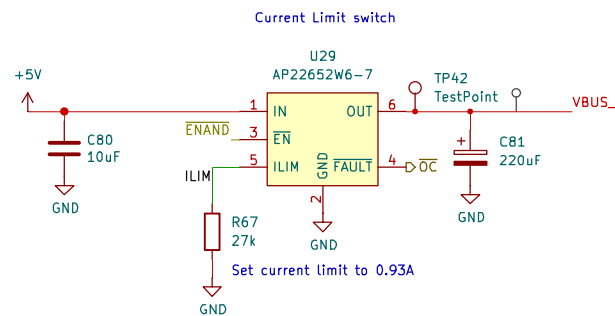
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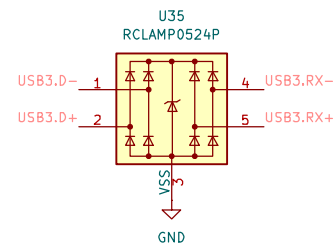
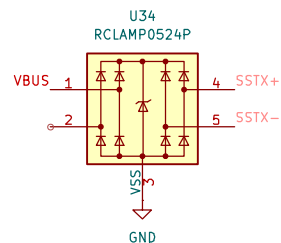
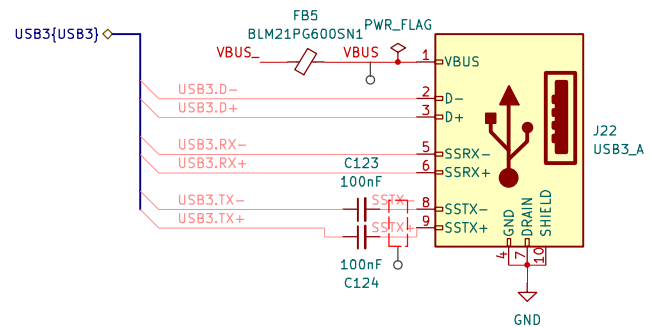
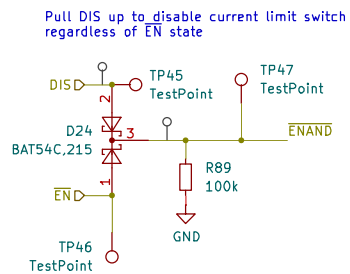
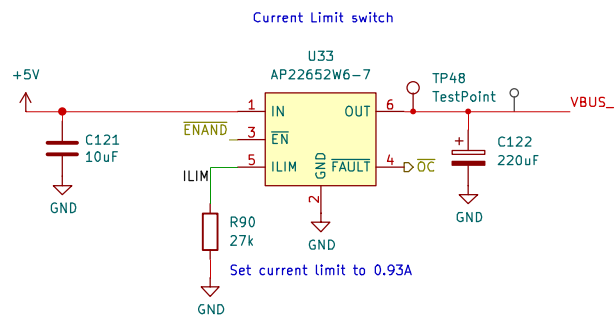
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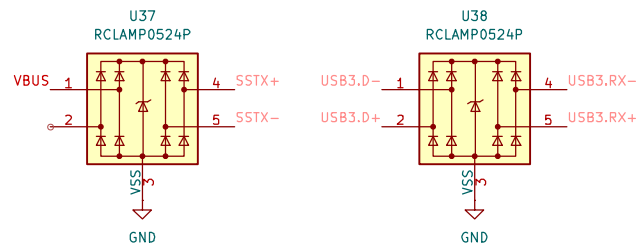
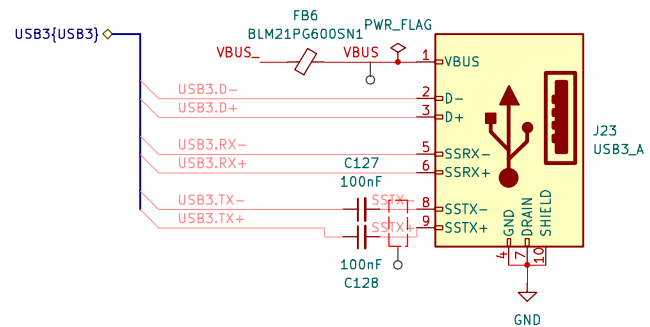
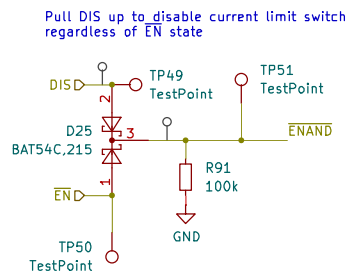
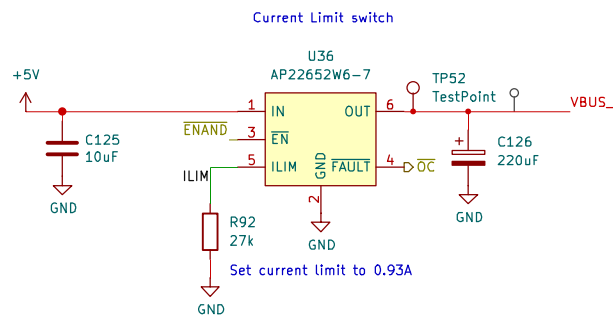
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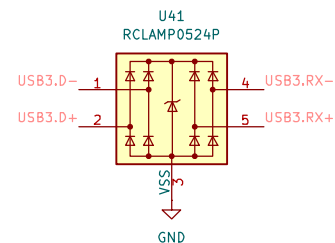
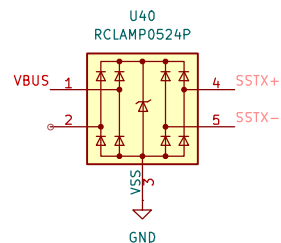
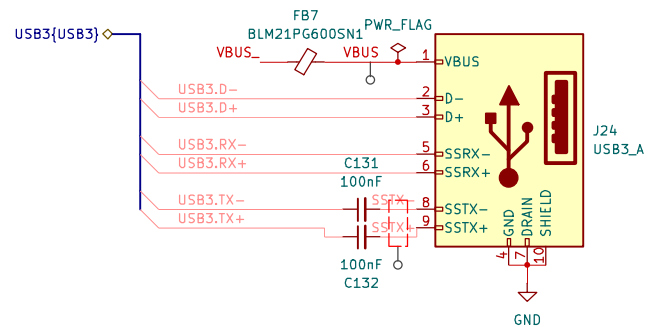
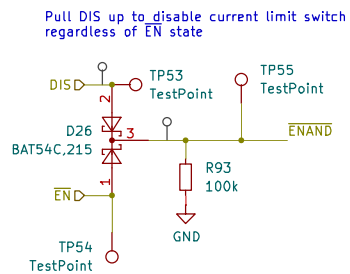
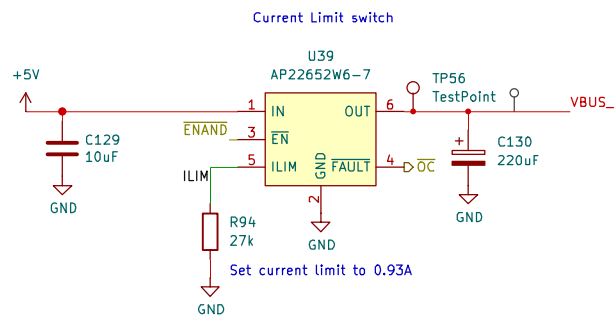
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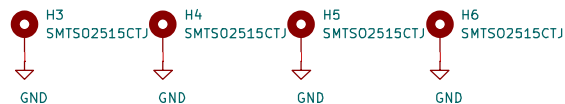
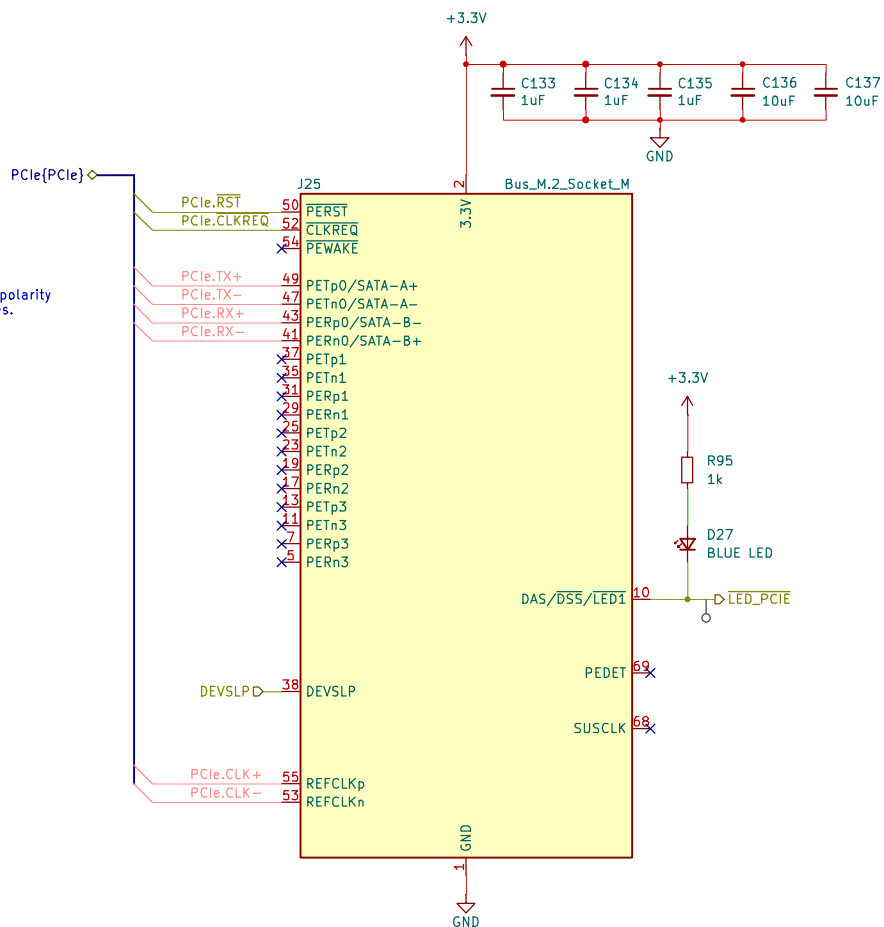
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PCIe allows diff pair polarity reversal for data lanes.



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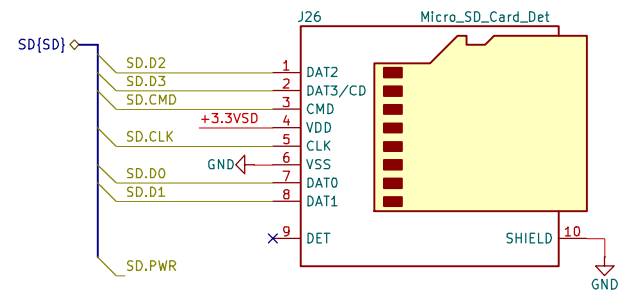
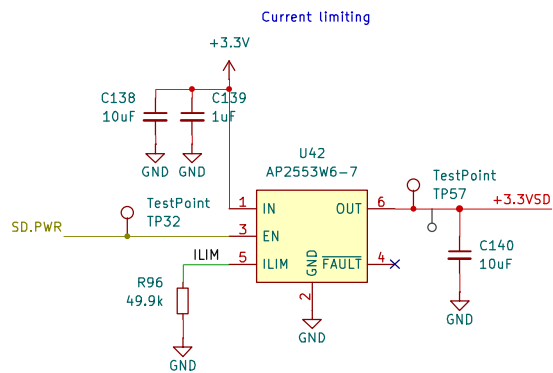
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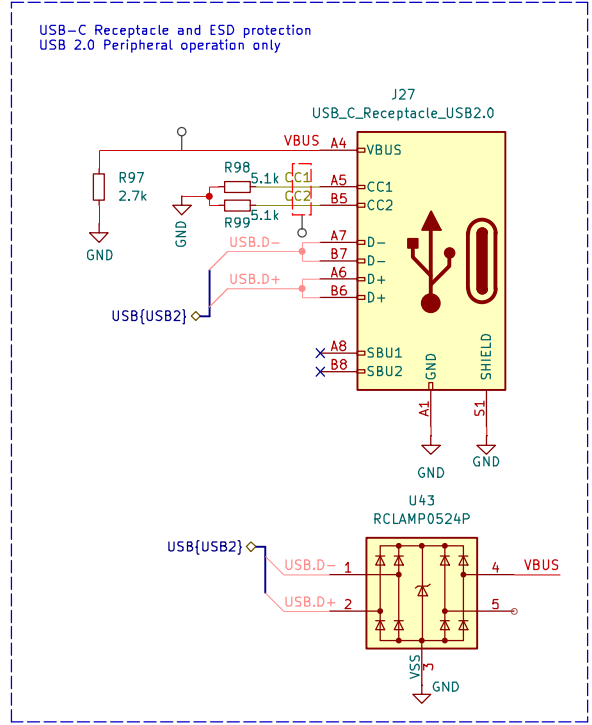
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Rev: v0.6.0

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Id: 31/38



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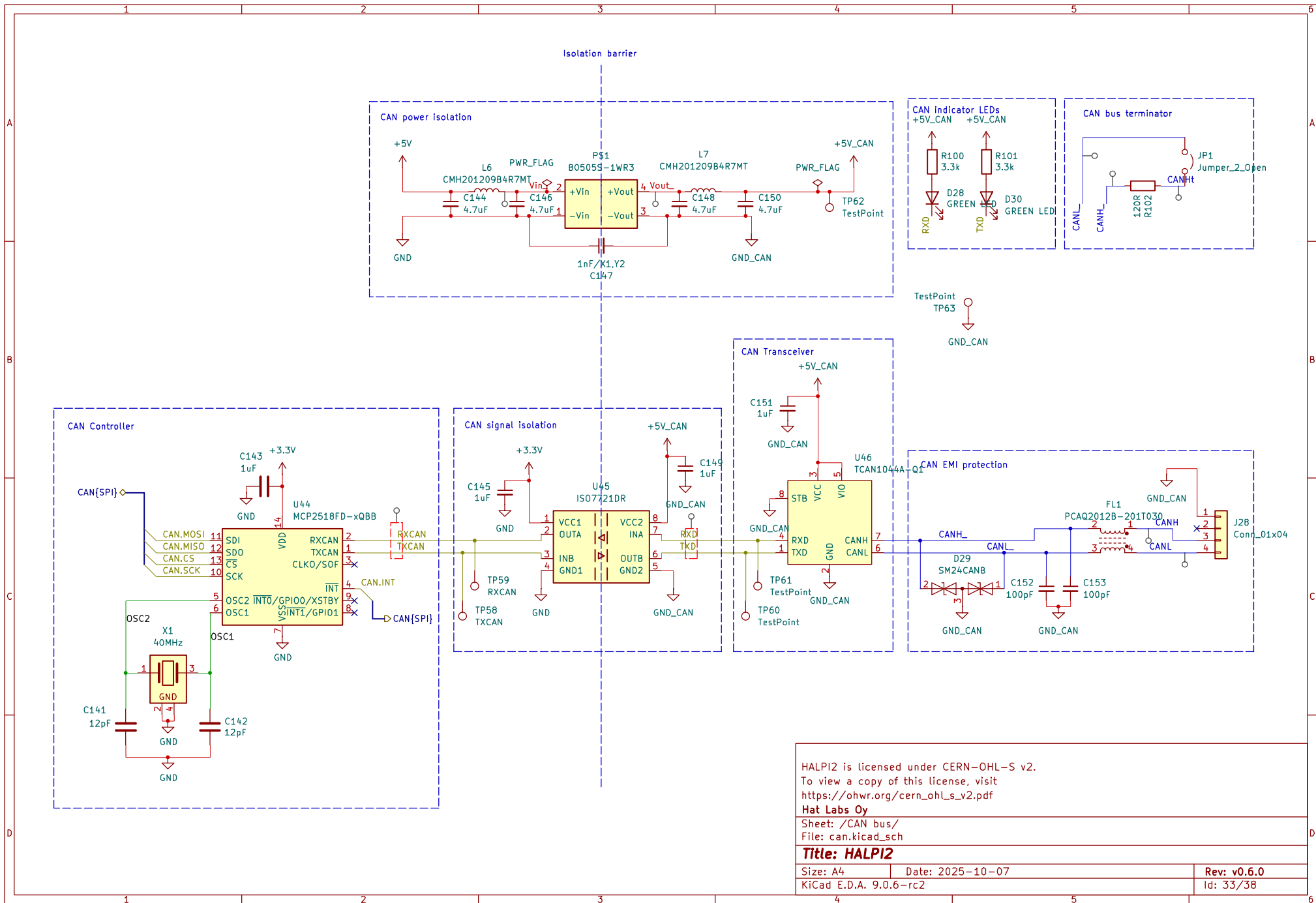
Hat Labs Oy

Sheet: /USB 2.0 Peripheral Interface/
File: USB2.kicad_sch

Title: HALPI2

Size: A4 Date: 2025-10-07
KiCad E.D.A. 9.0.6-rc2

Rev: v0.6.0
Id: 32/38



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Sheet: /CAN bus/

File: can.kicad_sch

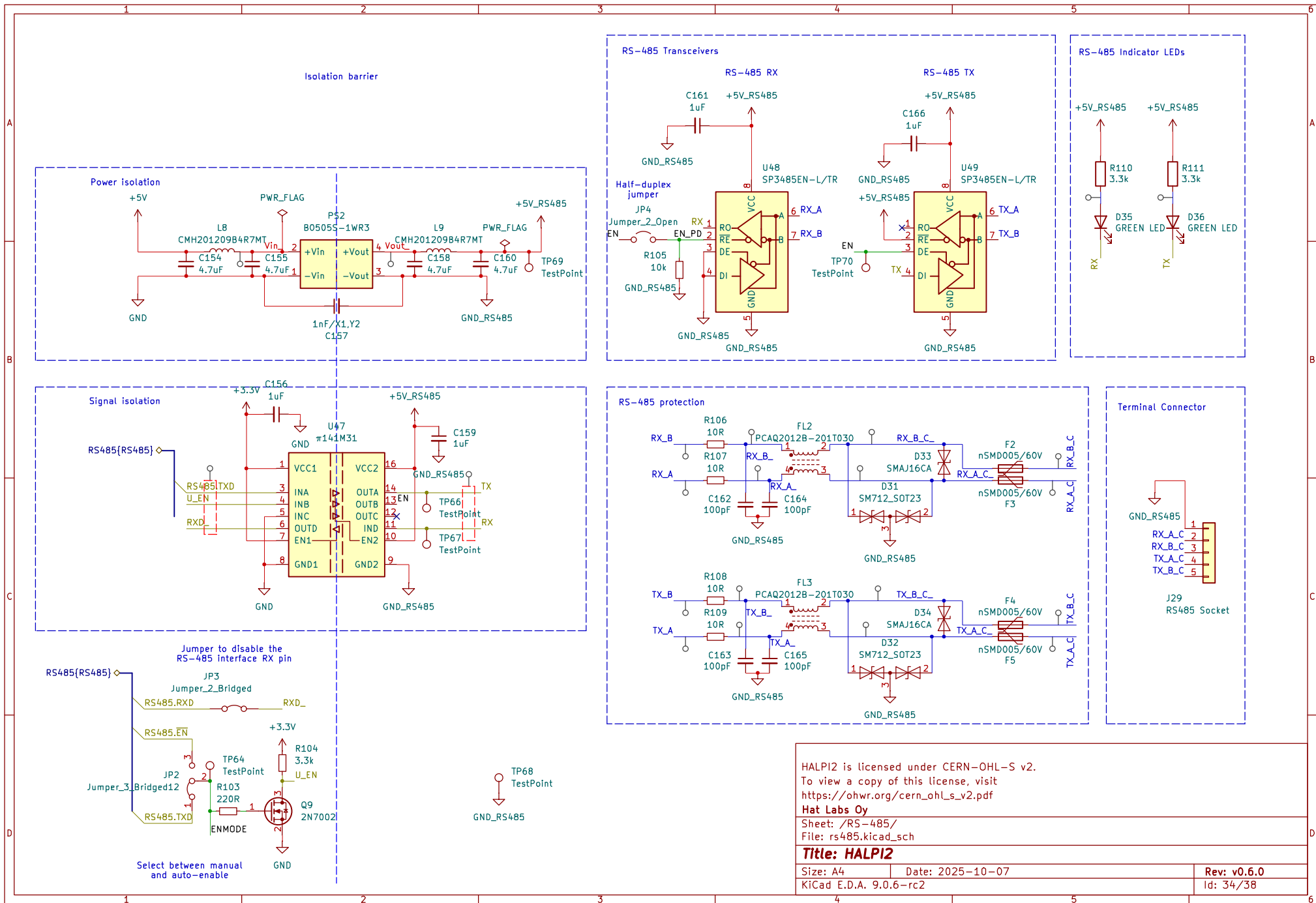
Title: HALPI2

Size: A4 Date: 2025-10-07

KiCad E.D.A. 9.0.6-rc2

Rev: v0.6.0

Id: 33/38



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Sheet: /RS-485/

File: rs485.kicad_sch

Title: HALPI2

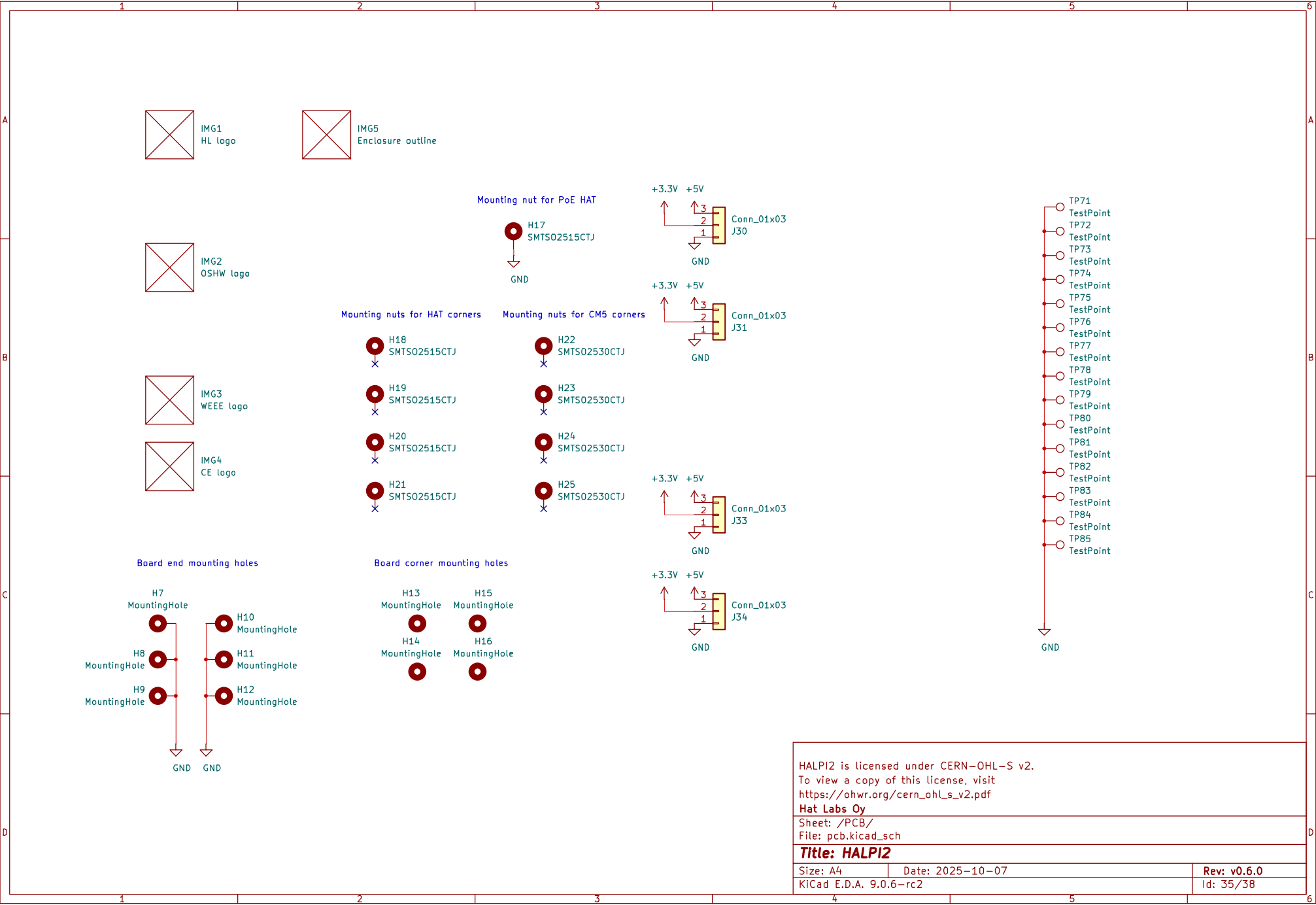
Size: A4

Date: 2025-10-07

Rev: v0.6.0

KiCad E.D.A. 9.0.6-rc2

Id: 34/38

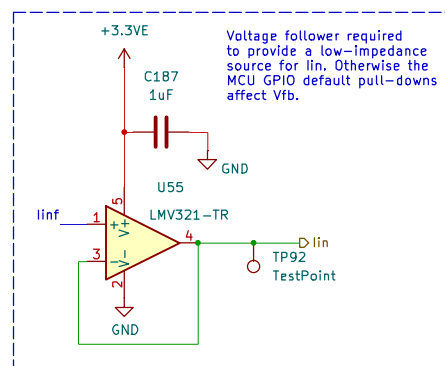
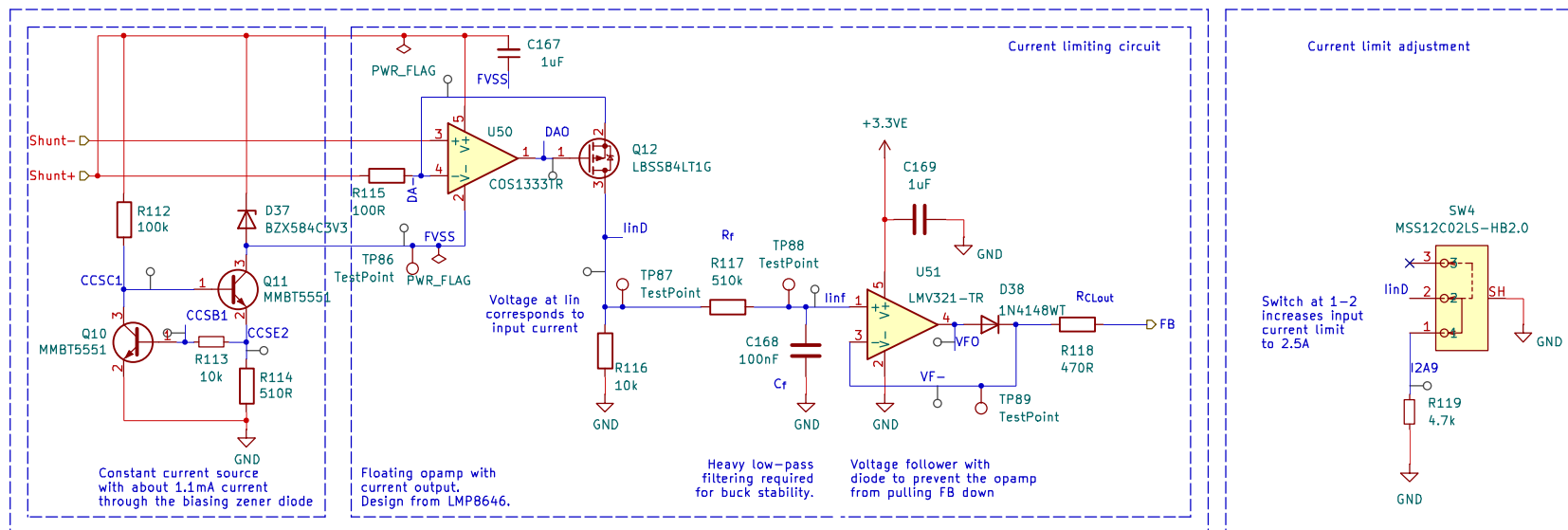


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Sheet: /PCB/
File: pcb.kicad_sch

Title: HALPI2

Size: A4	Date: 2025-10-07	Rev: v0.6.0
KiCad E.D.A. 9.0.6-rc2	Id: 35/38	



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Sheet: /Power/Buck converter 10V/Current Limiter/

File: current_limiter.kicad_sch

Title: HALPI2

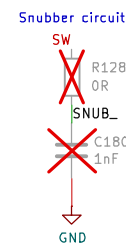
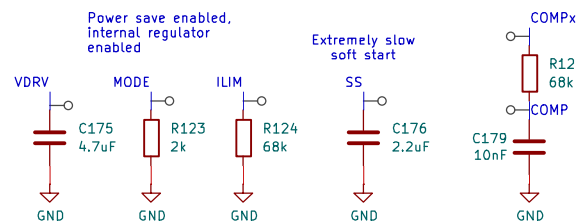
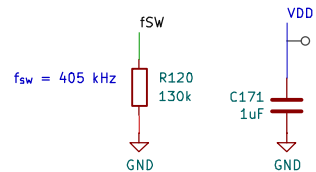
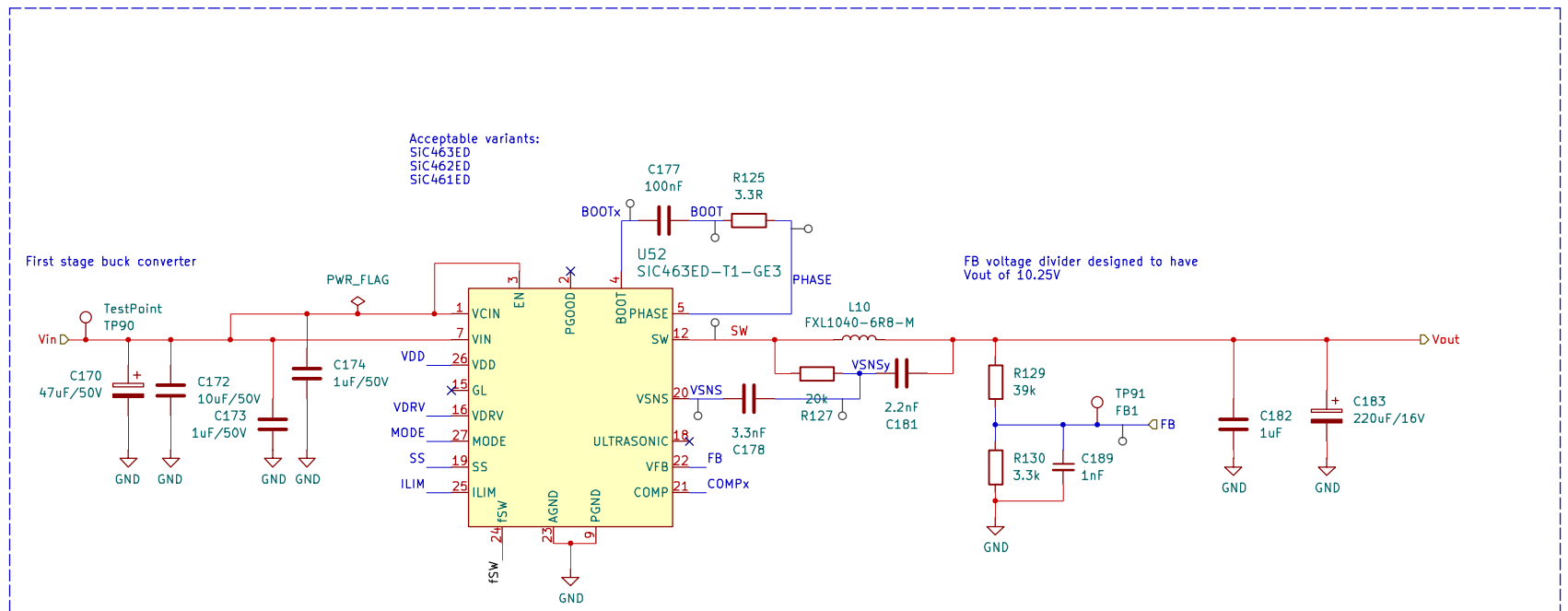
Size: A4

Date: 2025-10-07

Rev: v0.6.0

KiCad E.D.A. 9.0.6-rc2

Id: 36/38



NOTE: If the converter is configured
 with a different SIC46x variant,
 ILIM must be adjusted as well
 (even for the same output current value).

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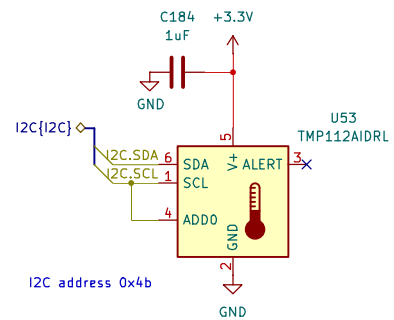
Hat Labs Oy

Sheet: /Power/Buck converter 10V/Buck 10V IC/
 File: buck_10V_ic.kicad_sch

Title: HALPI2

Size: A4 Date: 2025-10-07
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Sheet: /Controller/Temperature Sensor/
File: temp_sensor.kicad_sch

Title: HALPI2

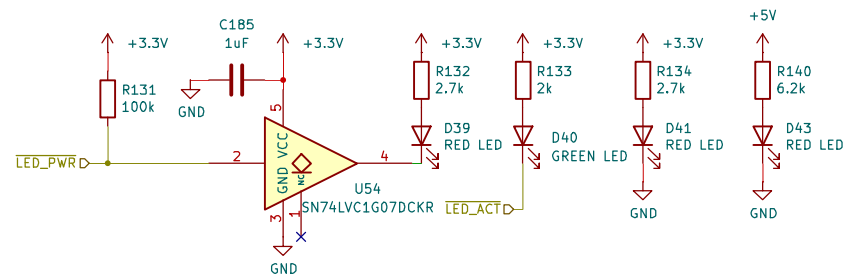
Size: A4

Date: 2025-10-07

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Sheet: /Controller/MCU LEDs/
File: mcu_leds.kicad_sch

Title: HALPI2

Size: A4 Date: 2025-10-07
KiCad E.D.A. 9.0.6-rc2

Rev: v0.6.0
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