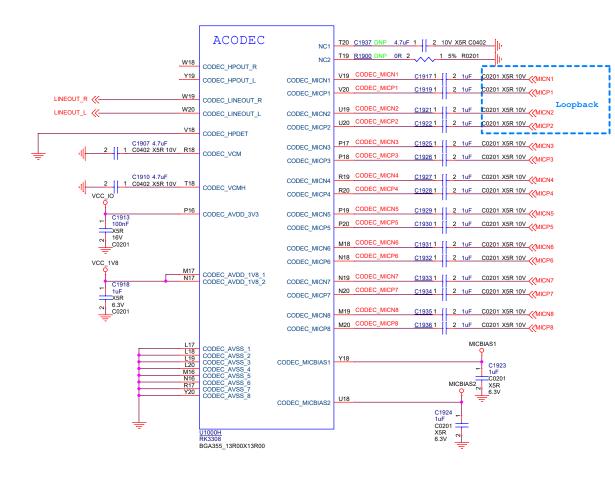
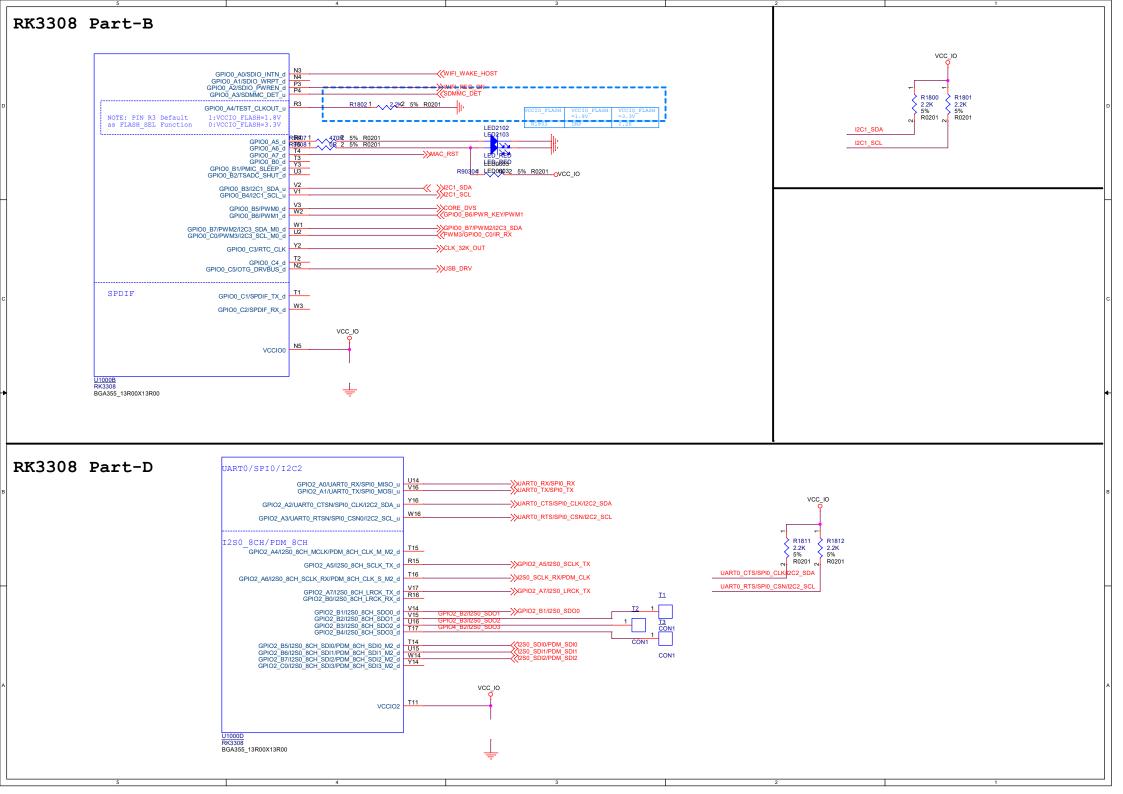
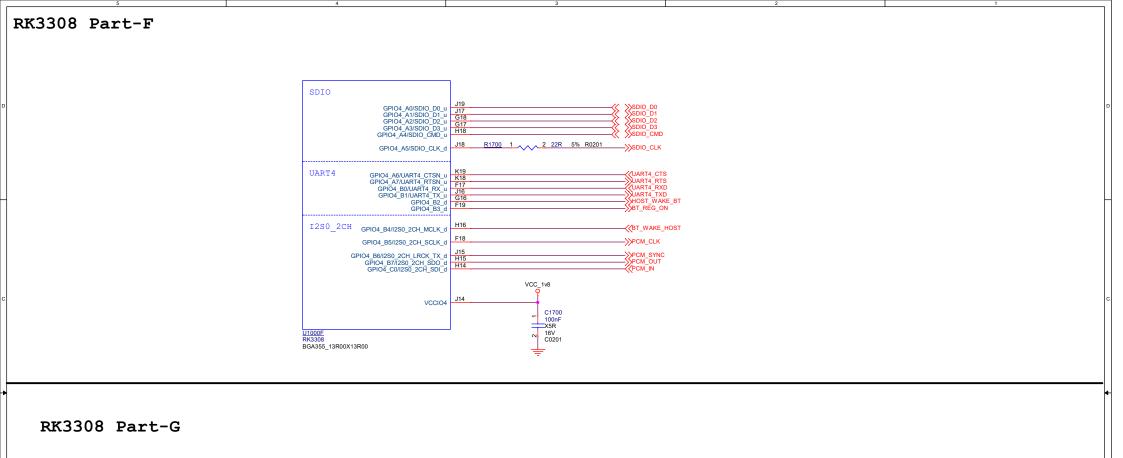


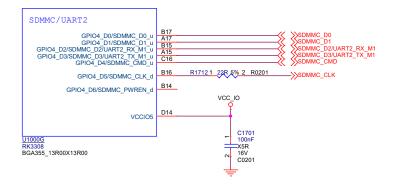
RK3308 Part-H



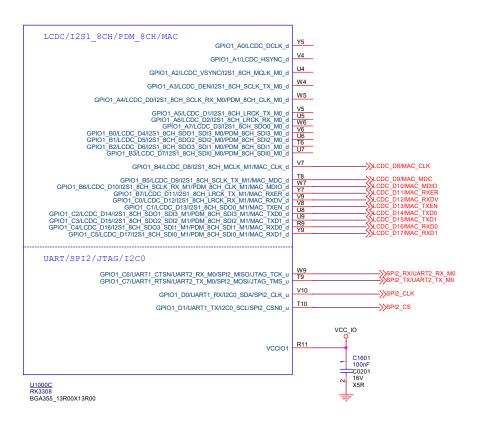
Application scene	MIC IN Channel	Loopback Channel
6MIC IN+ 2Speaker OUT	MIC3~MIC8	
6MIC IN+ 1Speaker OUT	MIC3~MIC8	
5MIC IN+ 2Speaker OUT	MIC4~MIC8	
5MIC IN+ 1Speaker OUT	MIC4~MIC8	MIC1~MIC2
4MIC IN+ 2Speaker OUT	MIC5~MIC8	
4MIC IN+ 1Speaker OUT	MIC5~MIC8	
3MIC IN+ 1Speaker OUT	MIC6~MIC8	
2MIC IN+ 1Speaker OUT	MIC7~MIC8	



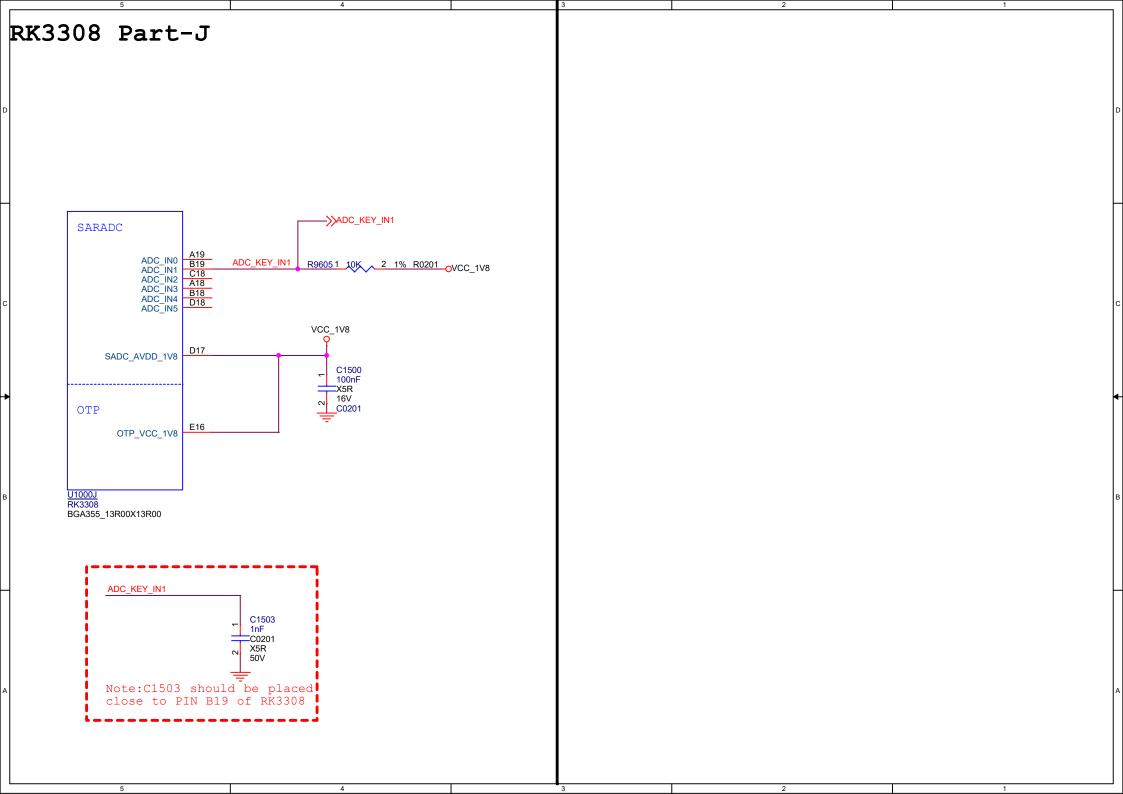




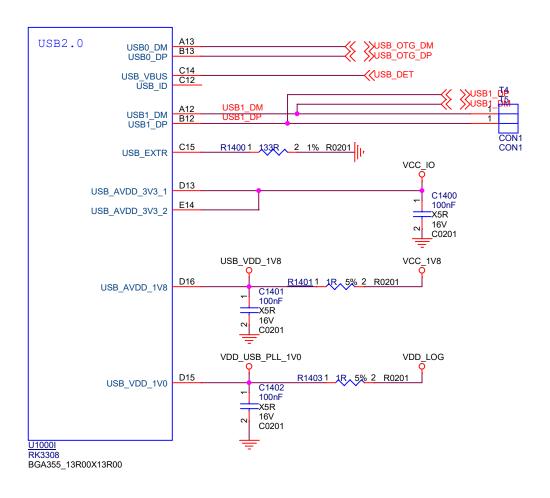
RK3308 Part-C



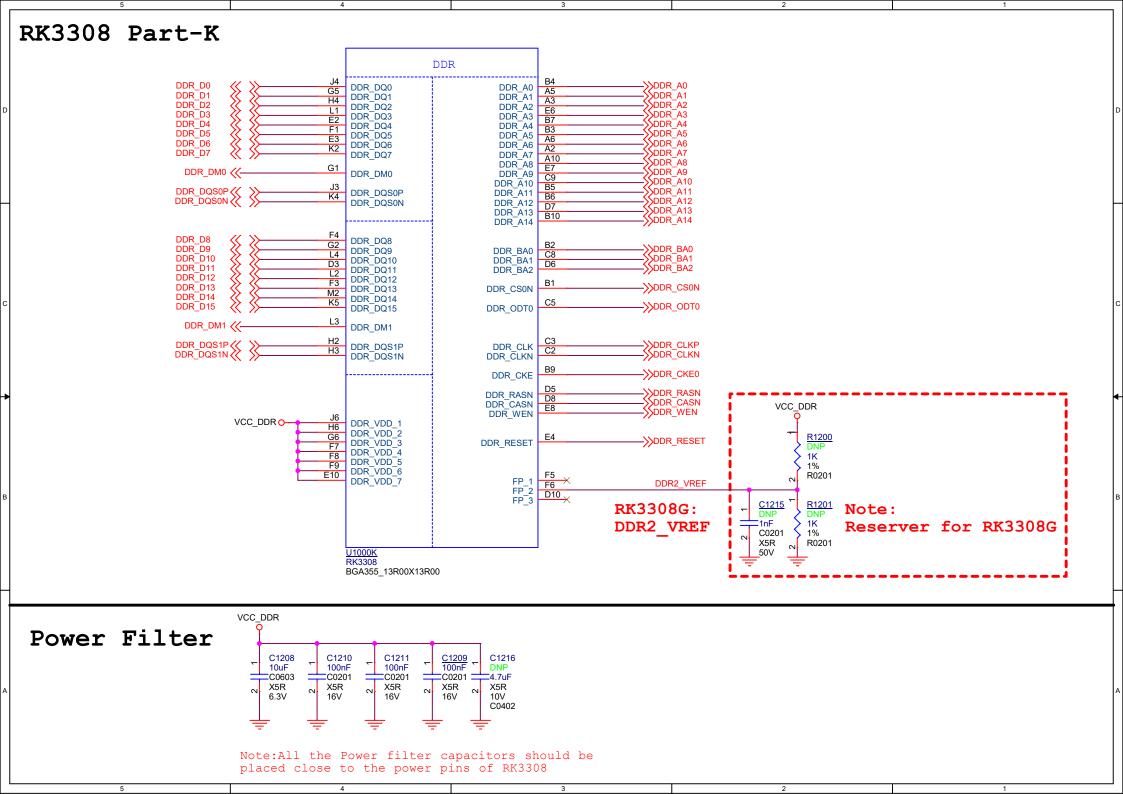
Correspondence between LCDC DATA and RGB/MCU				
LCDC	18bit RGB Panel	24bit RGB Panel	16bit MCU Panel	8bit MCU Panel
LCDC_D0	В0	B2	DB0	DB0
LCDC_D1	B1	В3	DB1	DB1
LCDC_D2	В2	B4	DB2	DB2
LCDC_D3	В3	B5	DB3	DB3
LCDC_D4	В4	В6	DB4	DB4
LCDC_D5	B5	В7	DB5	DB5
LCDC_D6	G0	G2	DB6	DB6
LCDC_D7	G1	G3	DB7	DB7
LCDC_D8	G2	G4	DB8	
LCDC_D9	G3	G5	DB9	
LCDC_D10	G4	G6	DB10	
LCDC_D11	G5	G7	DB11	
LCDC_D12	R0	R2	DB12	
LCDC_D13	R1	R3	DB13	
LCDC_D14	R2	R4	DB14	
LCDC_D15	R3	R5	DB15	
LCDC_D16	R4	R6		
LCDC_D17	R5	R7		
LCDC_CLK	rcdc_crk	rcdc_crk	MCU_CMD (Command)	MCU_CMD (Command)
LCDC_HSYNC	LCDC_HSYNC	LCDC_HSYNC	MCU_WR (write signal)	
LCDC_VSYNC	LCDC_VSYNC	LCDC_VSYNC	MCU_CS (Chip select)	MCU_CS (Chip select)
LCDC_DEN	LCDC_DEN	LCDC_DEN	MCU_RD (Read signal)	MCU_RD (Read signal)



RK3308 Part-I



RK3308 Part-E NAND FLASH/eMMC/SFC/SPI GPIO3_A0/FLASH_D0/EMMC_D0/SFC_SIO0_u V12 GPIO3_A1/FLASH_D1/EMMC_D1/SFC_SIO1_u Y12 GPIO3_A2/FLASH_D2/EMMC_D2/SFC_WP_SIO2_u Y11 GPIO3_A3/FLASH_D3/EMMC_D3/SFC_HOLD_SIO3_u >>FLASH_D0 >>FLASH_D1 >>FLASH_D2 SFLASH_D3 W11 FLASH_D4/SFC_CLK GPIO3_A4/FLASH_D4/EMMC_D4/SFC_CLK_u V11 GPIO3_A5/FLASH_D5/EMMC_D5/SFC_CSN0_u GPIO3_A6/FLASH_D6/EMMC_D6_u GPIO3_A7/FLASH_D7/EMMC_D7_u FLASH_D5/SFC_CS0 V13 GPIO3_B0/FLASH_WRN/EMMC_CMD_u T13 GPIO3_B1/FLASH_CLE/EMMC_CLK_d U13 GPIO3_B2/FLASH_RDN/SPI1_MISO_u R10 GPIO3_B3/FLASH_ALE/EMMC_PWREN/SPI1_CLK_d W12 T12 GPIO3_B4/FLASH_RDY/I2C3_SDA_M1/SPI1_MOSI/UART3_RX_u GPIO3_B5/FLASH_CSN0/I2C3_SCL_M1/SPI1_CSN0/UART3_TX_u VCC IO R12 VCCIO3 C1300 U1000E 100nF **RK3308** X5R BGA355 13R00X13R00 16V C0201



RK3308 Part-L RK3308 Part-N RK3308 Part-M VDD_CORE Trace>60mil(Max 1.5A) ARM CORE_VDD_1 N7 CORE VDD 2 N8 CORE VDD 3 CORE_VDD_4 P6 CORE VDD 5 CORE_VDD_6 CORE VDD 7 R6 CORE VDD 8 CORE_VDD_9 VSS_26 F15 VSS 1 VSS 51 VSS 79 VDD LOG Trace>30mil(Max 0.5A) K11 VSS_27 VSS_80 VSS_2 VSS_52 F16 K12 VSS³ VSS²⁸ VSS_53 VSS_81 B8 N10 G8 K13 Logic VSS 4 VSS 29 VSS 54 VSS 82 F10 B11 G9 K14 N11 LOGIC_VDD_1 LOGIC_VDD_2 LOGIC_VDD_3 VSS_5 VSS_30 VSS 55 VSS_83 F11 G13 C4 VSS_31 G14 K15 VSS_84 N13 VSS_6 VSS_7 VSS_56 K16 F12 C6 VSS_32 E18 VSS_85 N14 VSS_57 G10 C7 K17 LOGIC_VDD_4 VSS_8 VSS_33 VSS_58 VSS_86 C11 H7 L7 LOGIC VDD 5 VSS⁹ VSS³⁴ VSS 59 VSS⁸⁷ D2 Н8 L8 LOGIC_VDD_6 VSS_35 VSS_60 VSS_10 VSS_88 E1 L9 VSS_36 H10 VSS_89 P10 VSS¹¹ VSS⁶¹ E5 L10 VSS 12 VSS 37 VSS 62 VSS 90 E9 H11 L11 U1000L VSS 13 VSS_38 VSS 63 VSS_91 **RK3308** L12 VSS_39 VSS_40 VSS_92 VSS¹⁴ VSS 64 BGA355 13R00X13R00 G3 H13 L13 P13 VSS 15 VSS 65 VSS 93 J5 K1 J7 L14 VSS_41 J8 VSS¹⁶ VSS_66 VSS 94 VDD CORE L15 VSS_95 A20 VSS_42 J9 VSS_17 VSS 67 K3 L16 VSS_18 VSS_43 VSS_68 VSS_96 M4 VSS 19 VSS 44 VSS⁶⁹ VSS 97 D11 M5 VSS_20 VSS 45 VSS 70 VSS 98 C1000 C1002 C1003 C1004 C1001 D12 J12 M6 D20 VSS_46 J13 VSS²¹ VSS 71 VSS_99 22uF 100nF 100nF 100nF DNP E11 M7 VSS_47 K7 VSS_22 VSS 72 VSS_100 E12 M8 X5R X5R X5R X5R 4.7uF VSS_48 K8 VSS_101 R7 VSS₂₃ VSS 73 E13 M9 6.3V 16V 16V 16V X5R VSS_49 VSS_74 VSS_102 VSS 24 C0603 C0201 C0201 C0201 10V F13 M10 VSS_75 VSS_103 T7 VSS 25 VSS_50 C0402 M11 VSS_76 VSS_104 U17 M12 VSS_77 VSS_105 Y1 M13 VSS_78 VSS_106 VDD_LOG U1000M **RK3308** U1000N RK3308 BGA355_13R00X13R00 BGA355 13R00X13R00 C1006 C1007 C1008 C1009 10uF 100nF 100nF DNP 4.7uF X5R X5R X5R X5R 16V 16V 6.3V 2 C0603 C0201 C0201 10V C0402 Note: All the Power filter capacitors should be placed close to the power pins of RK3308

