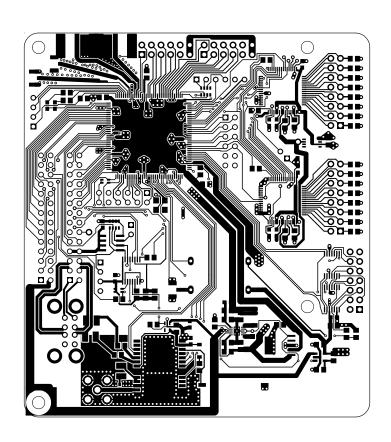
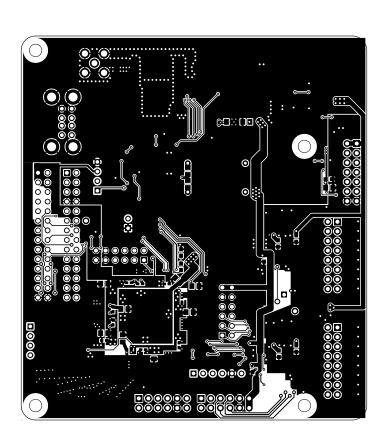
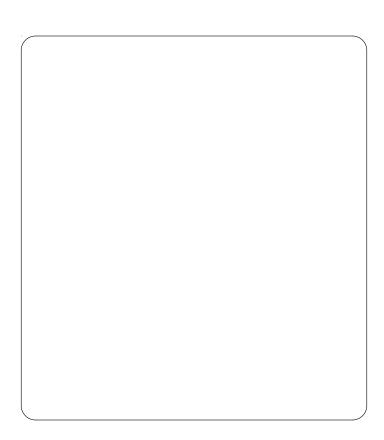
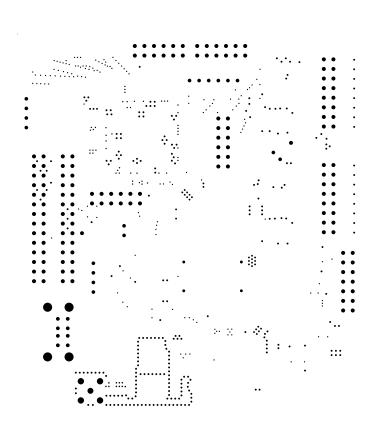
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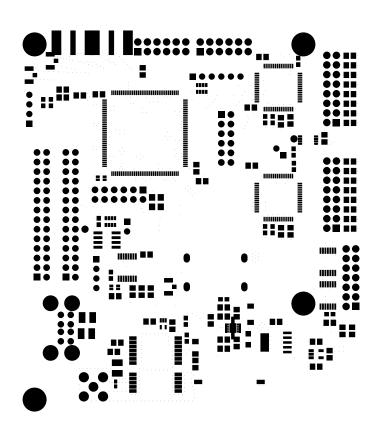


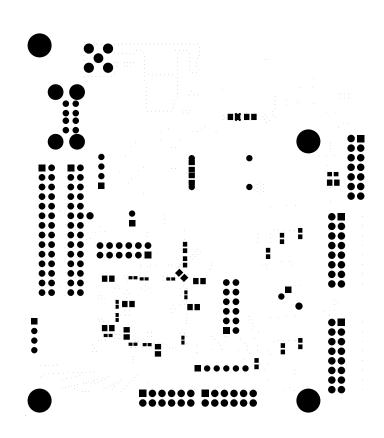


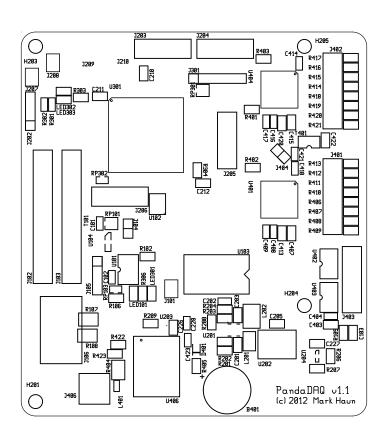


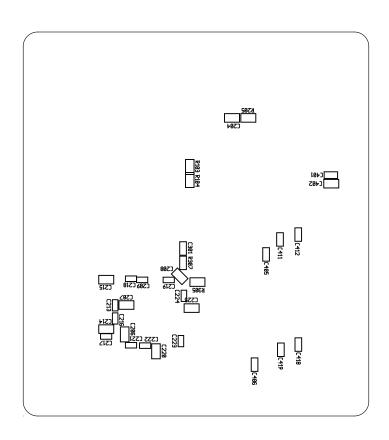


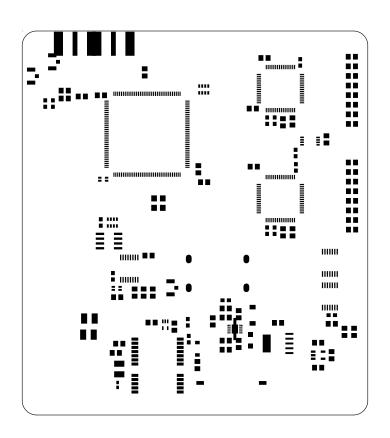
unplated-drill, scale = 1:1.000 pandadaq.pcb

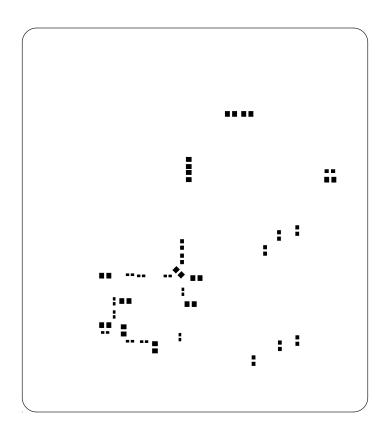


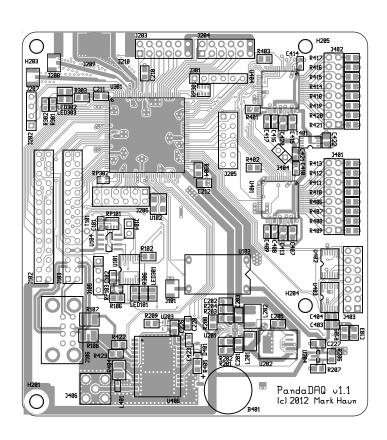


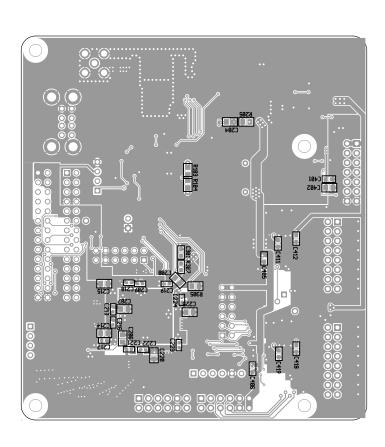






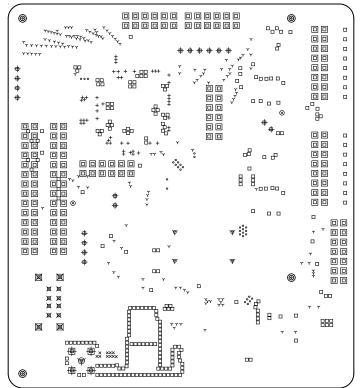






There are 14 different drill sizes used in this layout, 709 holes total

Symbol	Diam. (Inch)	Count	Plated?	
Y	0.013	150	YES	
+	0.014	49	YES	
×	0.016	10	YES	
۰	0.019	91	YES	
	0.020	274	YES	
φ.	0.030	4	YES	
#	0.038	18	YES	
×	0.039	8	YES	
0	0.041	2	YES	
	0.042	150	YES	
\$	0.062	1	YES	Title: (unknown) - Fabrication Drawing
<del></del>	0.066	4	YES	Author: Mark Haun
	0.094	4	YES	Date: Tue 10 Apr 2012 05:19:46 AM GMT UTC
<b>©</b>	0.130	4	NO	



Board outline is the centerline of this path