Memory Circuit Design Homeword #2

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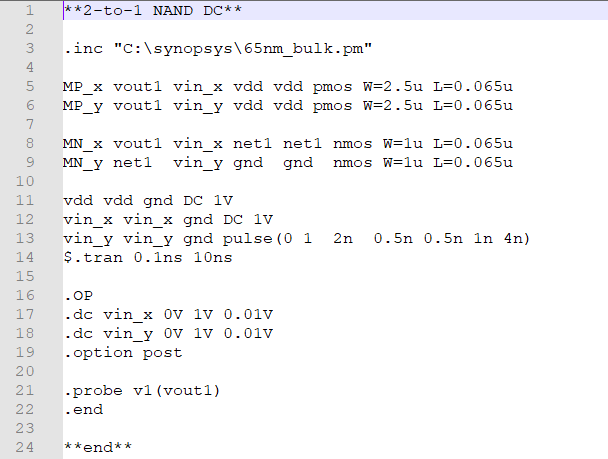
1. Functionality of the Basic Gate
2. NAND

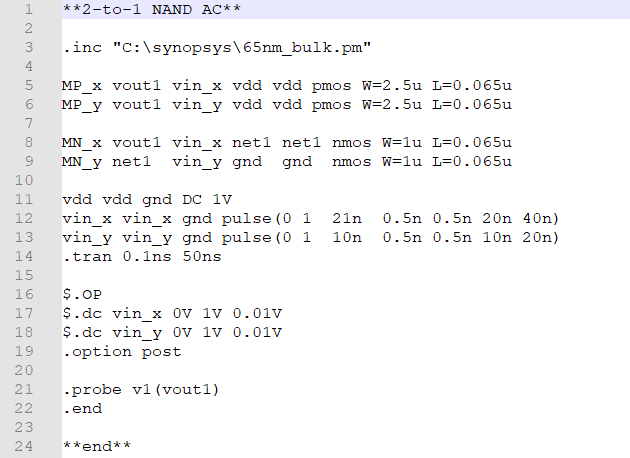
Ac:



Dc:





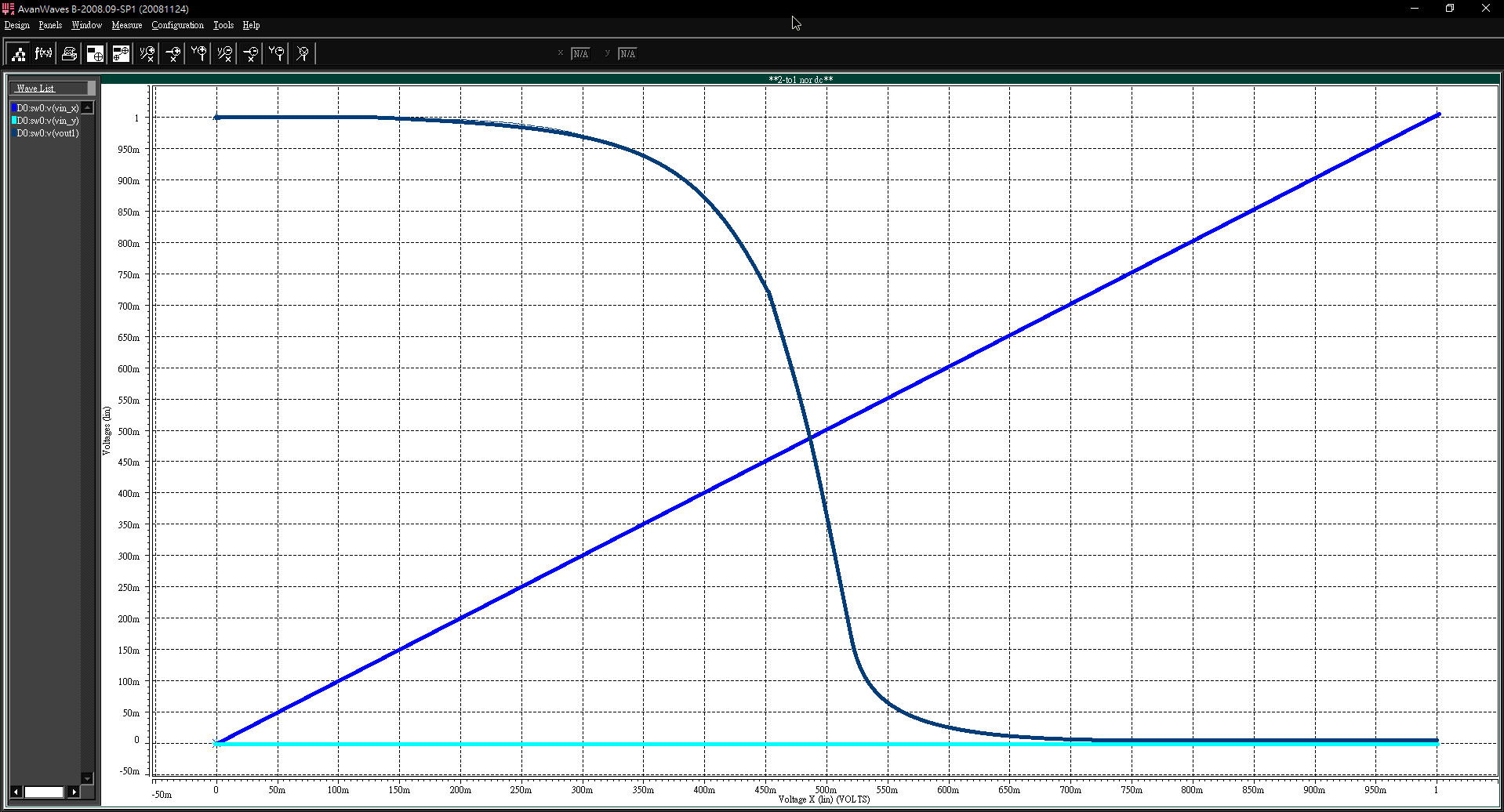


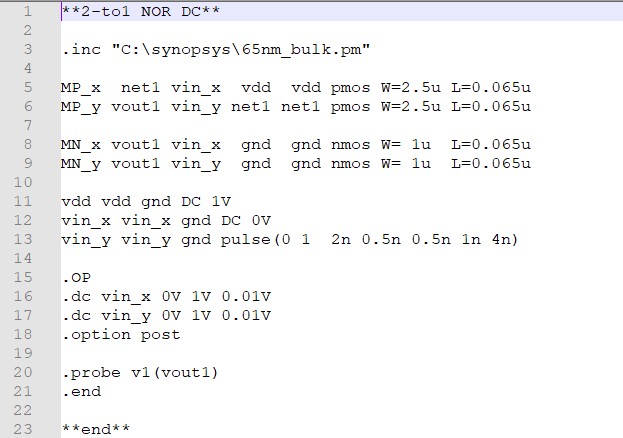
1. NOR

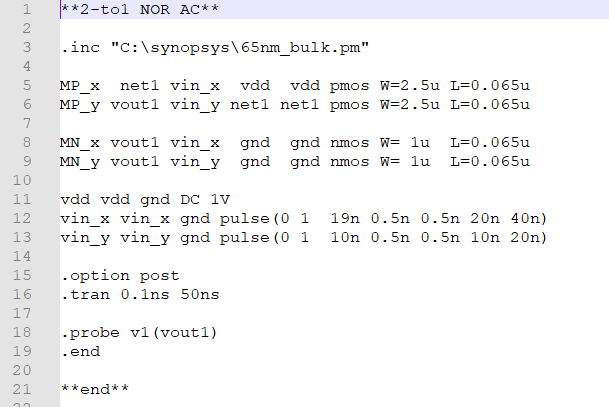
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Dc:

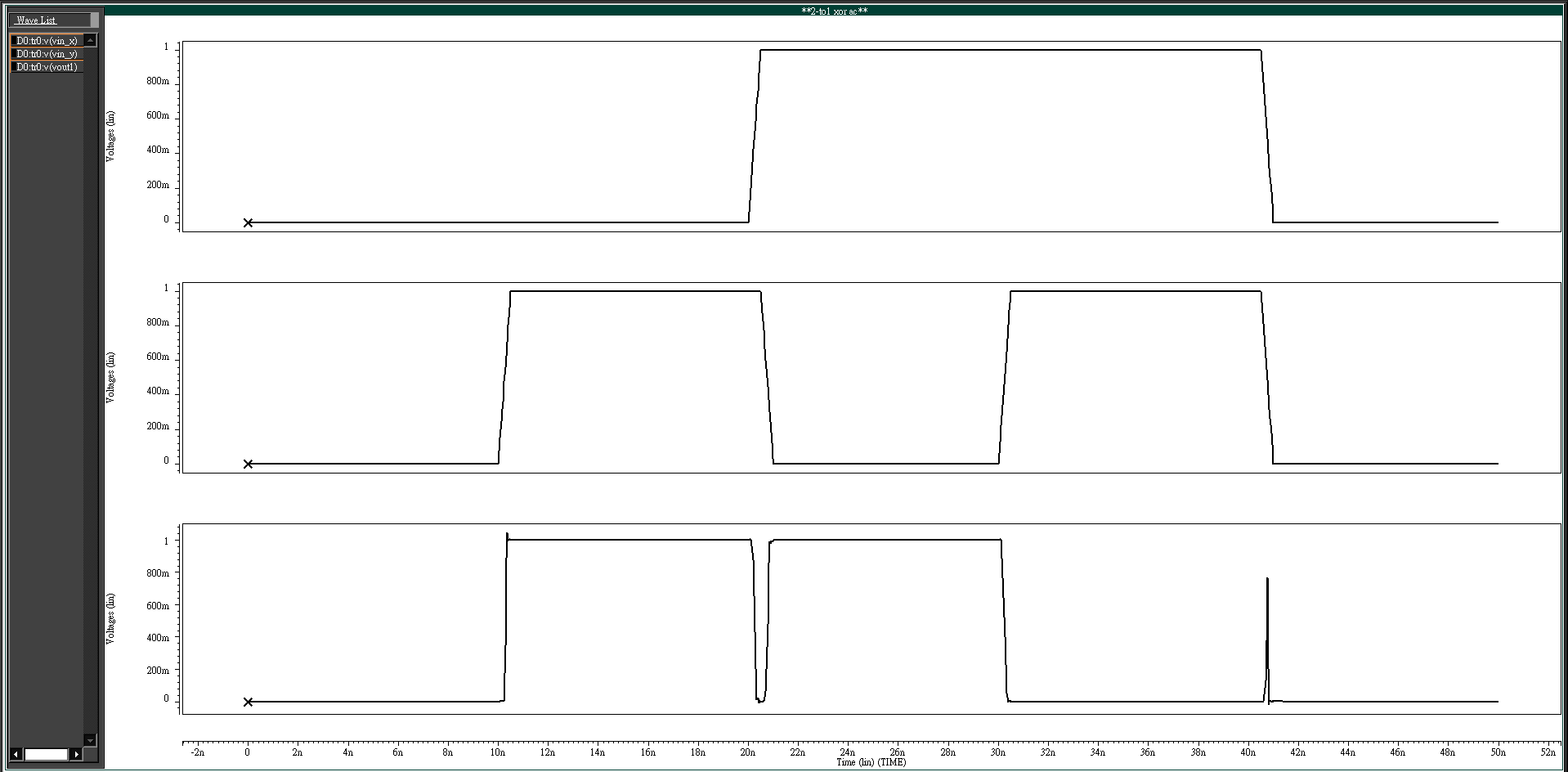






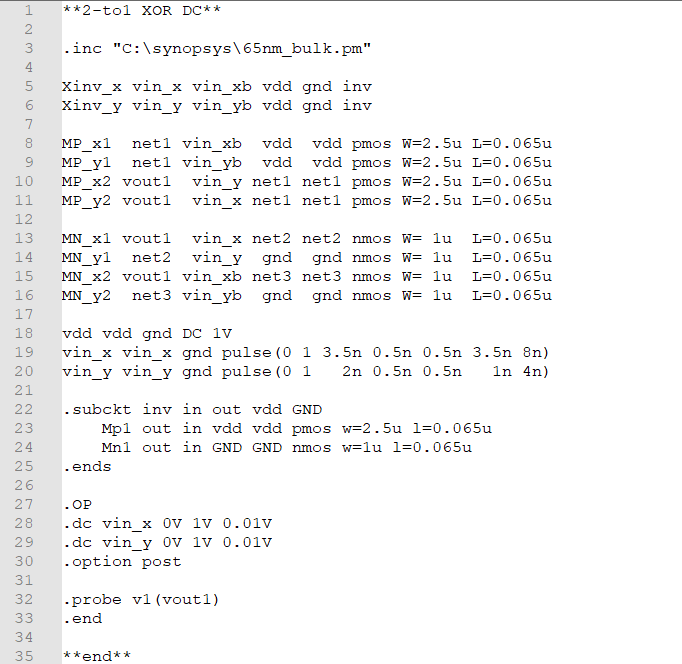
1. XOR

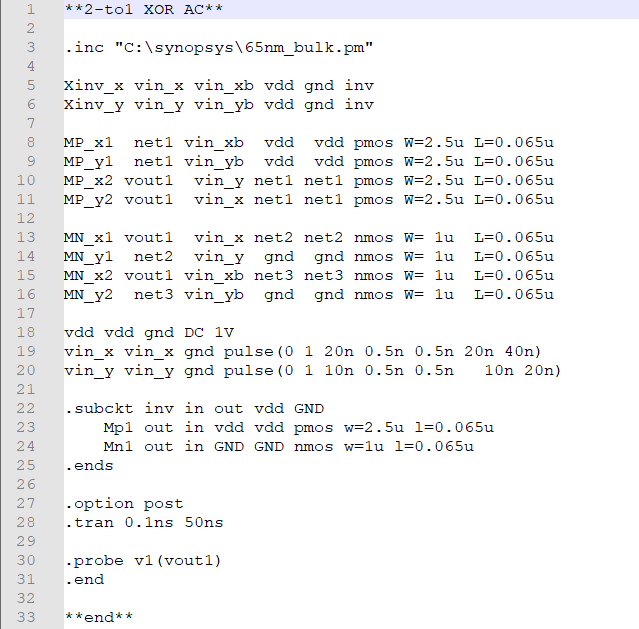
Ac:



Dc:

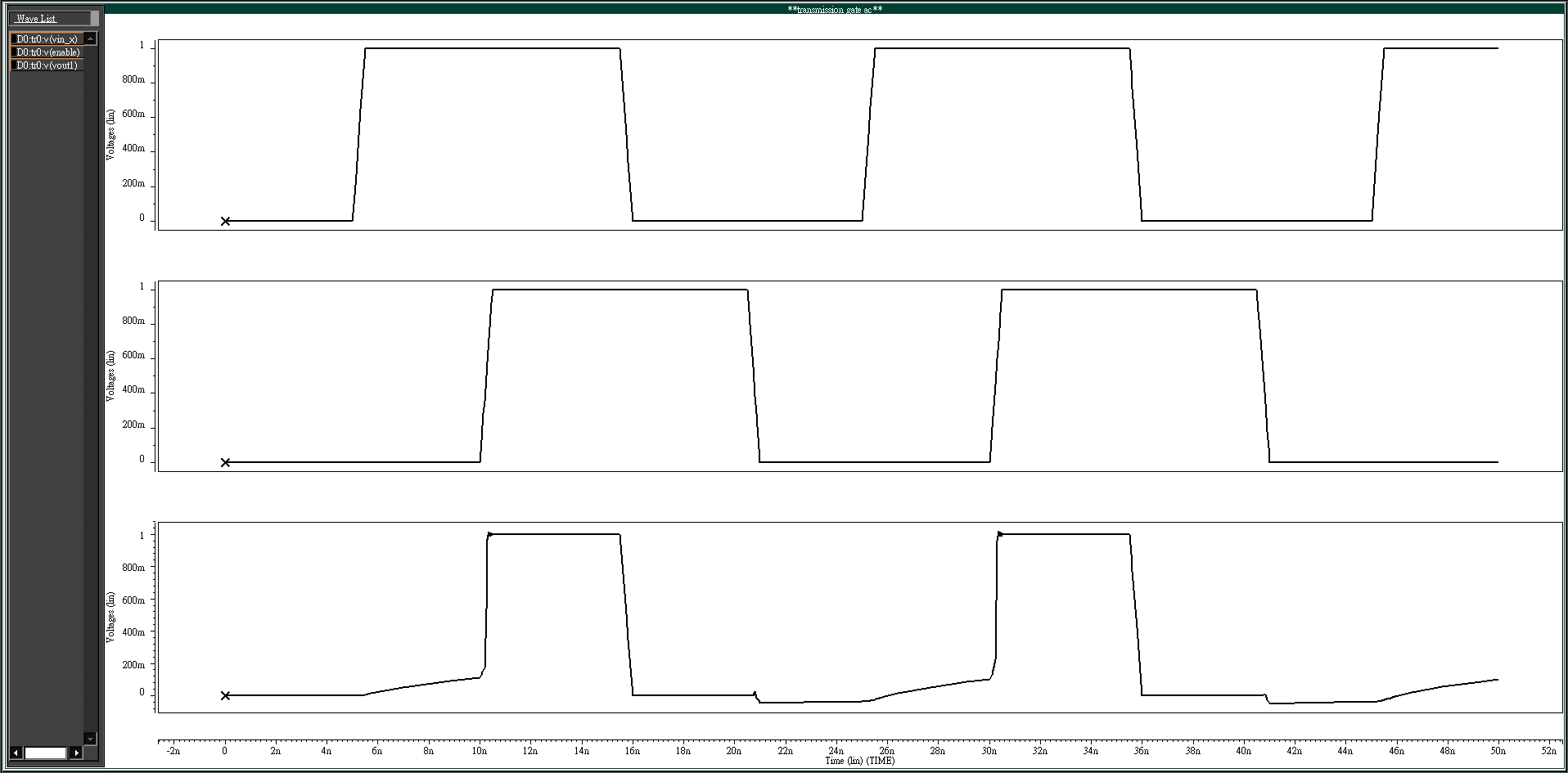




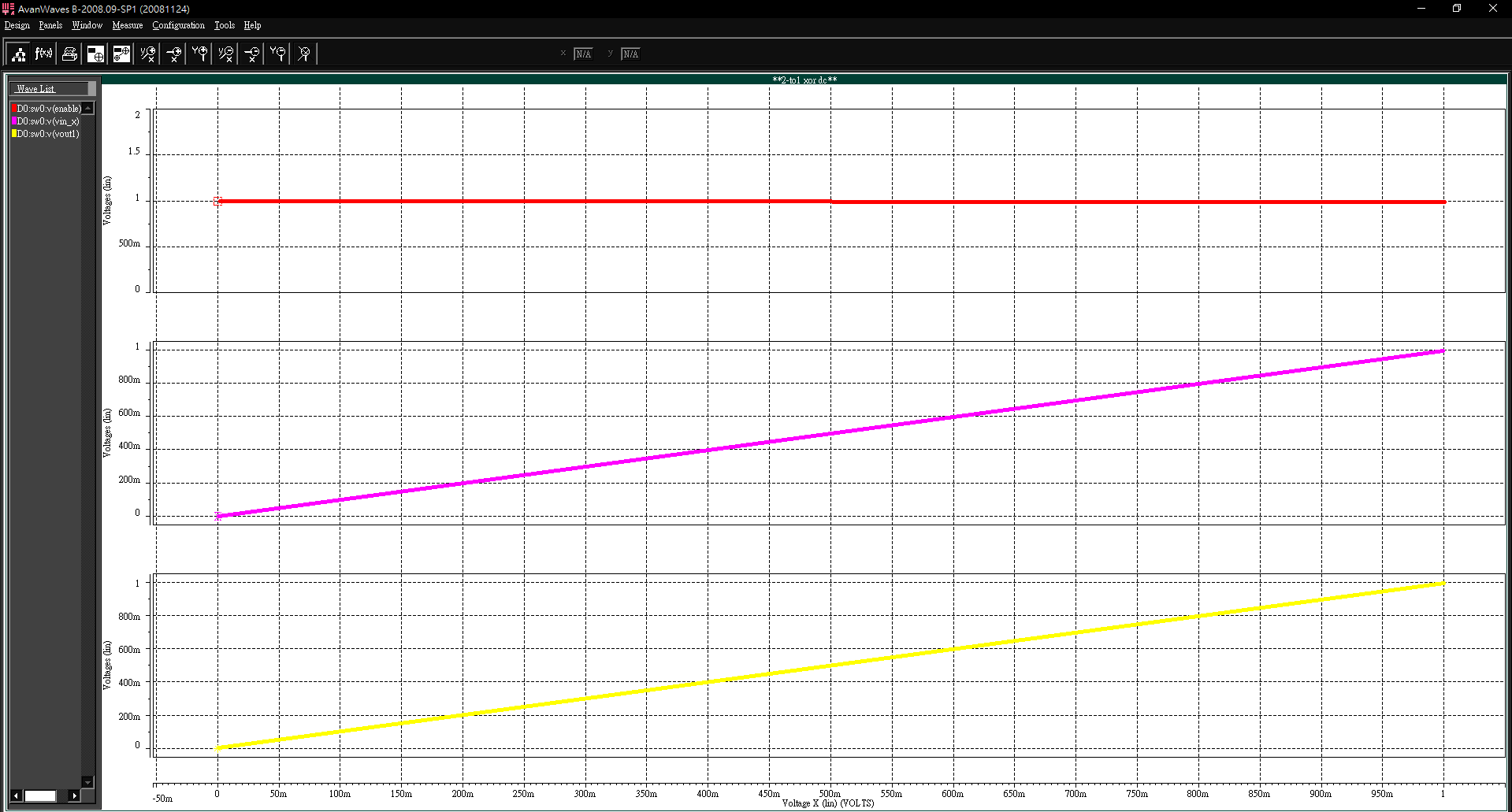


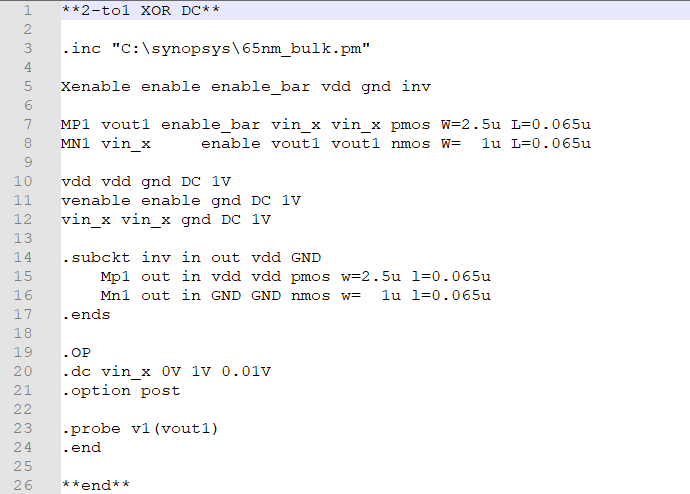
1. Functionality of the Transmission Gate

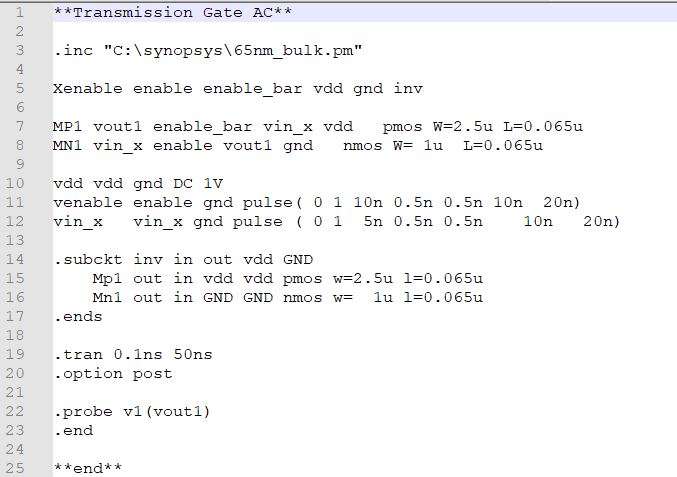
Ac:



Dc:

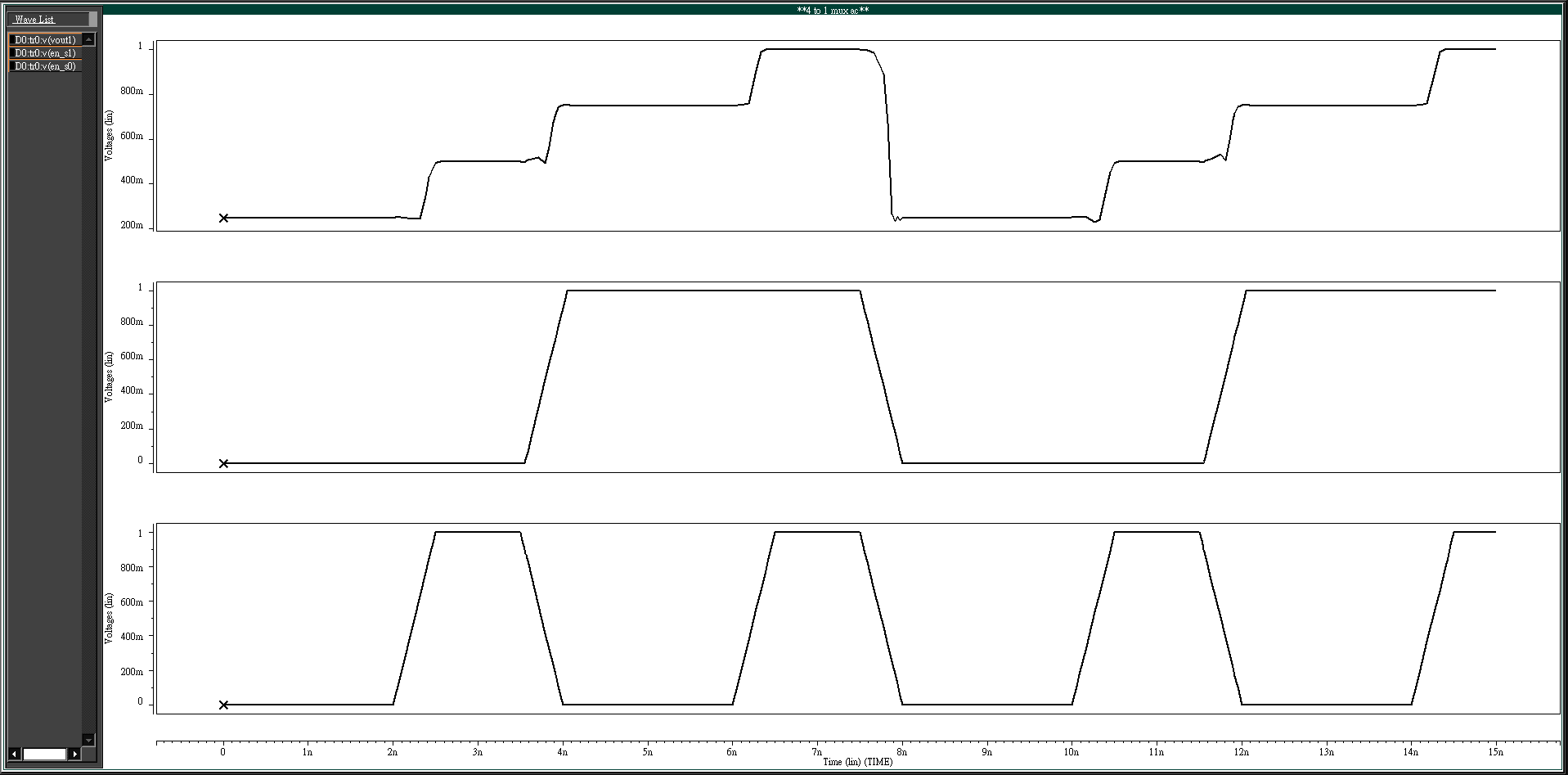


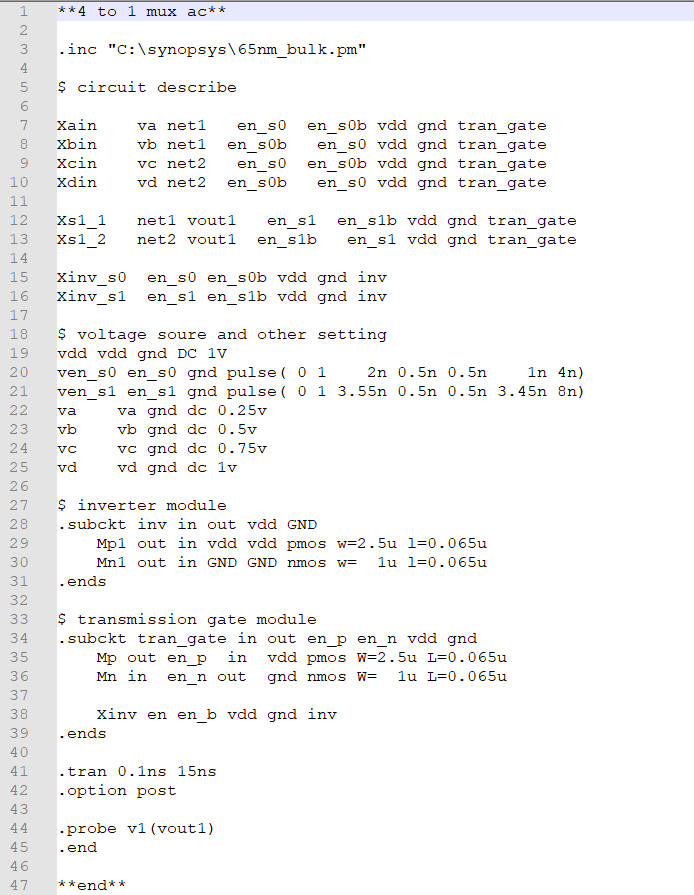




1. Functionality of the Basic Gate (4-to-1 Mux)

Ac:





1. Functionality of the Decoder (3-to-8 decoder)

Ac:

